# XN06113 (XN6113)

### Silicon PNP epitaxial planer transistor

#### For switching/digital circuits

#### Features

- Two elements incorporated into one package. (Transistors with built-in resistor)
- Reduction of the mounting area and assembly cost by one half.

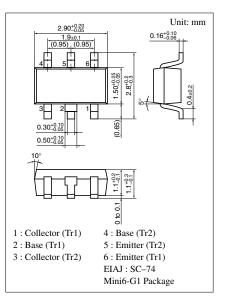
#### Basic Part Number of Element

• UNR1113(UN1113)  $\times$  2 elements

| Parameter               |                              | Symbol           | Ratings     | Unit |  |
|-------------------------|------------------------------|------------------|-------------|------|--|
| Rating<br>of<br>element | Collector to base voltage    | V <sub>CBO</sub> | -50         | V    |  |
|                         | Collector to emitter voltage | V <sub>CEO</sub> | -50         | V    |  |
|                         | Collector current            | I <sub>C</sub>   | -100        | mA   |  |
| Overall                 | Total power dissipation      | P <sub>T</sub>   | 300         | mW   |  |
|                         | Junction temperature         | Tj               | 150         | °C   |  |
|                         | Storage temperature          | T <sub>stg</sub> | -55 to +150 | °C   |  |

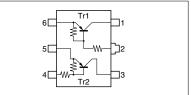
#### Absolute Maximum Ratings (Ta=25°C)

Electrical Characteristics (Ta=25°C)



#### Marking Symbol: 6W

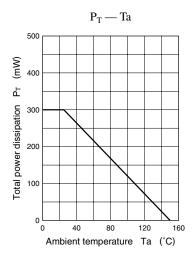
#### Internal Connection

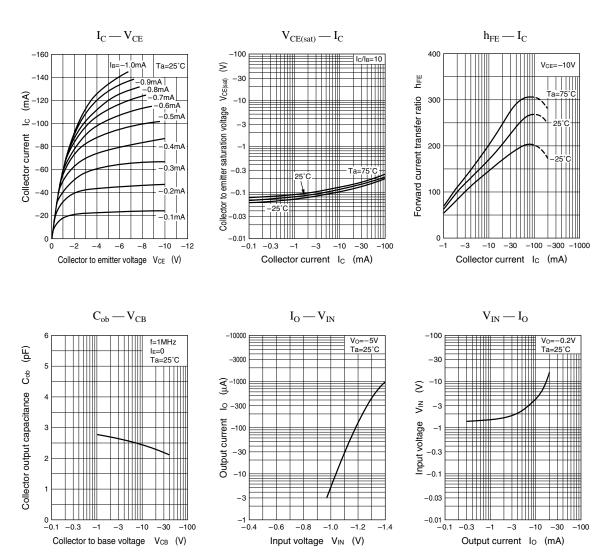


| Parameter                                      | Symbol                          | Conditions  | min  | typ  | max    | Unit |
|--|---------------------------------|---|------|------|--------|------|
| Collector to base voltage                      | V <sub>CBO</sub>                | $I_{\rm C} = -10 \mu A, I_{\rm E} = 0$                | -50  |      |        | V    |
| Collector to emitter voltage                   | V <sub>CEO</sub>                | $I_{\rm C} = -2mA, I_{\rm B} = 0$                     | -50  |      |        | v    |
| Callester artaff annuat                        | I <sub>CBO</sub>                | $V_{CB} = -50V, I_E = 0$                              |      |      | - 0.1  | μΑ   |
| Collector cutoff current                       | I <sub>CEO</sub>                | $V_{CE} = -50V, I_B = 0$                              |      |      | - 0.5  | μΑ   |
| Emitter cutoff current                         | I <sub>EBO</sub>                | $V_{EB} = -6V, I_C = 0$                               |      |      | - 0.1  | mA   |
| Forward current transfer ratio                 | h <sub>FE</sub>                 | $V_{CE} = -10V, I_C = -5mA$                           | 80   |      |        |      |
| Forward current transfer h <sub>FE</sub> ratio | h <sub>FE</sub> (small/large)*1 | $V_{CE} = -10V, I_C = -5mA$                           | 0.5  | 0.99 |        |      |
| Collector to emitter saturation voltage        | V <sub>CE(sat)</sub>            | $I_{\rm C} = -10 {\rm mA}, I_{\rm B} = -0.3 {\rm mA}$ |      |      | - 0.25 | V    |
| Output voltage high level                      | V <sub>OH</sub>                 | $V_{CC} = -5V, V_B = -0.5V, R_L = 1k\Omega$           | -4.9 |      |        | V    |
| Output voltage low level                       | V <sub>OL</sub>                 | $V_{CC} = -5V, V_B = -3.5V, R_L = 1k\Omega$           |      |      | - 0.2  | v    |
| Transition frequency                           | f <sub>T</sub>                  | $V_{CB} = -10V, I_E = 1mA, f = 200MHz$                |      | 80   |        | MHz  |
| Input resistance                               | R <sub>1</sub>                  |   | -30% | 47   | +30%   | kΩ   |
| Resistance ratio                               | R <sub>1</sub> /R <sub>2</sub>  |   | 0.8  | 1.0  | 1.2    |      |

<sup>\*1</sup> Ratio between 2 elements

Note) The Part number in the Parenthesis shows conventional part number.





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