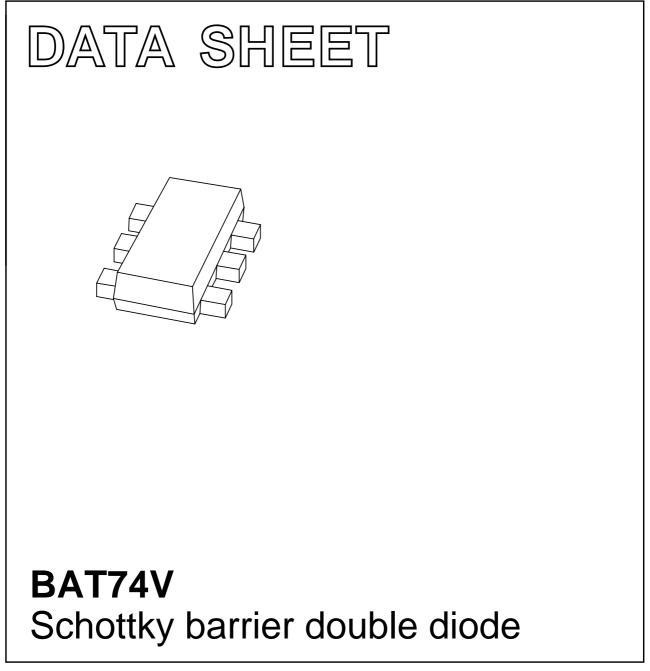
DISCRETE SEMICONDUCTORS



Product specification

2002 Sep 02



HILIP

FEATURES

- Low forward voltage
- Low capacitance
- Ultra small SMD plastic package
- Flat leads: excellent coplanarity and improved thermal behaviour.

APPLICATIONS

- Ultra high-speed switching
- Voltage clamping
- Line termination
- Inverse polarity protection.

DESCRIPTION

Planar Schottky barrier double diode with an integrated guard ring for stress protection.

Two separate dies encapsulated in a SOT666 ultra small SMD plastic package.

PINNING

PIN	DESCRIPTION	
1	anode 1	
2	not connected	
3	cathode 2	
4	anode 2	
5	not connected	
6	cathode 1	

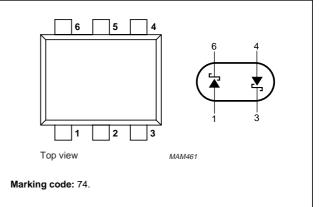


Fig.1 Simplified outline (SOT666) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _R	continuous reverse voltage		_	30	V
I _F	continuous forward current		-	200	mA
I _{FRM}	repetitive peak forward current	$t_p \le 1 \text{ s}; \delta \le 0.5$	_	300	mA
I _{FSM}	non-repetitive peak forward current	t _p < 10 ms		600	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	_	230	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	125	°C
T _{amb}	operating ambient temperature		-65	+125	°C

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CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MAX.	UNIT
V _F	continuous forward voltage	I _F = 0.1 mA	240	mV
		I _F = 1 mA	320	mV
		I _F = 10 mA	400	mV
		I _F = 30 mA	500	mV
		I _F = 100 mA; note 1; see Fig.2	800	mV
I _R	reverse current	V _R = 25 V; note 1; see Fig.3	2	μA
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; see Fig.4	10	pF

Note

1. Pulse test: $t_p = 300 \ \mu s$; $\delta = 0.02$.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	416	K/W

Note

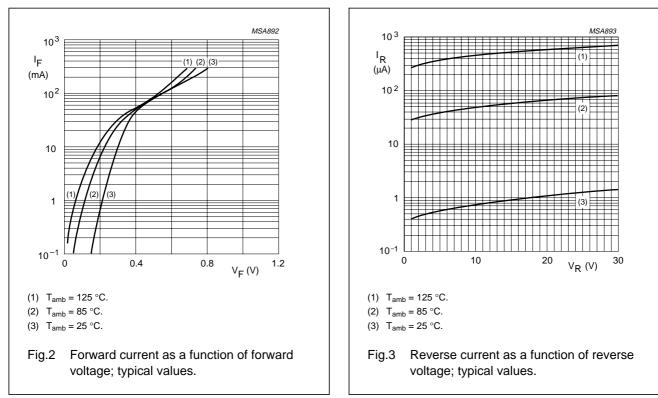
1. Refer to SOT666 standard mounting conditions.

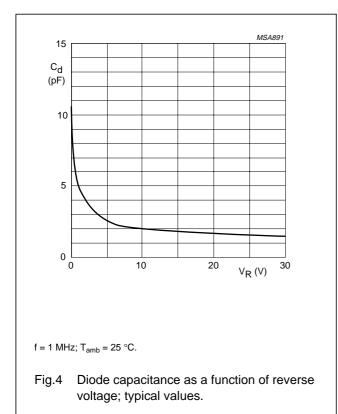
Soldering

The only recommended soldering method is reflow soldering.

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GRAPHICAL DATA

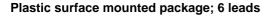


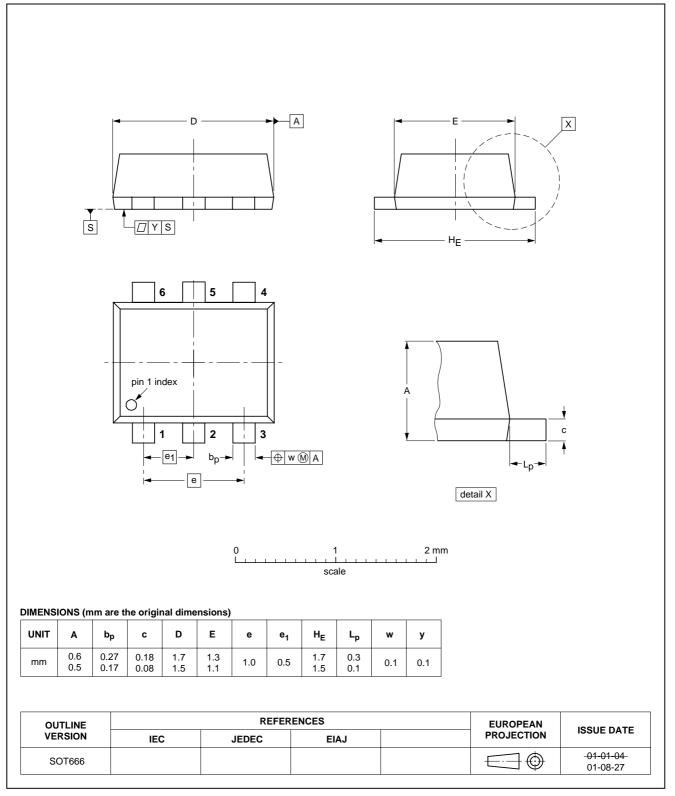


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Schottky barrier double diode

PACKAGE OUTLINE





SOT666

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DATA SHEET STATUS

DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

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