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2SK2373

Silicon N-Channel MOS FET



ADE-208-268 (Z) 1st. Edition Mar. 2001

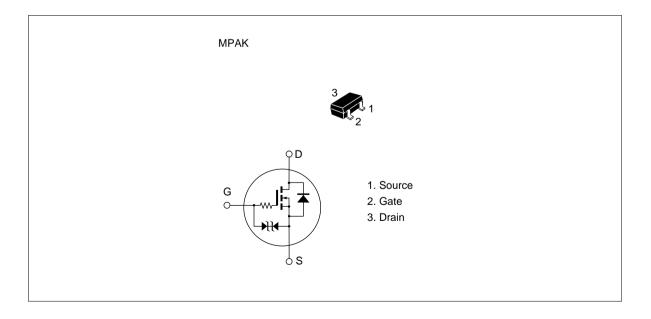
Application

Low frequency power switching

Features

- Low on-resistance
- Small package
- Low drive current
- 4 V gate drive device can be driven from 5 V source.
- Suitable for low signal load switch

Outline



2SK2373

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{\scriptscriptstyle DSS}$	30	V
Gate to source voltage	$V_{\rm GSS}$	±20	V
Drain current	I _D	0.2	Α
Drain peak current	I _{D(pulse)} *1	0.4	А
Body to drain diode reverse drain current	I _{DR}	0.2	Α
Channel dissipation	Pch*2	150	mW
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

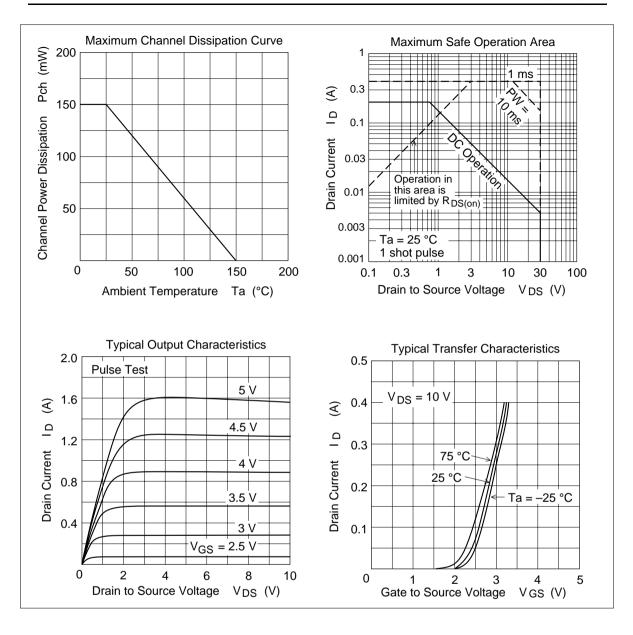
Notes 1. PW 100 µs, duty cycle 10 %

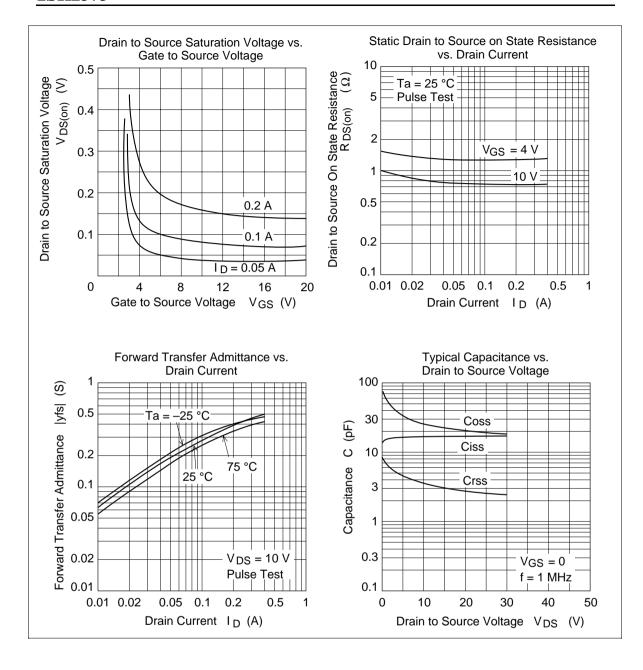
2. Marking is "ZE-".

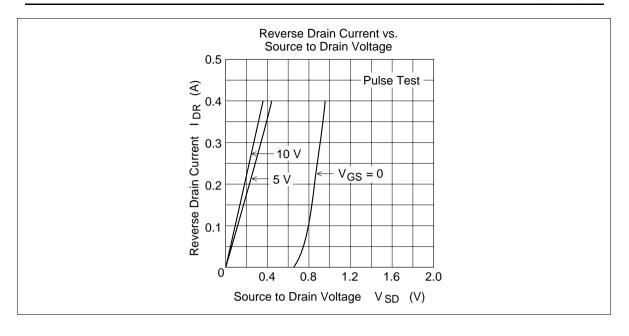
Electrical Characteristics ($Ta = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 100 \ \mu A, \ V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±2	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.0	V	$I_{D} = 10 \mu A, V_{DS} = 5 V$
Static drain to source on state resistance	$R_{DS(on)}$	_	1.4	2.5		$I_D = 20 \text{ mA}$ $V_{GS} = 4 \text{ V}^{*1}$
		_	1.0	1.4		$I_D = 10 \text{ mA}$ $V_{GS} = 10 \text{ V}^{*1}$
Input capacitance	Ciss	_	17.8	_	pF	$V_{DS} = 10 \text{ V}$
Output capacitance	Coss	_	25.4	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	3.7	_	pF	f = 1 MHz
Turn-on delay time	$\mathbf{t}_{\text{d(on)}}$	_	50	_	ns	I _D = 0.1 A
Rise time	t _r	_	125	_	ns	V _{GS} = 10 V
Turn-off delay time	$t_{\text{d(off)}}$	_	660	_	ns	R _L = 100
Fall time	t _f	_	400	_	ns	PW = 2 μs

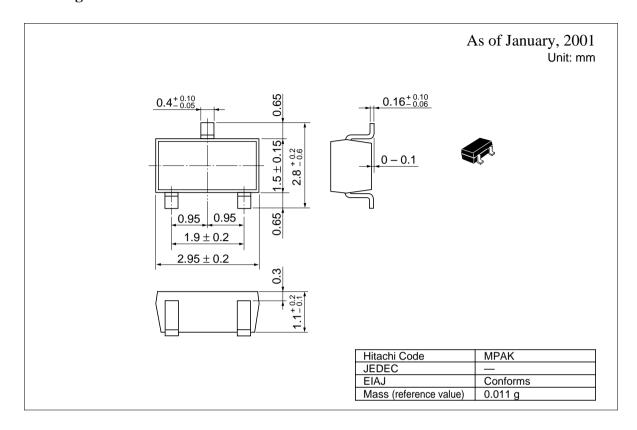
Note 1. Pulse Test







Package Dimensions



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