

Advance Information
**Surface Mount
Schottky Power Rectifier**

SMA Power Surface Mount Package

... employing the Schottky Barrier principle in a metal-to-silicon power rectifier. Features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency switching power supplies; free wheeling diodes and polarity protection diodes.

- Compact Package with J-Bend Leads Ideal for Automated Handling
- Highly Stable Oxide Passivated Junction
- Guardring for Over-Voltage Protection
- Low Forward Voltage Drop

Mechanical Characteristics:

- Case: Molded Epoxy
- Epoxy Meets UL94, V_0 at 1/8"
- Weight: 70 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Polarity: Notch in Plastic Body Indicates Cathode Lead
- Available in 12 mm Tape, 5000 Units per 13 inch Reel, Add "T3" Suffix to Part Number
- Marking: B3

MBRA130LT3

**SCHOTTKY BARRIER
RECTIFIER
1 AMPERES
30 VOLTS**



**CASE 403B-01
SMA**

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	30	V
Average Rectified Forward Current (At Rated V_R , $T_C = 105^\circ\text{C}$)	I_O	1.0	A
Peak Repetitive Forward Current (At Rated V_R , Square Wave, 100 kHz, $T_C = 105^\circ\text{C}$)	I_{FRM}	2.0	A
Non-Repetitive Peak Surge Current (Surge applied at rated load conditions, halfwave, single phase, 60 Hz)	I_{FSM}	25	A
Storage/Operating Case Temperature	T_{stg}, T_C	-55 to +150	$^\circ\text{C}$
Operating Junction Temperature	T_J	-55 to +125	$^\circ\text{C}$
Voltage Rate of Change (Rated V_R , $T_J = 25^\circ\text{C}$)	dv/dt	10,000	$\text{V}/\mu\text{s}$

THERMAL CHARACTERISTICS

Thermal Resistance — Junction-to-Lead (2)	R_{tjl}	35	$^\circ\text{C}/\text{W}$
Thermal Resistance — Junction-to-Ambient (2)	R_{tja}	86	

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (1), see Figure 2 ($I_F = 1.0 \text{ A}$) ($I_F = 2.0 \text{ A}$)	V_F	$T_J = 25^\circ\text{C}$	$T_J = 100^\circ\text{C}$	V
		0.41 0.47	0.35 0.43	
Maximum Instantaneous Reverse Current, see Figure 4 ($V_R = 30 \text{ V}$) ($V_R = 15 \text{ V}$)	I_R	$T_J = 25^\circ\text{C}$	$T_J = 100^\circ\text{C}$	mA
		1.0 0.4	25 12	

This document contains advance information on a new product. Specifications and information herein are subject to change without notice.

(1) Pulse Test: Pulse Width $\leq 250 \mu\text{s}$, Duty Cycle $\leq 2\%$.

(2) Mounted with minimum recommended pad size (2 mm * 2 mm), PC Board FR4, see Figure 8.

REV 2

MBRA130LT3

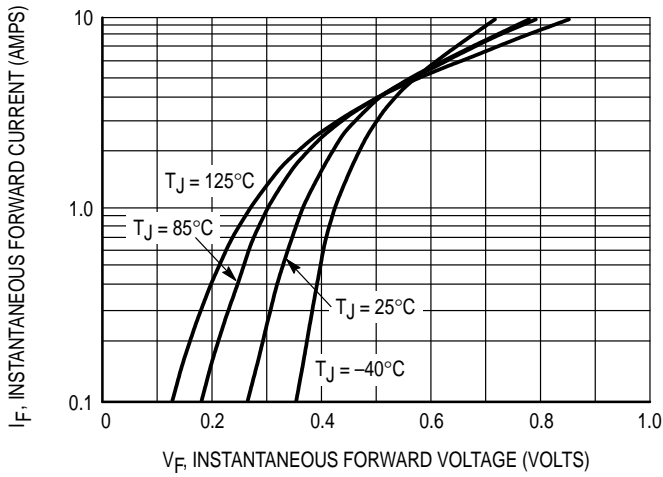


Figure 1. Typical Forward Voltage

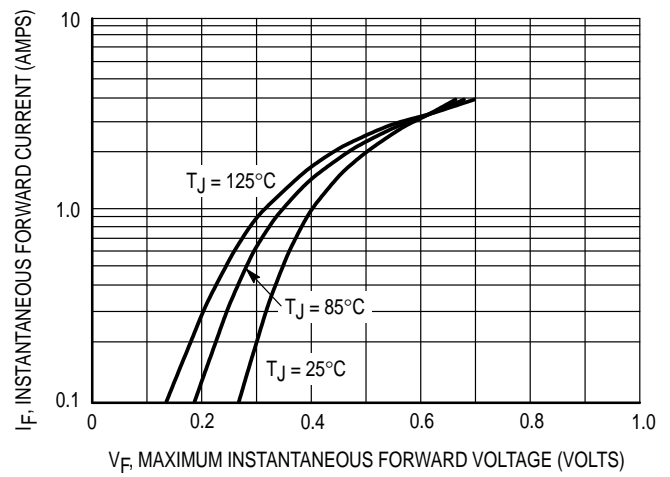


Figure 2. Maximum Forward Voltage

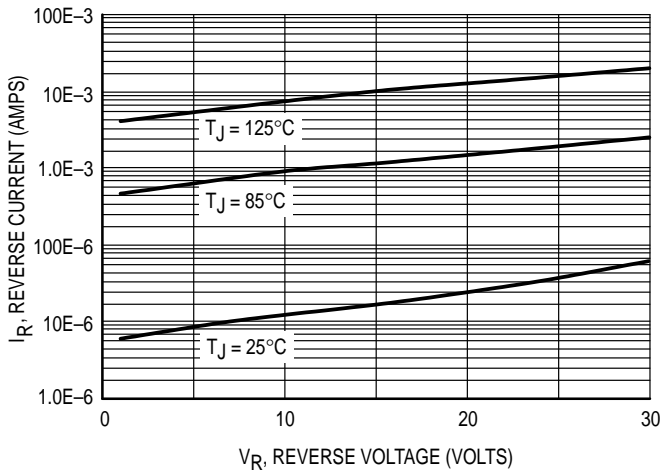


Figure 3. Typical Reverse Current

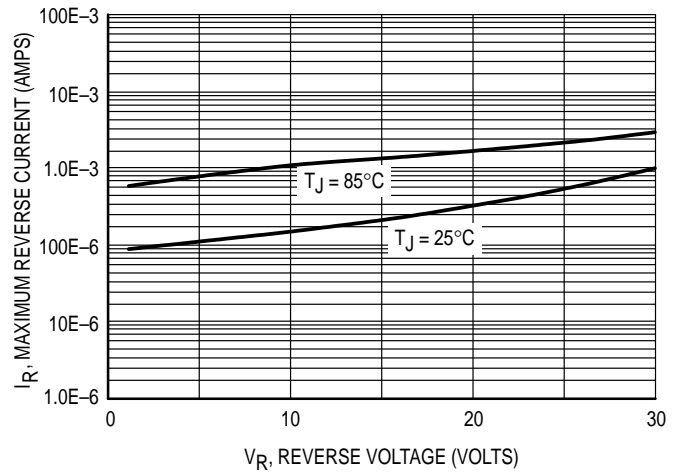


Figure 4. Maximum Reverse Current

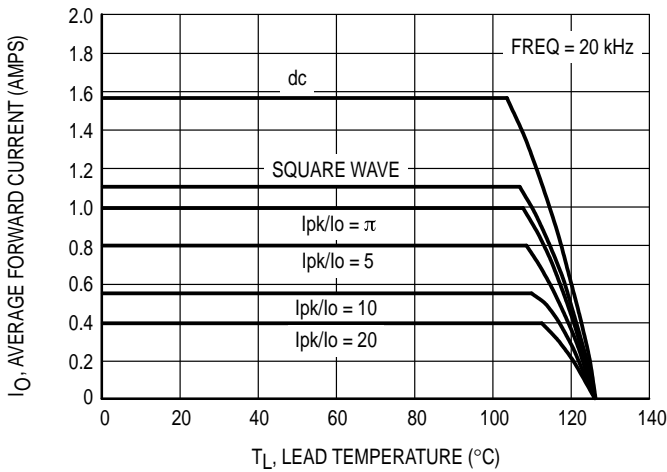


Figure 5. Current Derating

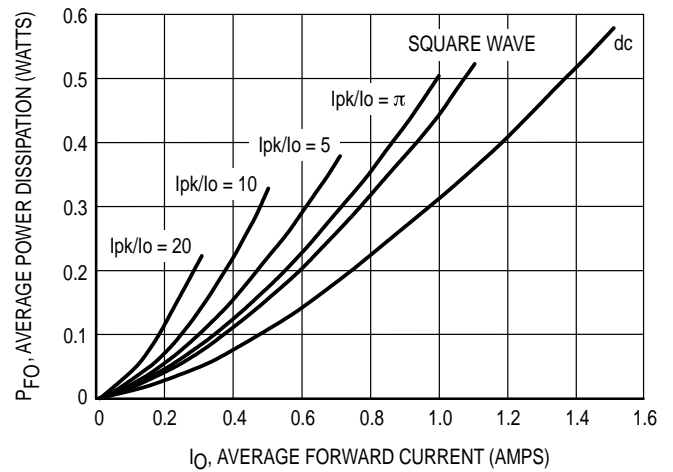


Figure 6. Forward Power Dissipation

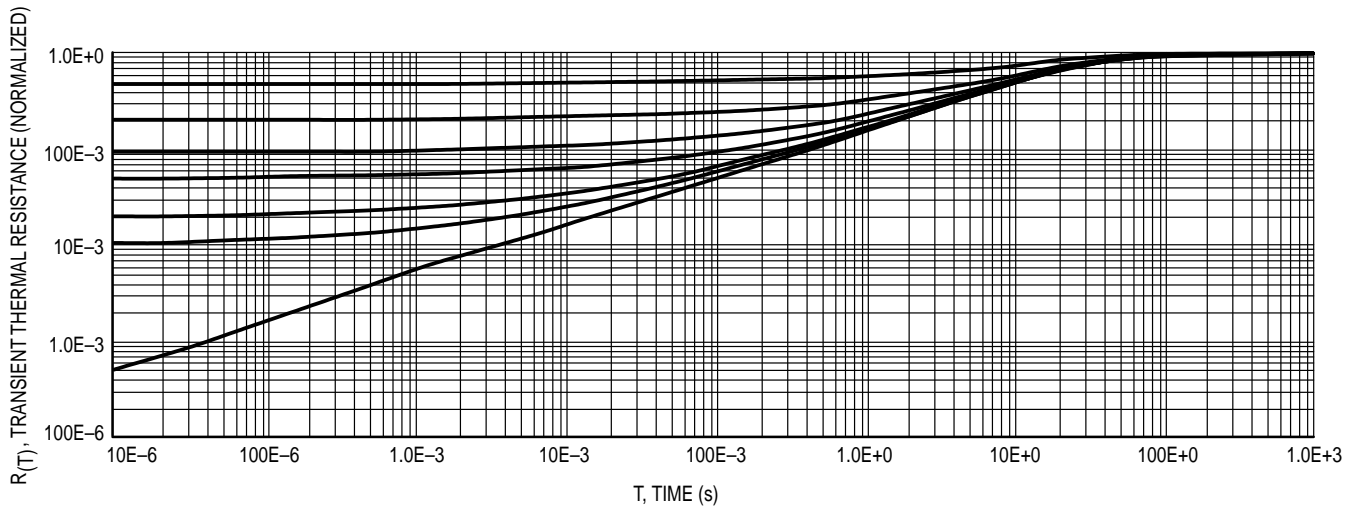


Figure 7. Thermal Response

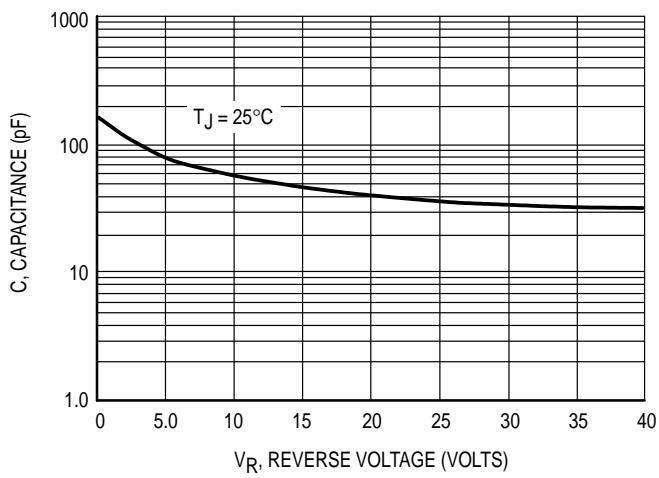
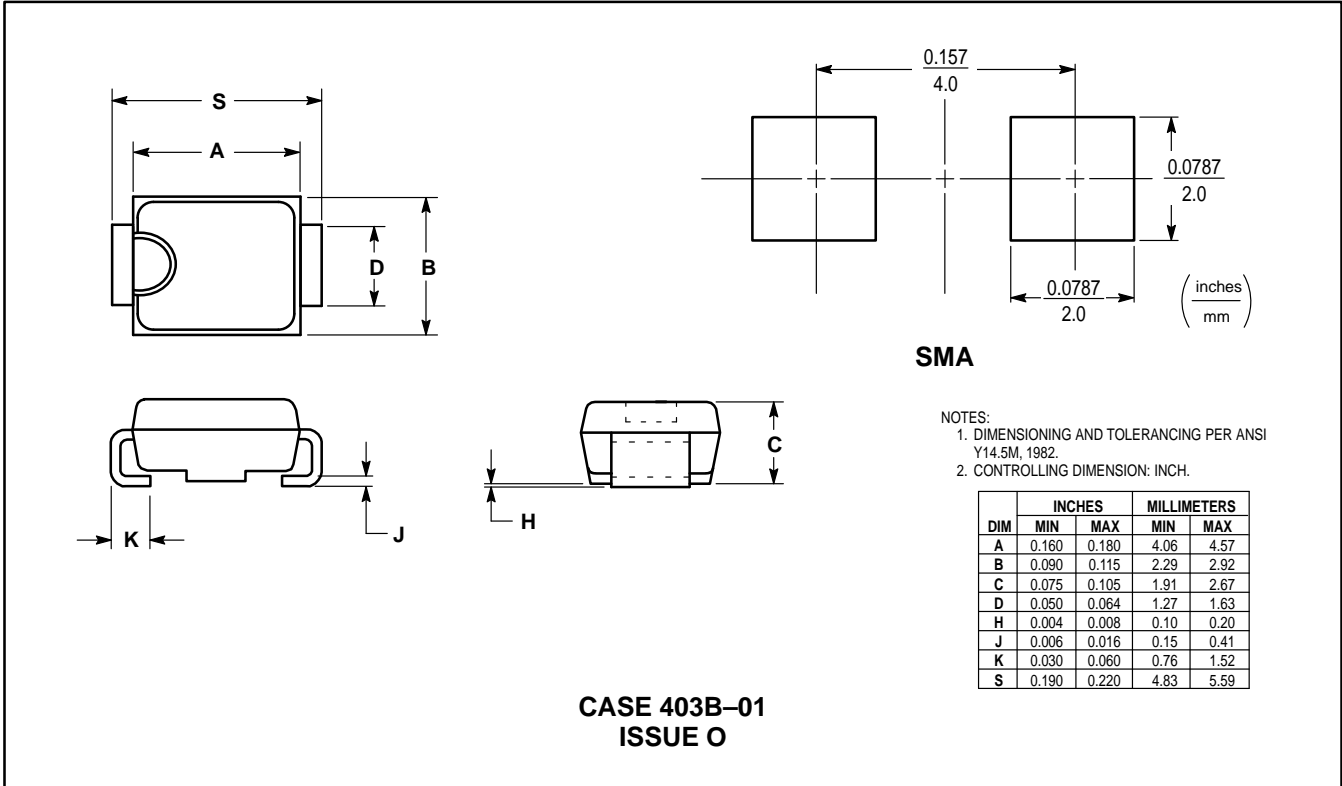


Figure 8. Capacitance

PACKAGE DIMENSIONS



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