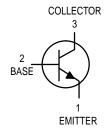
One Watt High Voltage Transistor NPN Silicon

MPSW42

Motorola Preferred Device





MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	VCEO	300	Vdc
Collector-Base Voltage	Vсво	300	Vdc
Emitter-Base Voltage	VEBO	6.0	Vdc
Collector Current — Continuous	IC	500	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	1.0 8.0	Watt mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	2.5 20	Watts mW/°C
Operating and Storage Junction Temperature Range	TJ, T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	125	°C/W
Thermal Resistance, Junction to Case	$R_{ heta JC}$	50	°C/W

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage ⁽¹⁾ (I _C = 1.0 mAdc, I _B = 0)	V(BR)CEO	300	_	Vdc
Collector–Base Breakdown Voltage ($I_C = 100 \mu Adc, I_E = 0$)	V(BR)CBO	300	_	Vdc
Emitter–Base Breakdown Voltage ($I_E = 100 \mu Adc$, $I_C = 0$)	V(BR)EBO	6.0	_	Vdc
Collector Cutoff Current (V _{CB} = 200 Vdc, I _E = 0)	ICBO	_	0.1	μAdc
Emitter Cutoff Current (V _{EB} = 6.0 Vdc, I _C = 0)	IEBO	_	0.1	μAdc

^{1.} Pulse Test: Pulse Width $\leq 300 \ \mu s$, Duty Cycle $\leq 2.0\%$.

Preferred devices are Motorola recommended choices for future use and best overall value.

REV 1



MPSW42

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit
ON CHARACTERISTICS				
DC Current Gain (I _C = 1.0 mAdc, V _{CE} = 10 Vdc) (I _C = 10 mAdc, V _{CE} = 10 Vdc) (I _C = 30 mAdc, V _{CE} = 10 Vdc)	hFE	25 40 40		_
Collector–Emitter Saturation Voltage (I _C = 20 mAdc, I _B = 2.0 mAdc)	VCE(sat)	_	0.5	Vdc
Base–Emitter Saturation Voltage (I _C = 20 mAdc, I _B = 2.0 mAdc)	VBE(sat)	_	0.9	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Current–Gain — Bandwidth Product (IC = 10 mAdc, VCE = 20 Vdc, f = 20 MHz)	fΤ	50	_	MHz
Collector Capacitance (V _{CB} = 20 Vdc, I _E = 0, f = 1.0 MHz)	C _{cb}	_	3.0	pF

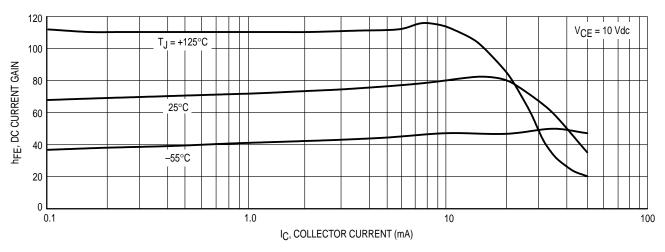


Figure 1. DC Current Gain

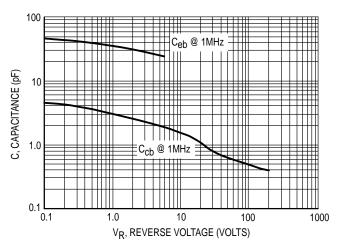


Figure 2. Capacitance

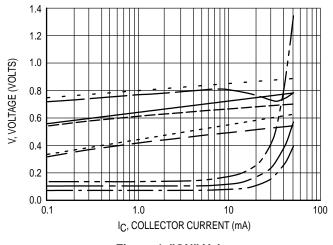


Figure 4. "ON" Voltages

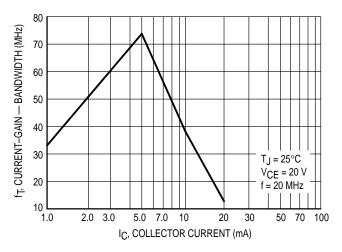
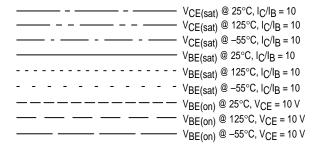
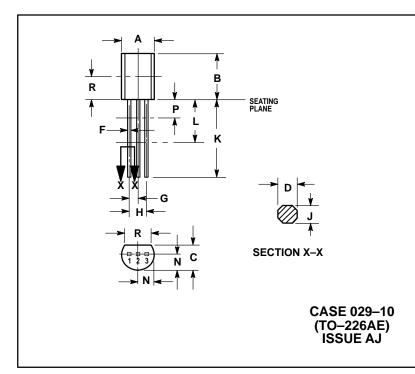


Figure 3. Current-Gain - Bandwidth



PACKAGE DIMENSIONS



NOTES

- DIMENSIONING AND TOLERANCING PER ANSI
 V14 5M 1092
- Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.
- 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- DIMENSION F APPLIES BETWEEN P AND L.
 DIMENSIONS D AND J APPLY BETWEEN L AND K
 MIMIMUM. LEAD DIMENSION IS UNCONTROLLED
 IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.175	0.205	4.44	5.21	
В	0.290	0.310	7.37	7.87	
С	0.125	0.165	3.18	4.19	
D	0.018	0.021	0.457	0.533	
F	0.016	0.019	0.407	0.482	
G	0.045	0.055	1.15	1.39	
Н	0.095	0.105	2.42	2.66	
J	0.018	0.024	0.46	0.61	
K	0.500		12.70		
L	0.250		6.35		
N	0.080	0.105	2.04	2.66	
Р		0.100		2.54	
R	0 135		3 43		

STYLE 1: PIN 1. EMITTER 2. BASE

2. BASE 3. COLLECTOR

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JAPAN: Nippon Motorola Ltd.; SPD, Strategic Planning Office, 141, 4–32–1 Nishi–Gotanda, Shinagawa–ku, Tokyo, Japan. 81–3–5487–8488

Customer Focus Center: 1-800-521-6274

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