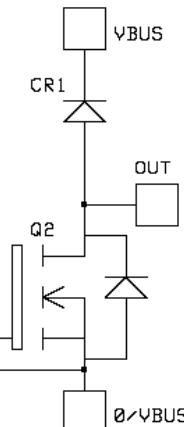


Boost chopper *MOSFET Power Module*

V_{DSS} = 200V
R_{DSon} = 5mΩ max @ T_j = 25°C
I_D = 317A @ T_c = 25°C



Application

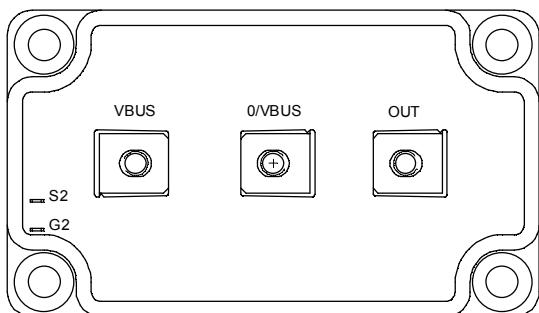
- AC and DC motor control
- Switched Mode Power Supplies
- Power Factor Correction

Features

- Power MOS 7® MOSFETs
 - Low R_{DSon}
 - Low input and Miller capacitance
 - Low gate charge
 - Avalanche energy rated
 - Very rugged
- Kelvin source for easy drive
- Very low stray inductance
 - Symmetrical design
 - M5 power connectors
- High level of integration

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile



Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V _{DSS}	Drain - Source Breakdown Voltage	200	V
I _D	Continuous Drain Current	T _c = 25°C T _c = 80°C	317 237
I _{DM}	Pulsed Drain current		
V _{GS}	Gate - Source Voltage	±30	V
R _{DSon}	Drain - Source ON Resistance	5	mΩ
P _D	Maximum Power Dissipation	T _c = 25°C	1136
I _{AR}	Avalanche current (repetitive and non repetitive)		
E _{AR}	Repetitive Avalanche Energy	89	A
E _{AS}	Single Pulse Avalanche Energy	50	mJ
		2500	

 **CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

All ratings @ $T_j = 25^\circ\text{C}$ unless otherwise specified

Electrical Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
BV_{DSS}	Drain - Source Breakdown Voltage	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 500\mu\text{A}$	200			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 200\text{V}$			200	μA
		$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 160\text{V}$	$T_j = 25^\circ\text{C}$		1000	
$R_{\text{DS(on)}}$	Drain – Source on Resistance	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 158.5\text{A}$			5	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}} = V_{\text{DS}}, I_{\text{D}} = 10\text{mA}$	3		5	V
I_{GSS}	Gate – Source Leakage Current	$V_{\text{GS}} = \pm 30\text{ V}, V_{\text{DS}} = 0\text{V}$			± 200	nA

Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C_{iss}	Input Capacitance	$V_{\text{GS}} = 0\text{V}$ $V_{\text{DS}} = 25\text{V}$ $f = 1\text{MHz}$		27.4		nF
C_{oss}	Output Capacitance			8.72		
C_{rss}	Reverse Transfer Capacitance			0.38		
Q_g	Total gate Charge	$V_{\text{GS}} = 10\text{V}$ $V_{\text{Bus}} = 100\text{V}$ $I_{\text{D}} = 300\text{A}$		448		nC
Q_{gs}	Gate – Source Charge			172		
Q_{gd}	Gate – Drain Charge			188		
$T_{\text{d(on)}}$	Turn-on Delay Time	Inductive switching @ 125°C $V_{\text{GS}} = 15\text{V}$ $V_{\text{Bus}} = 133\text{V}$ $I_{\text{D}} = 300\text{A}$		28		ns
T_r	Rise Time			56		
$T_{\text{d(off)}}$	Turn-off Delay Time			81		
T_f	Fall Time			99		
E_{on}	Turn-on Switching Energy ①	Inductive switching @ 25°C $V_{\text{GS}} = 15\text{V}, V_{\text{Bus}} = 133\text{V}$ $I_{\text{D}} = 300\text{A}, R_{\text{G}} = 1.2\Omega$		1852		μJ
E_{off}	Turn-off Switching Energy ②			1820		
E_{on}	Turn-on Switching Energy ①			2432		μJ
E_{off}	Turn-off Switching Energy ②			2124		

Diode ratings and characteristics

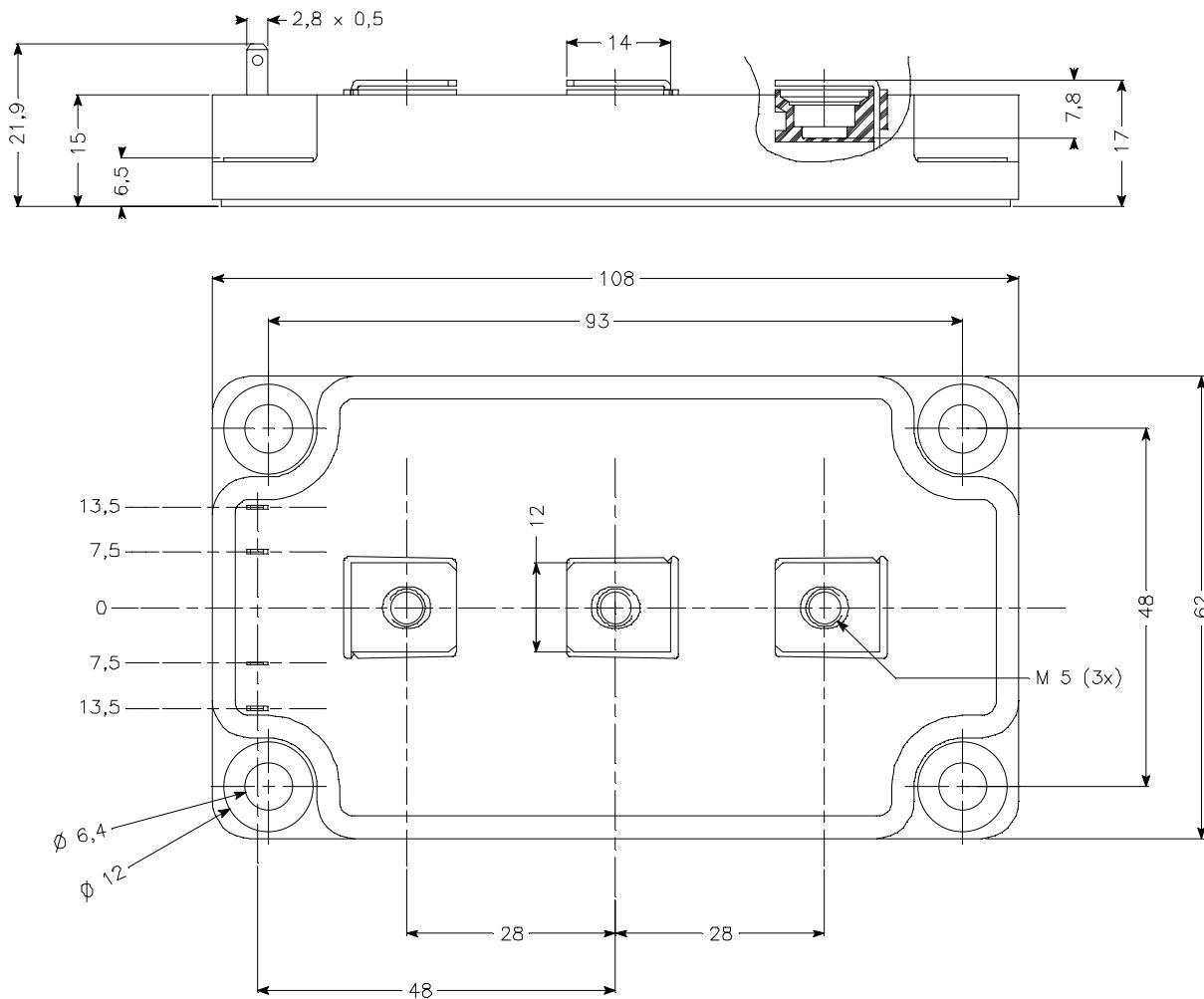
Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
$I_{\text{F(AV)}}$	Maximum Average Forward Current	50% duty cycle $T_c = 85^\circ\text{C}$		240		A
V_F	Diode Forward Voltage	$I_{\text{F}} = 240\text{A}$		1.1	1.15	V
		$I_{\text{F}} = 480\text{A}$		1.4		
		$I_{\text{F}} = 240\text{A}$ $T_j = 125^\circ\text{C}$		0.9		
t_{rr}	Reverse Recovery Time	$I_{\text{F}} = 240\text{A}$ $V_R = 133\text{V}$ $di/dt = 800\text{A}/\mu\text{s}$	$T_j = 25^\circ\text{C}$	31		ns
			$T_j = 125^\circ\text{C}$	60		
Q_{rr}	Reverse Recovery Charge	$I_{\text{F}} = 240\text{A}$ $V_R = 133\text{V}$ $di/dt = 800\text{A}/\mu\text{s}$	$T_j = 25^\circ\text{C}$	240		nC
			$T_j = 125^\circ\text{C}$	1000		

① E_{on} includes diode reverse recovery.

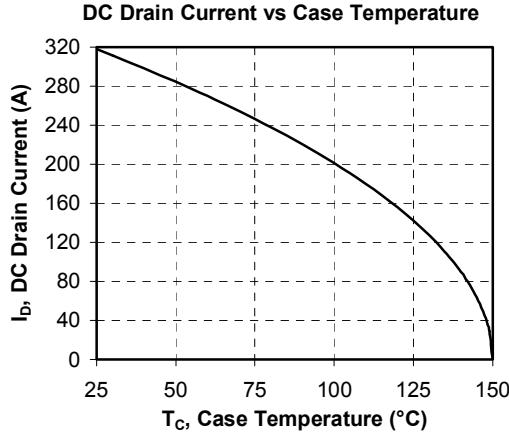
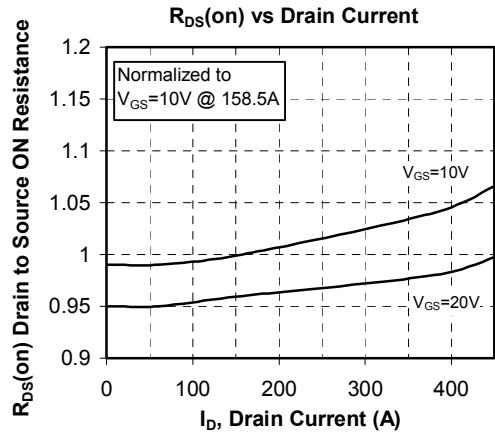
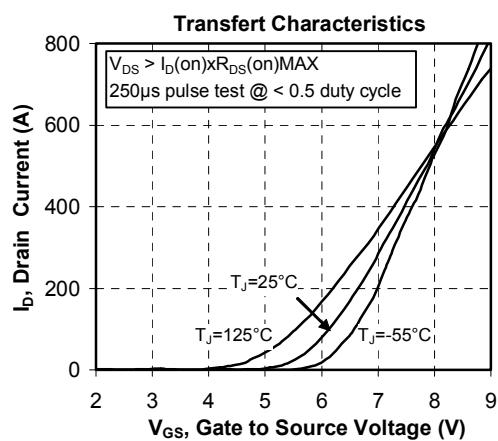
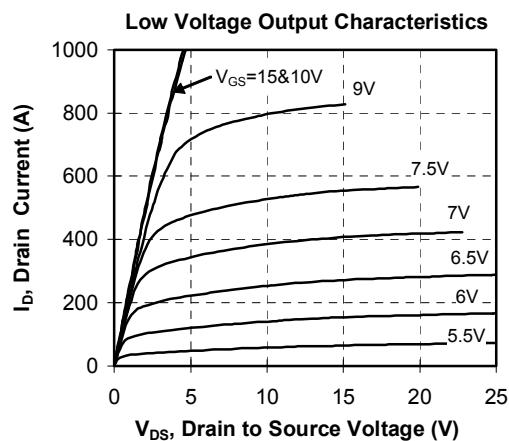
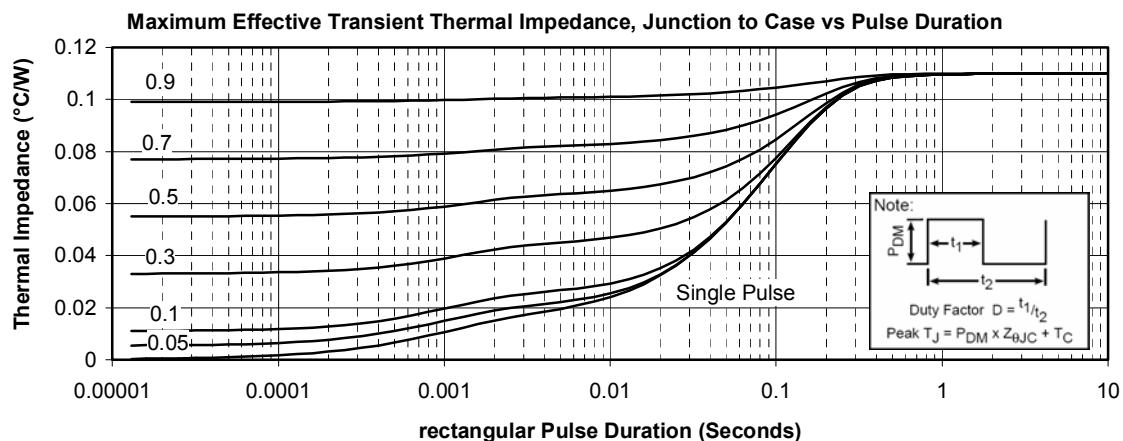
② In accordance with JEDEC standard JESD24-1.

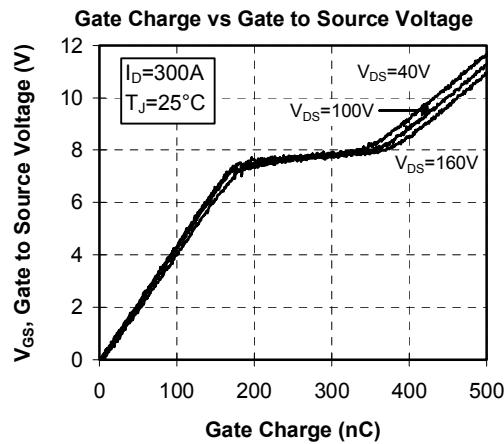
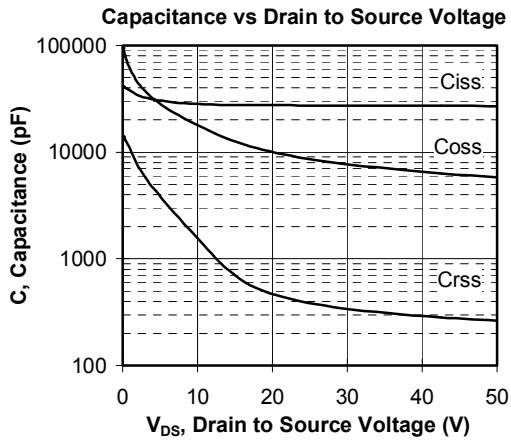
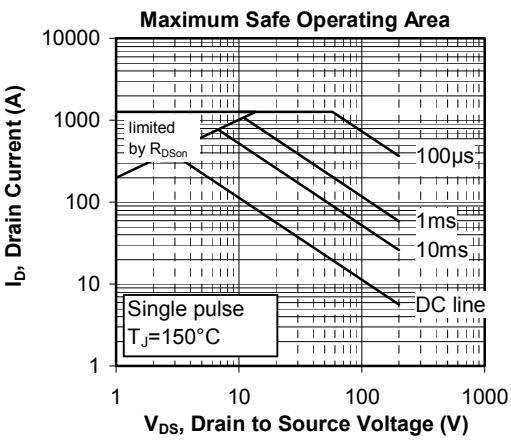
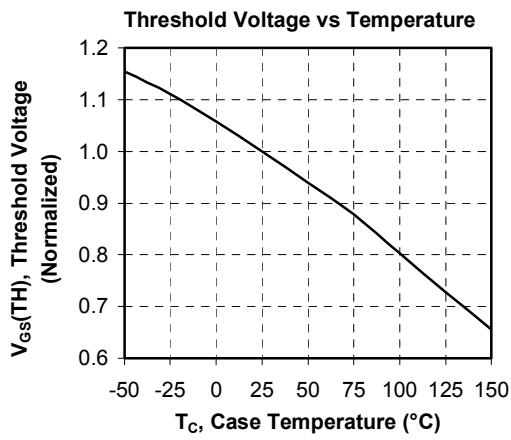
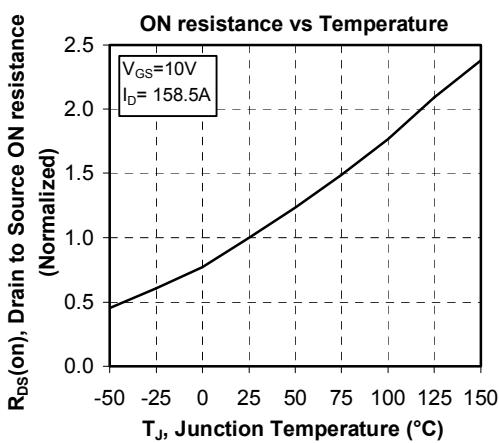
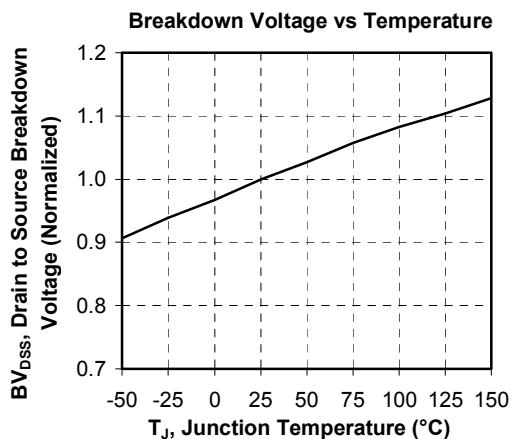
Thermal and package characteristics
Symbol **Characteristic**

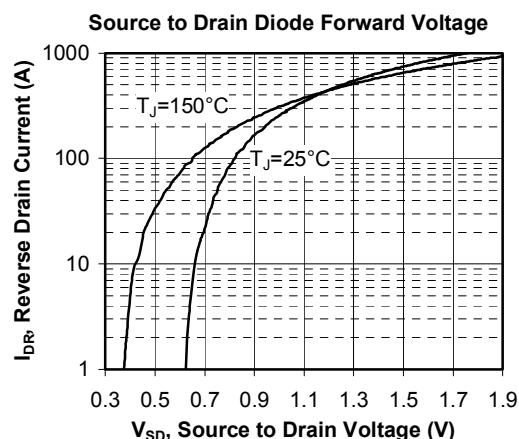
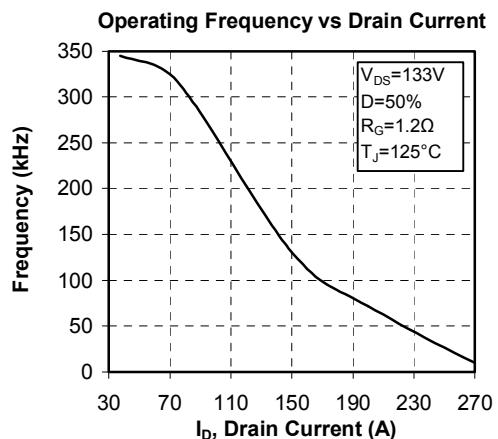
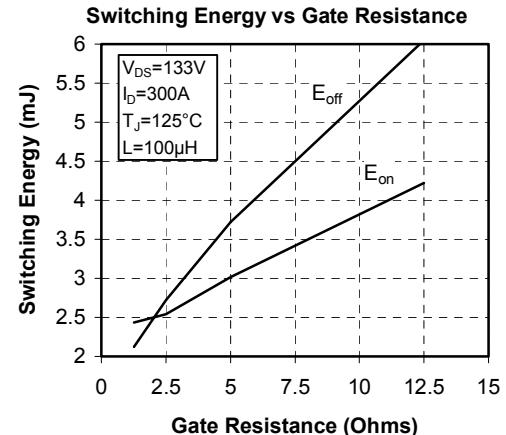
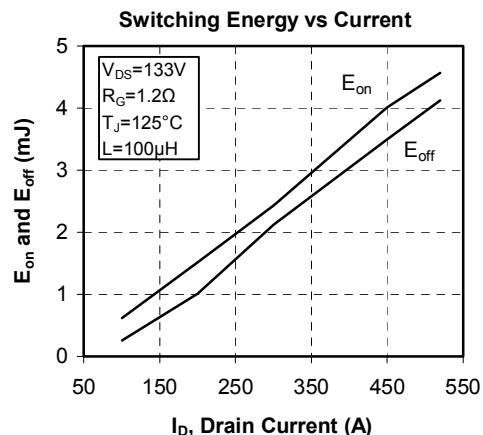
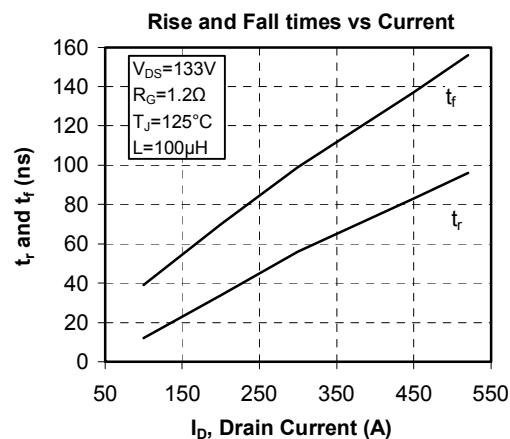
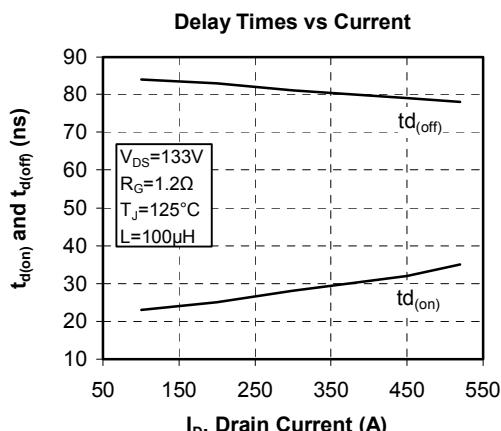
			<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
R_{thJC}	Junction to Case	Transistor			0.11	$^{\circ}\text{C}/\text{W}$
		Diode			0.23	
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, $I_{isol}<1\text{mA}$, 50/60Hz		2500			V
T_J	Operating junction temperature range		-40		150	$^{\circ}\text{C}$
T_{STG}	Storage Temperature Range		-40		125	
T_C	Operating Case Temperature		-40		100	
Torque	Mounting torque	To heatsink	M6	3	5	N.m
		For terminals	M5	2	3.5	
Wt	Package Weight				280	g

Package outline


Typical Performance Curve







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APT's products are covered by one or more of U.S patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S and Foreign patents pending. All Rights Reserved.