

Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company



NPN SILICON PLANAR TRANSISTOR

CJD86

DPAK (TO-252) Plastic Package



For High Speed Switching Application

ABSOLUTE MAXIMUM RATINGS (T_c=25°C)

DESCRIPTION	SYMBOL	VALUE	UNITS
Collector Base Voltage	V _{CBO}	60	V
Collector Emitter Voltage	V _{CEO}	50	V
Emitter Base Voltage	V_{EBO}	6.0	V
Collector Current	I _C	3.0	А
Peak Collector Current	I _{CP}	6.0	А
Power Dissipation	P _D	0.5	W
Mounted on Ceramic Board (250mm ² X 8.0 mm)		1.5	W
Junction Temperature	T _j	150	°C
Storage Temperature Range	T _{stg}	- 55 to +150	

^{*}These ratings are applicable when surface mounted on the minimum pad sizes recommended. (see page no 3)

ELECTRICAL CHARACTERISTICS (T_c=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Emitter Voltage	V_{CEO}	$I_C=1mA$, $I_B=0$	50			V
Collector Base Voltage	V_{CBO}	$I_C=10\mu A, V_{CE}=2V$	60			V
Emitter Base Voltage	V_{EBO}	$I_{E}=10\mu A, I_{C}=0$	6.0			V
Collector Cut Off Current	I _{CBO}	V_{CB} =40V, I_{E} =0			1.0	μΑ
Emitter Cut Off Current	I _{EBO}	V_{EB} =4 V , I_{C} = 0			1.0	μΑ
DC Current Gain	*h _{FE}	**I _C =100mA, V _{CE} =2V	100		560	
		$I_C = 3A, V_{CE} = 2V$	35			

DYNAMIC CHARACTERISTICS

Transition Frequency	f _T	V_{CE} =10V, I_{C} =50mA	150	MHz
Output Capacitance	C_{ob}	V_{CB} =10V, I_{E} =0, f=1MHz	25	pF

**h _{FE} Classifications	R : 100 - 200,	S : 140 - 280,	T : 200 - 400,	U: 280 - 560	
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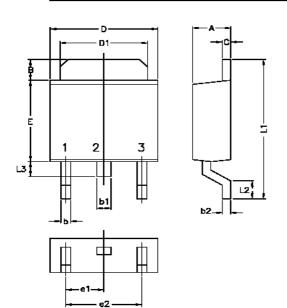
MARKING	CDIL	CDIL	CDIL	CDIL	CDIL
	MJD86R	MJD86S	MJD86T	MJD86U	MJD86
	XY MX	XY MX	XY MX	XY MX	XY MX
XY= Date Code					

^{**}Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%

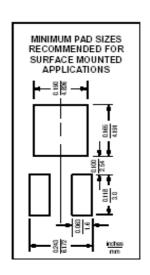
CJD86Rev100605E

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DPAK PACKAGE OUTLINE DIMENSIONS



MIN.	MAX.
2.20	2.40
1.30	1.50
0.55	0.65
0.75	0.85
0.46	0.56
0.46	0.56
6.40	6.60
5.20	5.40
5.40	5.60
2.25	2.35
4.50	4.70
9.25	9.75
0.5	_
0.90	1.10
	2.20 1.30 0.55 0.75 0.46 0.46 6.40 5.20 5.40 2.25 4.50 9.25 0.5

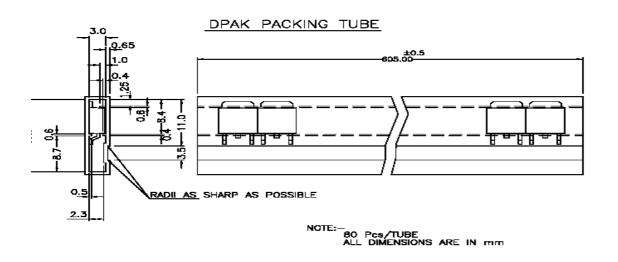




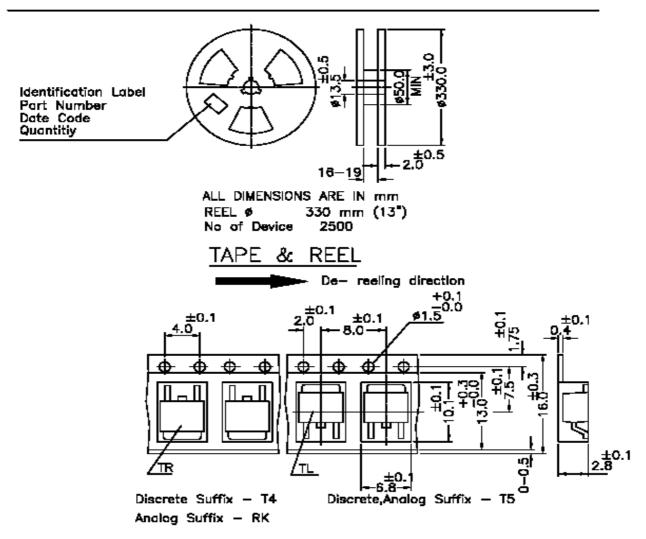
ALL DIMENSIONS ARE IN mm

PIN CONFIGURATION

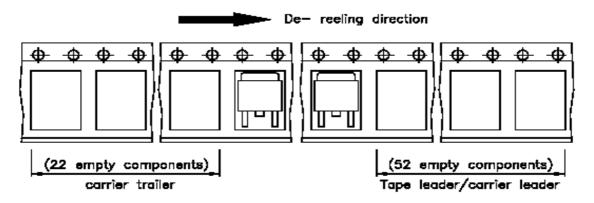
- 1. BASE
- 2. COLLECTOR
- 3. EMITTER



DPAK TAPE & REEL SPECIFICATION



Notes:A maximum of three consecutive components may be missing. Provided this gap is followed by six consecutive components.



CJD86Rev100605E

Customer Notes CJD86

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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Continental Device India Limited

C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119
email@cdil.com www.cdilsemi.com