

SUR533H

Epitaxial planar PNP silicon transistor

Description

• Dual chip digital transistor

Features

- Two SRA2219 chips in SOT-353 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

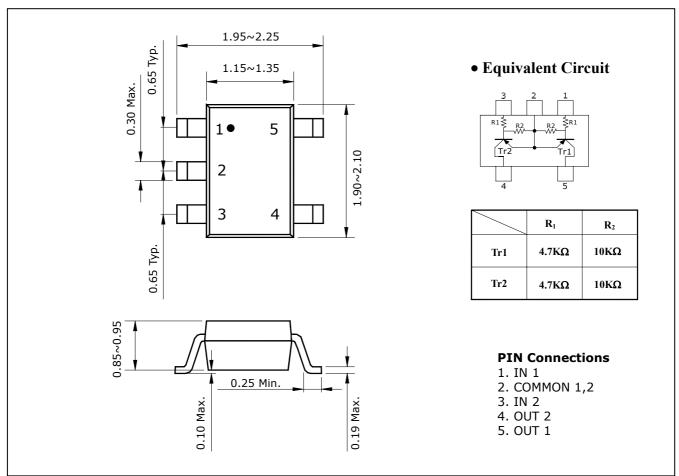
Ordering Information

Type NO.	Marking	Package Code
SUR533H	33H	SOT-353

Outline Dimensions

unit: mm

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Absolute maximum ratings [Tr1,Tr2]

(Ta=25°C)

Characteristic	Symbol	Rating	Unit	
Output voltage	V _o -50		V	
Input voltage	V _I -20, 7		V	
Output current	I_{O}	-100	mA	
Power dissipation	P _D [∗]	200	mW	
Junction temperature	T _J	150	°C	
Storage temperature range	T_{stg}	-55 ~ 150	°C	

^{*:} Total rating

Electrical Characteristics [Tr1,Tr2]

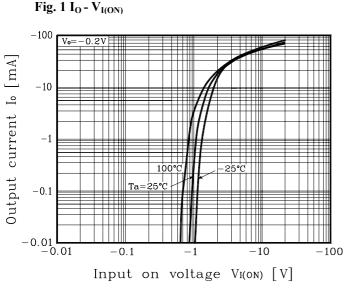
(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	$I_{O(OFF)}$	V _O =-50V, V _I =0	-	-	-500	nA
DC current gain	G_{I}	V _O =-5V, I _O =-10mA	30	-	-	-
Output voltage	V _{O(ON)}	I_{O} =-10mA, I_{I} =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_0 = -0.2V$, $I_0 = -5mA$	-	-1.2	-1.6	٧
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = -5V$, $I_0 = -0.1$ mA	-0.5	-0.82	-	V
Transition frequency	f _T *	V_0 =-10V, I_0 =-5mA, f=1MHz	-	200	1	MHz
Input current	I_{I}	V_{I} =-5V, I_{O} =0	-	-	-1.8	mA
Input resistor (Input to base)	R ₁	-	3.3	4.7	6.1	K Ω
Input resistor (Base to common)	R ₂	-	7	10	13	K Ω

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves [Tr1,Tr2]



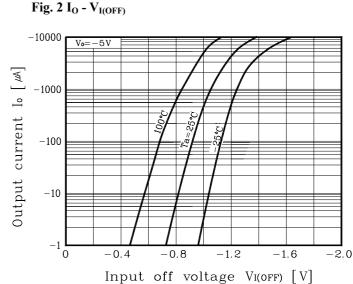
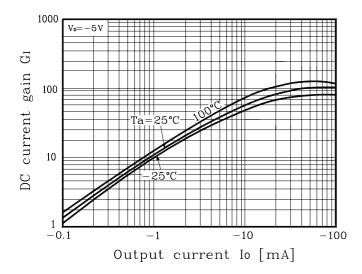


Fig. 3 G_I - I_O



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