

unit : mm

Epitaxial planar PNP silicon transistor

Description

• Dual chip digital transistor

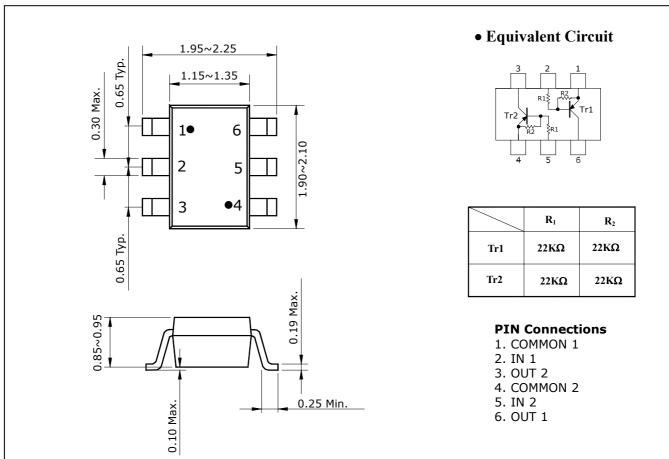
Features

- Two SRA2203 chips in SOT-363 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

Ordering Information

Type NO.	Marking Package Code			
SUR546J	8H8	SOT-363		

Outline Dimensions



SUR546J

(Ta=25°C)

Absolute Maximum Ratings [Tr1,Tr2]

Absolute Maximum Ratings [Tr1,Tr2]			(Ta=25°C)	
Characteristic	Symbol	Rating	Unit	
Output voltage	Vo	-50	V	
Input voltage	VI	-40, 10	V	
Output current	Io	-100	mA	
Power dissipation	P _D *	200	mW	
Junction temperature	Tյ	150	°C	
Storage temperature range	T _{stg}	-55 ~ 150	°C	

*: Total rating

Electrical Characteristics [Tr1,Tr2]

Characteristic Symbol **Test Condition** Min. Typ. Max. Unit -500 Output cut-off current $V_0 = -50V, V_I = 0$ nA $I_{O(OFF)}$ _ -DC current gain G_{I} $V_0 = -5V$, $I_0 = -10mA$ 70 120 --Output voltage I_0 =-10mA, I_I =-0.5mA -0.1 -0.3 V V_{O(ON)} _ Input voltage (ON) V_0 =-0.2V, I_0 =-5mA -2.1 -3.0 V -V_{I(ON)} Input voltage (OFF) V₀=-5V, I₀=-0.1mA -1.2 V -1.0 $V_{I(OFF)}$ _ f_{T}^{*} _ -Transition frequency V_0 =-10V, I_0 =-5mA, f=1MHz 200 MHz V_{I} =-5V, I_{O} =0 Input current -0.36 I_{I} mΑ -Input resistor (Input to base) 15.4 22 28.6 KΩ R_1 -Input resistor (Base to common) 15.4 22 28.6 KΩ R_2 -

* : Characteristic of transistor only

Electrical Characteristic Curves [Tr1,Tr2]

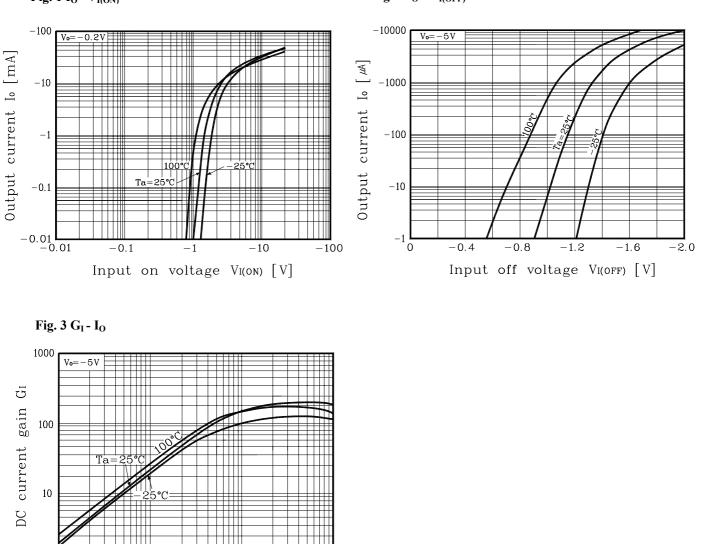


Fig. 1 $I_0 - V_{I(ON)}$

1

-0.1

Fig. 2 I_O - V_{I(OFF)}

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Output current Io [mA]

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