



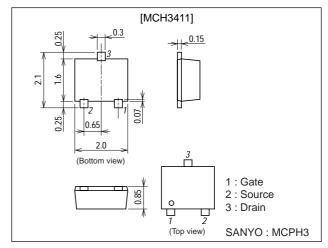
Ultrahigh-Speed Switching Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 2.5V drive.

Package Dimensions

unit : mm 2167A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ID		3	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	12	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm ² X0.8mm)	1	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			1.114
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _G S=0	30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VDS=0			±10	μΑ
Cutoff Voltage	Vgs(off)	V _{DS} =10V, I _D =1mA	0.4		1.3	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =1.5A	3.5	5.0		S
Static Drain-to-Source On-State Resistance	RDS(on)1	ID=1.5A, VGS=4V		69	90	mΩ
	R _{DS} (on)2	I _D =1A, V _{GS} =2.5V		84	118	mΩ

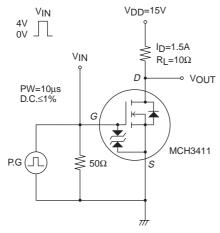
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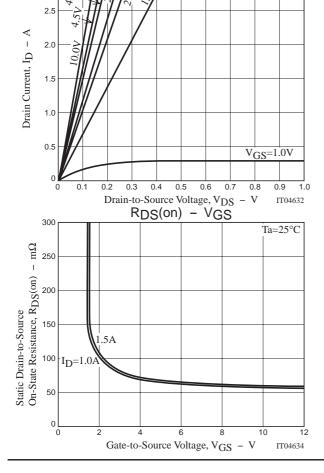
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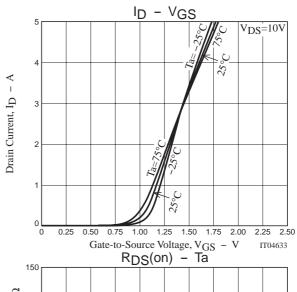
Parameter	Cumbal	Conditions	Ratings			Unit
	Symbol		min	typ	max	Unit
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		270		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		38		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		23		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		10		ns
Rise Time	t _r	See specified Test Circuit.		30		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		42		ns
Fall Time	tf	See specified Test Circuit.		52		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =4V, I _D =3A		3.7		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =4V, I _D =3A		0.7		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =4V, I _D =3A		0.5		nC
Diode Forward Voltage	V _{SD}	I _S =3A, V _{GS} =0		0.86	1.2	V

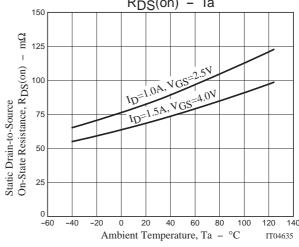
Switching Time Test Circuit

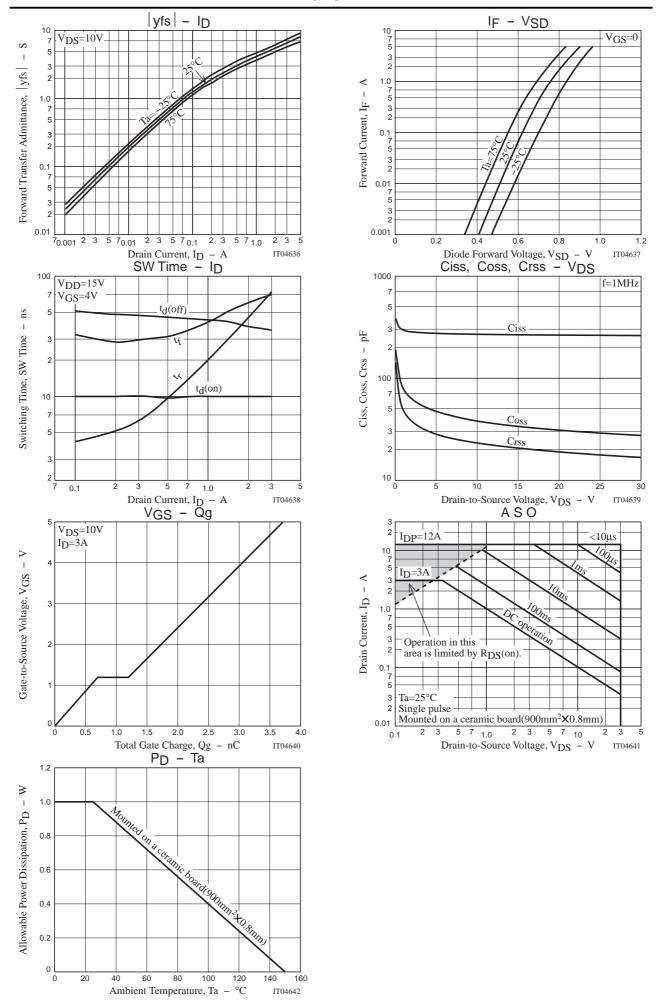


ID - VDS









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