

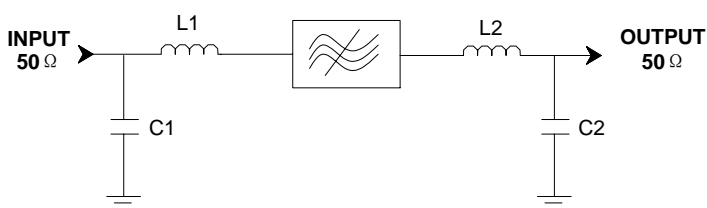
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	61.28	61.38	61.48
Insertion Loss	dB	-	9.5	10
1 dB Bandwidth	MHz	-	0.48	-
3 dB Bandwidth	MHz	0.9	0.93	-
40 dB Bandwidth	MHz	-	2.84	-
50 dB Bandwidth	MHz	-	3.3	4
Passband Variation	dB	-	0.2	2
Absolute Delay	usec	-	1.3	-
Ultimate Rejection($f_0 \pm 12\text{MHz}$)	dB	40	54	-
Material Temperature coefficient	KHz/°C	-1.1048		
Ambient Temperature	°C	25		
Package Size	SMD19*6.5			

Notes:


1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration

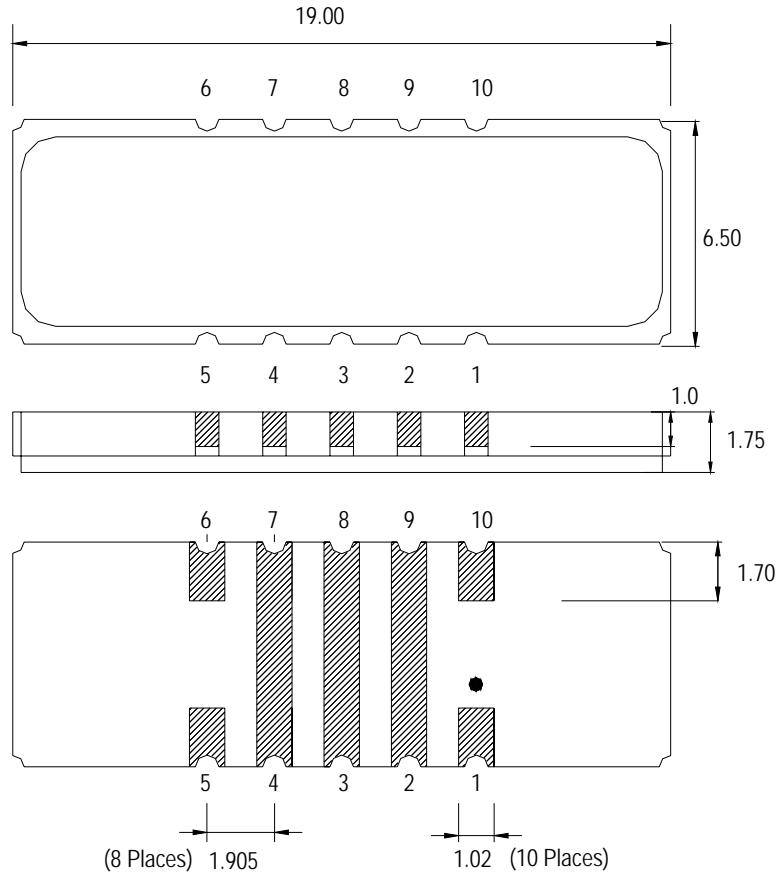


L1=180nH L2=220nH
C1=39pF C2=56pF
Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

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		Rev. Date	2006-6-22	
		Rev.	1.0	Page

Package Dimension



Package:SMD19* 6.5B

Unit:mm

Input 10
Output 5
Ground 1,2,3,4,6,7,8,9

Package: SMD19*6.5

Unit: mm

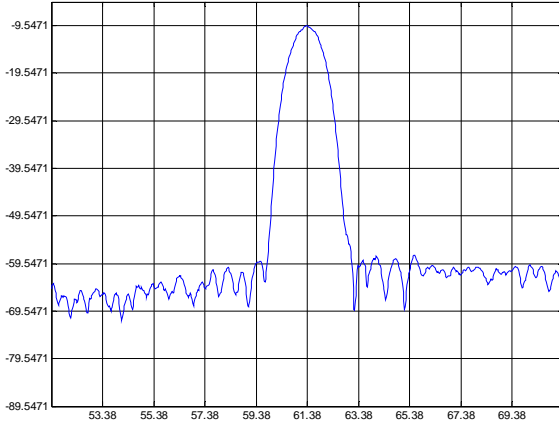


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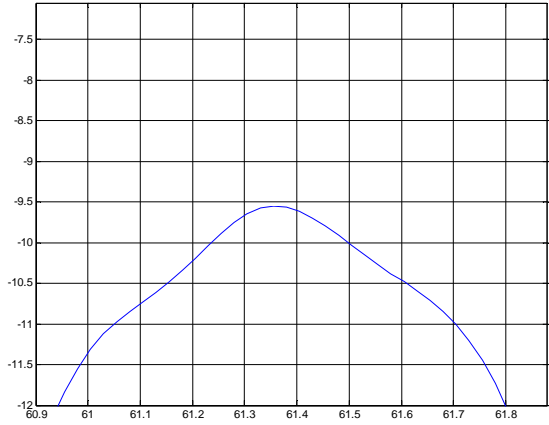
Part Number	LB061DS01	
Rev. Date	2006-6-22	
Rev.	1.0	Page 2/3

Typical Performance

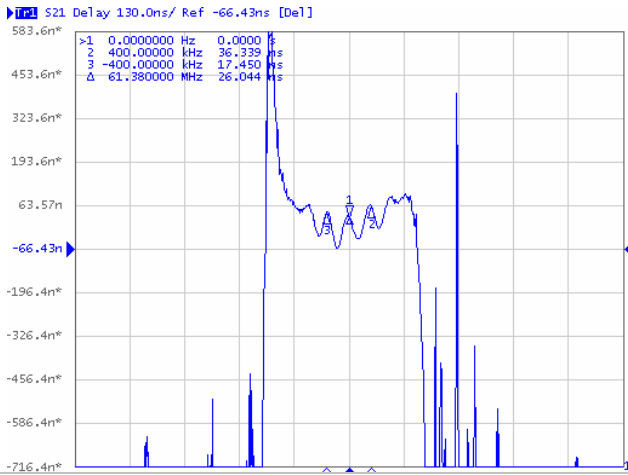
Frequency Respond



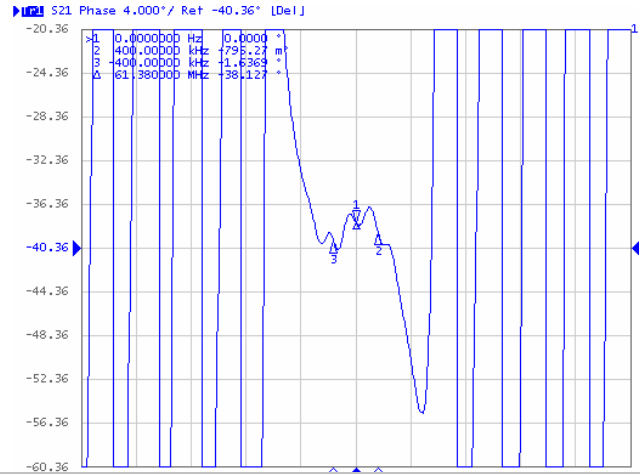
Passband Respond



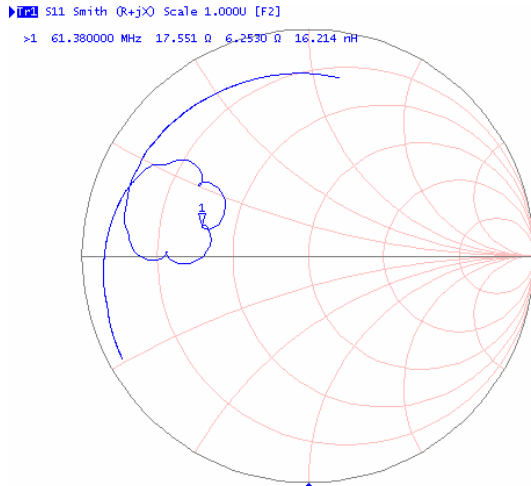
Group Delay Variation($f_0 \pm 0.4$ MHz)



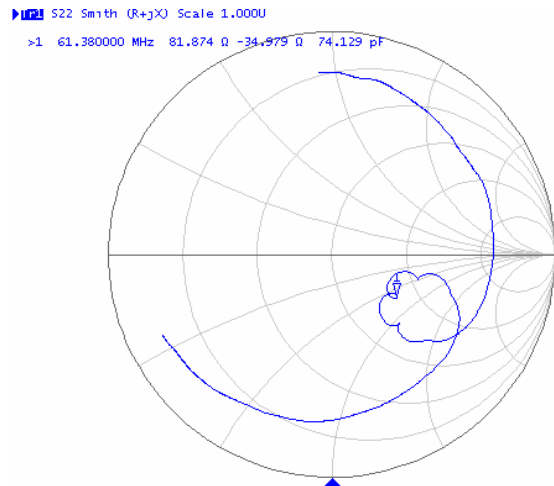
Phase Linearity($f_0 \pm 0.4$ MHz)



Smith Chart S11



Smith Chart S22



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Part Number	LB061DS01	
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Rev.	1.0	Page 3/3