

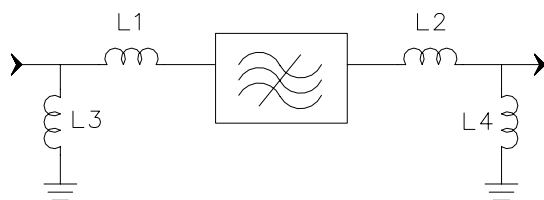
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	139.95	140	140.05
Insertion Loss	dB	-	13	14
1 dB Bandwidth	MHz	1.2	1.28	-
3 dB Bandwidth	MHz	1.6	1.81	-
40 dB Bandwidth	MHz	-	3.64	3.75
Passband Variation	dB	-	0.4	1
Group delay Variation ($F_0 \pm 0.8\text{MHz}$)	nsec	-	60	-
Phase Linearity ($F_0 \pm 0.8\text{MHz}$)	degree	-	3	-
Ultimate Rejection	dB	40	42	-
Absolute Delay	usec	-	1.5	-
Material Temperature coefficient	KHz/°C	0.14		
Ambient Temperature	°C	25		
Package Size	SF-26			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration




L1=82nH L2=68nH

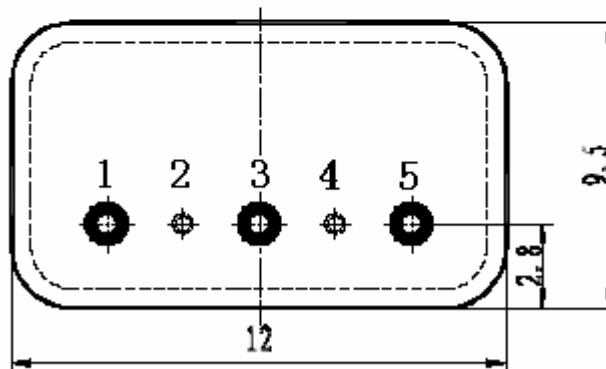
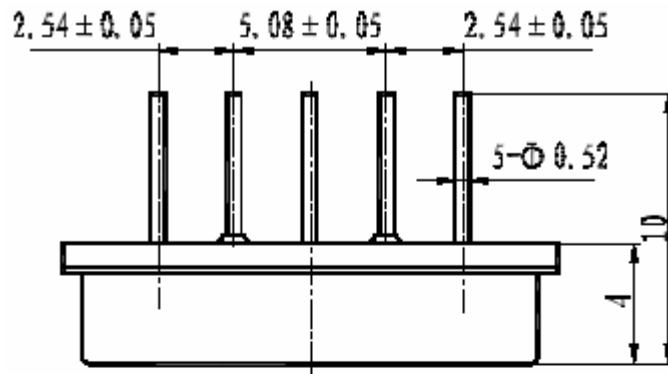
L3=27nH L4=22nH

Source/Load Impedance=50 ohm


Notes - Component values may change depending on board layout.

	SIPAT Co., Ltd. (CETC No. 26 Research Institute) Nanping Huayuan Road No. 14 Chongqing, China, 400060	Part Number	LB140DS24	
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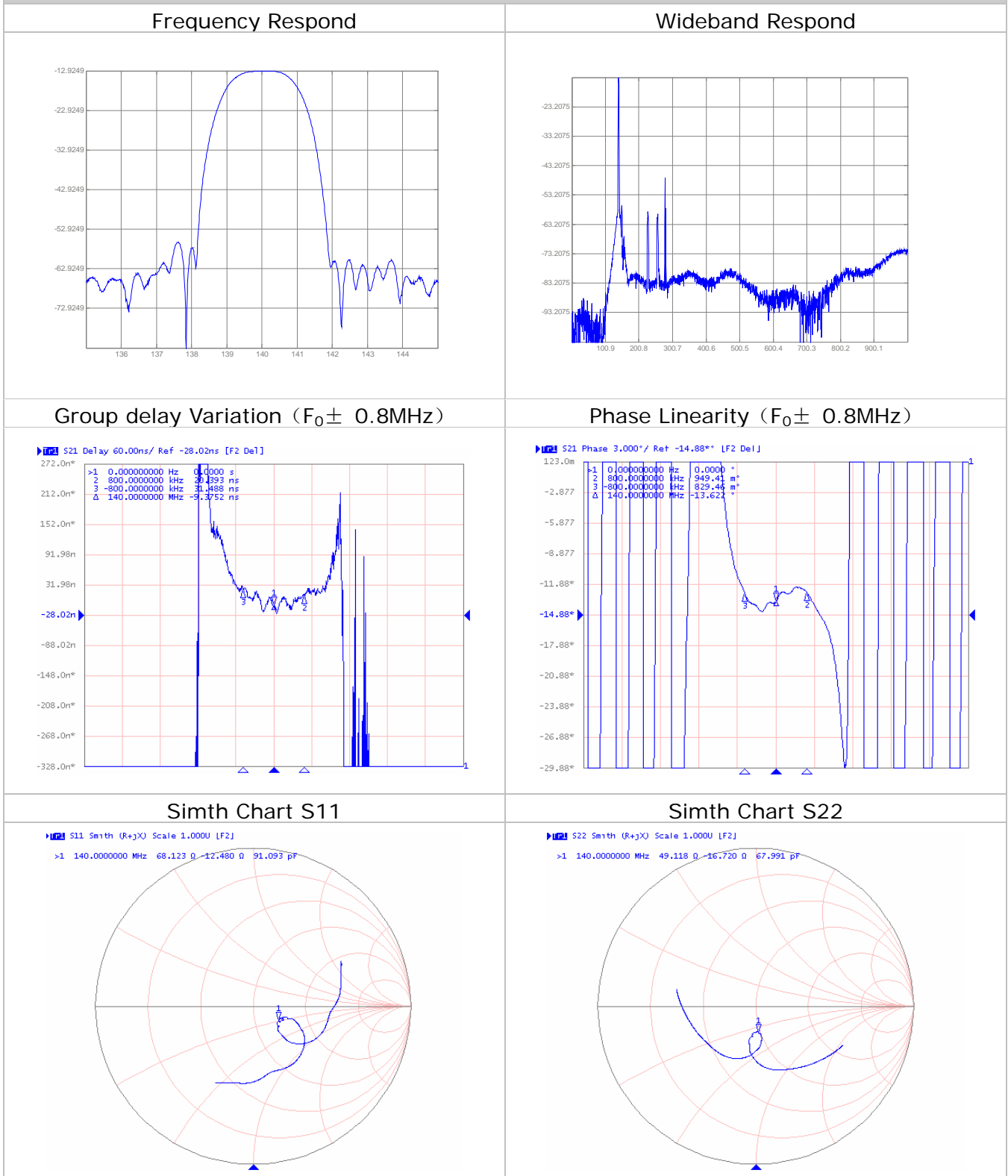
Package Dimension



Pin 1:input
pin 5:Output

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Typical Performance



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