PNP Silicon General Purpose Amplifier Transistor

This PNP transistor is designed for general purpose amplifier applications. This device is housed in the SC-75/SOT-416/SC-90 package which is designed for low power surface mount applications, where board space is at a premium.

Features

- Reduces Board Space
- High h_{FE}, 210–460 (typical)
- Low V_{CE(sat)}, < 0.5 V
- Available in 8 mm, 7-inch/3000 Unit Tape and Reel
- Pb–Free Package is Available*

MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{(BR)CBO}	-60	Vdc
Collector – Base Voltage	V _{(BR)CEO}	-50	Vdc
Emitter – Base Voltage	V _{(BR)EBO}	-6.0	Vdc
Collector Current – Continuous	۱ _C	-100	mAdc

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

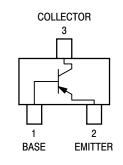
Characteristic	Symbol	Max	Unit
Power Dissipation (Note 1)	PD	150	mW
Junction Temperature	TJ	150	°C
Storage Temperature Range	T _{stg}	-55 ~ +150	°C

 Device mounted on a FR-4 glass epoxy printed circuit board using the minimum recommended footprint.



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MARKING DIAGRAM



F9 = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$)

Characteristic	Symbol	Min	Тур	Max	Unit
Collector–Base Breakdown Voltage ($I_C = -50 \ \mu Adc$, $I_E = 0$)	V _{(BR)CBO}	-60	-	-	Vdc
Collector–Emitter Breakdown Voltage ($I_C = -1.0 \text{ mAdc}, I_B = 0$)	V _{(BR)CEO}	-50	-	-	Vdc
Emitter–Base Breakdown Voltage ($I_E = -50 \ \mu Adc$, $I_E = 0$)	V _{(BR)EBO}	-6.0	-	-	Vdc
Collector–Base Cutoff Current ($V_{CB} = -30$ Vdc, $I_E = 0$)	I _{CBO}	-	-	-0.5	nA
Emitter–Base Cutoff Current ($V_{EB} = -5.0 \text{ Vdc}, I_B = 0$)	I _{EBO}	-	-	-0.5	μΑ
Collector–Emitter Saturation Voltage (Note 2) ($I_c = -50$ mAdc, $I_B = -5.0$ mAdc)	V _{CE(sat)}	-	-	-0.5	Vdc
DC Current Gain (Note 2) ($V_{CE} = -6.0 \text{ Vdc}, I_C = -1.0 \text{ mAdc}$)	h _{FE}	120	-	560	-
Transition Frequency ($V_{CE} = -12$ Vdc, $I_C = -2.0$ mAdc, f = 30 MHz)	f _T	_	140	_	MHz
Output Capacitance ($V_{CB} = -12$ Vdc, $I_E = 0$ Adc, $f = 1$ MHz)	C _{OB}	-	3.5	-	pF

2. Pulse Test: Pulse Width \leq 300 µs, D.C. \leq 2%.

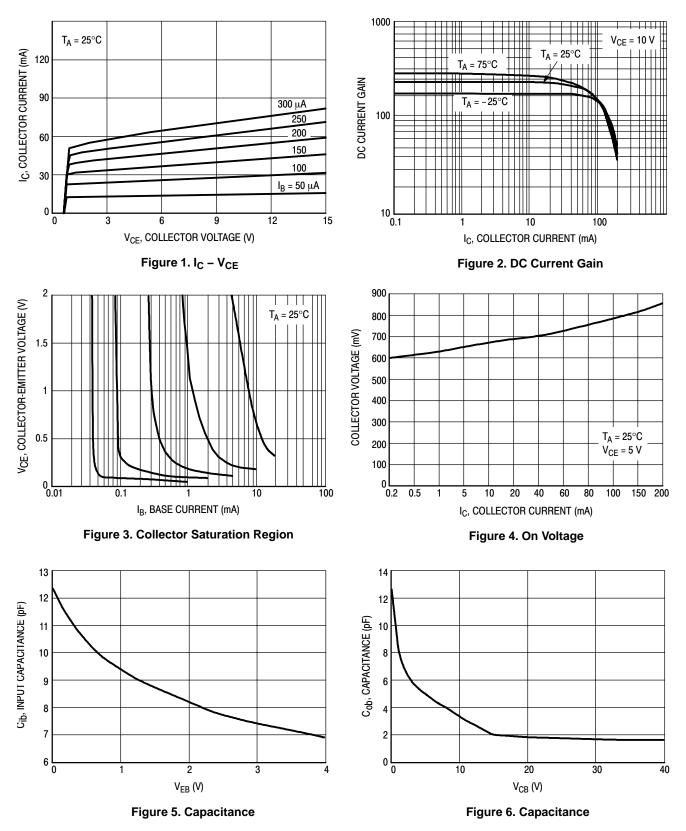
ORDERING INFORMATION

Device	Package	Shipping [†]
2SA1774	SC-75	3000 Tape & Reel
2SA1774G	SC-75 (Pb-Free)	3000 Tape & Reel
2SA1774T1	SC-75	3000 Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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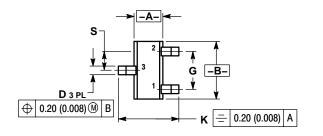


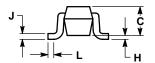


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PACKAGE DIMENSIONS

SC-75/SOT-416 CASE 463-01 ISSUE C





NOTES:

 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
CONTROL LING DIMENSION: MILLIMETER

2. (. CONTROLLING DIMENSION: MILLIMETER.							
		MILLIMETERS		INC	HES			
	DIM	MIN	MAX	MIN	MAX			
	Α	0.70	0.90	0.028	0.035			
	в	1.40	1.80	0.055	0.071			
	С	0.60	0.90	0.024	0.035			

	0.10 0.00		0.020	0.000
В	1.40	1.80	0.055	0.071
С	0.60	0.90	0.024	0.035
D	0.15	0.30	0.006	0.012
G	1.00 BSC		0.039 BSC	
н		0.10		0.004
J	0.10	0.25	0.004	0.010
к	1.45	1.75	0.057	0.069
L	0.10	0.20	0.004	0.008
S	0.50 BSC		0.020 BSC	

STYLE 1: PIN 1. BASE

2. EMITTER 3. COLLECTOR

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