UNISONIC TECHNOLOGIES CO., LTD

T78040

LINEAR INTEGRATED CIRCUIT

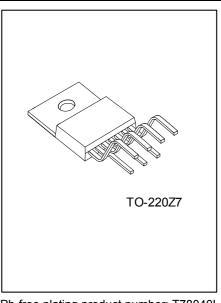
VERTICAL DEFLECTION OUTPUT CIRCUIT

DESCRIPTION

The UTC T78041 is a monolithic integrated circuit and designed for use in high-definition TV and CRT monitors. It is intended to directly drive the deflection coil. Besides, the T78040 offers a maximum deflection current of 1.8A peak to peak to suitable for small to medium diameter CRTs.

FEATURES

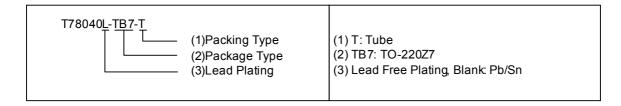
- * Deflection current can be 1.8A peak value
- * Deflection voltage up to 70V
- * Flyback generator
- * Thermal protection circuit
- * Low cross-over distortion
- * Supports DC Coupling



*Pb-free plating product number: T78040L

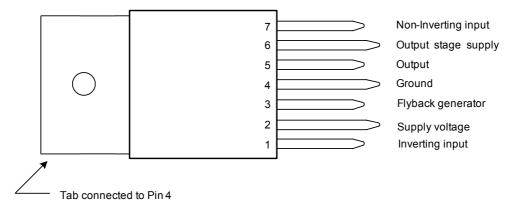
ORDERING INFORMATION

Order Number		Dookaga	Dooking	
Normal	Lead Free Plating	Package	Packing	
T78040-TB7-T	T78040L-TB7-T	TO-220Z7	Tube	

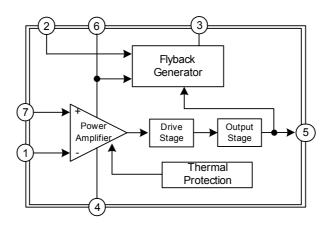


www.unisonic.com.tw 1 of 5 QW-R121-013,A

■ PIN CONFIGURATIONS



■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS (Ta= 25)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage (pin 2 to Pin4)	Vcc2	34	V
Output Peak Power Supply Voltage (Pin 5 to Pin 4)	Vcc6	70	V
Output Peak Current	I _{5MAX}	-1.5 ~ +1.5	Α
Power Dissipation	P_{D}	9	W
Junction Temperature	TJ	150	
Operating Temperature	T_OPR	-20 ~ +85	
Storage Temperature	T_{STG}	-40 ~ +150	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERAML DATA

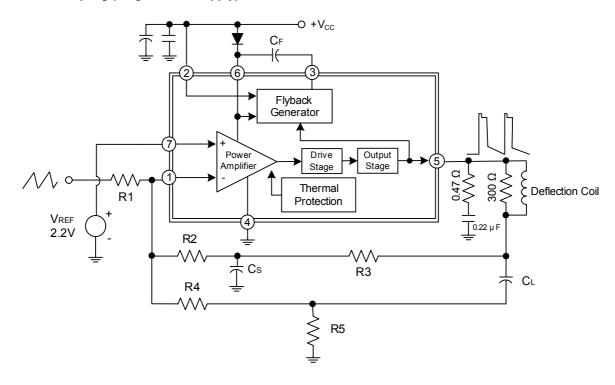
PARAMETER	SYMBOL	RATINGS	UNIT	
Thermal Resistance Junction-Case	θ_{JC}	4.0	°C/W	

■ **ELECTRICAL CHARACTERISTICS** (Ta = 25 , V_{CC} = 24V, unless otherwise specified)

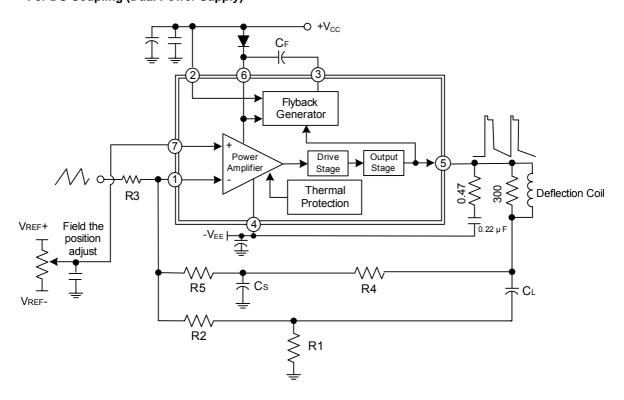
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Voltage	Vcc		16	24	33	V
Output Saturated Voltage to GND	V _{S5-4}	I ₅ =0.9A			1.3	V
Output Saturated Voltage to Supply	V _{S5-6}	I ₅ =-0.9A			3.2	V
Saturation Voltage on Pin 3	V _{S3-4}	I ₃ = 20mA			1.8	V
Saturation Voltage to Pin 3 (2nd part of flyback)	V _{S3-2}	$I_3 = -0.9A$			3.0	V
Output Middle Point Voltage	$V_{O(MID)}$		11	12	13	V
Quiescent Current	ΙQ		35		65	mA
Recommend Biggest Deflect Current	15 _{P-P}		•		1.8	Α

APPLICATION CIRCUITS

For AC Coupling (Single Power Supply)



For DC Coupling (Dual Power Supply)



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