

MD2001FX

High voltage NPN Power transistor for standard Definition CRT display

Preliminary Data

Features

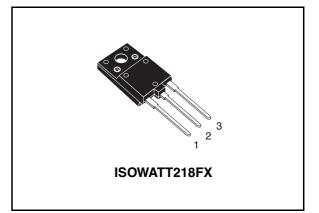
- State-of-the-art technology:
 - Diffused collector "Enhanced generation"
- More stable performances versus operating temperature variation
- Low base-drive requirements
- Tighter h_{FE} range at operating collector current
- Fully insulated power package U.L. compliant

Applications

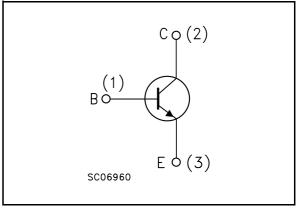
 Horizontal deflection output for monitor and real flat TV

Description

The MD2001FX is manufactured using Diffused Collector in Planar Technology adopting new and enhanced high voltage structure. The new MD product series show improved silicon efficiency bringing updated performance to the Horizontal Deflection stage.



Internal schematic diagram



Order code

Part Number	Marking	Package	Packaging
MD2001FX	MD2001FX	ISOWATT218FX	Tube

1/8

1 Electrical ratings

Table 1.	Absolute	maximum	rating
	Absolute	maximum	raung

Symbol	Parameter	Value	Unit	
V _{CES}	Collector-emitter voltage (V _{BE} = 0)	1500	V	
V _{CEO}	Collector-emitter voltage $(I_B = 0)$	700	V	
V _{EBO}	Collector-base voltage $(I_{\rm C} = 0)$	9	V	
Ι _C	Collector current	12	А	
I _{CM}	Collector peak current (t _P < 5ms)	18	А	
Ι _Β	Base current	6	А	
P _{TOT}	Total dissipation at $T_c = 25^{\circ}C$	58	W	
V _{ins}	Insulation withstand voltage (RMS) from all three leads to external heatsink 2500		V	
T _{stg}	Storage temperature	-65 to 150		
Т _Ј	Max. operating junction temperature	150	- °C	

Table 2. Thermal data

Symbol	Parameter	Value	Unit
R _{thj-case}	Thermal resistance junction-case max	2.15	°C/W

2 Electrical characteristics

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current (V _{BE} =0)	$V_{CE} = 1500V$ $V_{CE} = 1500V$; $T_{C} = 125^{\circ}C$			0.2 2	mA mA
I _{EBO}	Emitter cut-off current (I _C =0)	V _{EB} = 9V			1	mA
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage (I _C =0)	I _C = 100mA	700			v
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 6A I _B = 1.5A			1.8	V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C = 6A I _B = 1.5A			1.2	V
h _{FE} ⁽¹⁾	DC current gain	$I_{C} = 6A \qquad V_{CE} = 1V$ $I_{C} = 6A \qquad V_{CE} = 5V$		4.5	7	
t _s t _f	Inductive load Storage time Fall time	$\begin{split} I_{C} &= 5A \qquad I_{B(on)} = 0.9A \\ V_{BE(off)} &= -2.7V f_{h} = 64KHz \\ I_{BB(off)} &= 1.6\mu H \end{split}$		2.6 0.2		μs μs

Table 3. Electrical characteristics

1. Pulsed: Pulse duration = 300 ms, duty cycle 1.5 %

57

2.1 Test circuits

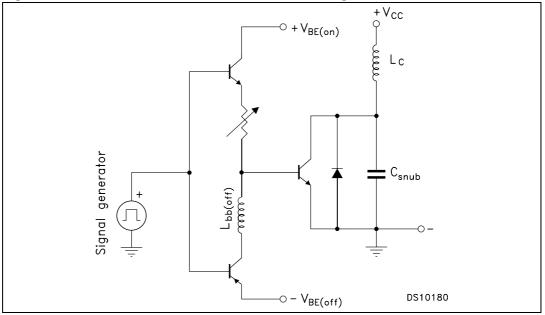
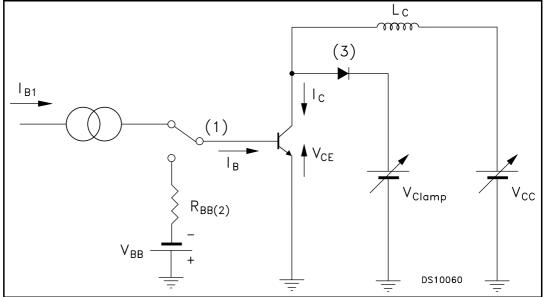


Figure 1. Power losses and inductive load switching





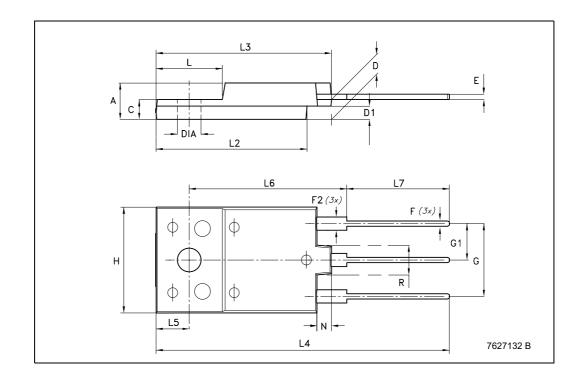
3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



-	mm.		
DIM.	MIN.	ТҮР	MAX.
А	5.30		5.70
С	2.80		3.20
D	3.10		3.50
D1	1.80		2.20
E	0.80		1.10
F	0.65		0.95
F2	1.80		2.20
G	10.30		11.50
G1		5.45	
Н	15.30		15.70
L	9		10.20
L2	22.80		23.20
L3	26.30		26.70
L4	43.20		44.40
L5	4.30		4.70
L6	24.30		24.70
L7	14.60		15
N	1.80		2.20
R	3.80		4.20
Dia	3.40		3.80





4 Revision history

Table 4. Revision history

Date	Revision	Changes
25-May-2007	1	Initial release.



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