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APPROVAL SHEET

Model No.: PMLCM140RM

Description: 4.0 inch TFT LCD+ Driving Board mock-up

Customer P/N:

Revision:	0.2
Date:	March 14TH, 2000
Company(English/Chines	e):
Vandor Source: Unipac O	ptoelectronics Corporation/ Promate Electronics Co., Ltd.
Project Leader approved	Sign:

Prepared By Design Engineer	Reviewed By Project Leader	Issued By D.C.C.	
		\	Wiscoll
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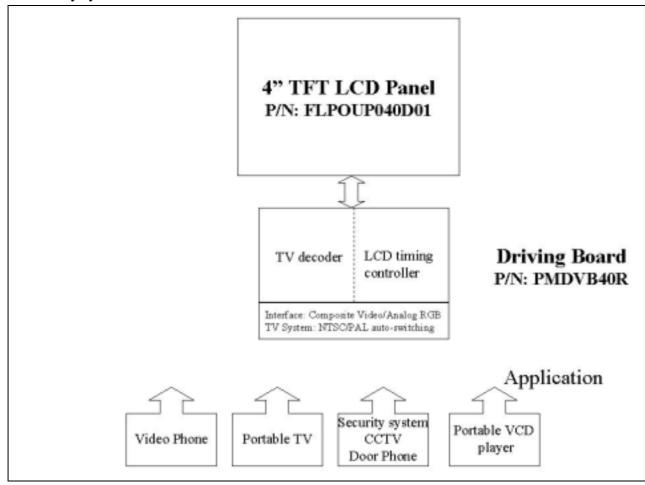
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A. General Description:

The model "PMLCM40R" is designed to replace the traditional CRT terminals via the TFT LCD panels. The model provides the easy selection of the standard composite video input or the specific R/G/B signals input. Besides, it also provides the most friendly fixed position molding for all customers in the worldwide.

B. Applications:

- * Videophone system
- * Door phone system
- * Portable TV
- * Security system



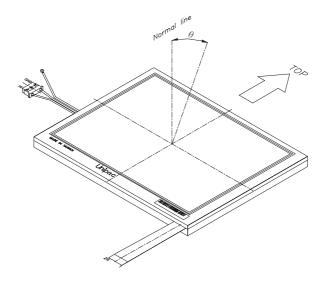
C. Totally LCD module general specification

C-1. LCD specification

Items	Specification	Remark	
Display resolution(dot)	480 x 234		
Active area(mm)	82.1(W) x 61.8(H)		
Screen size(inch)	4.05(Diagonal)		
Dot pitch(mm)	0.171(W) x 0.264(H)		
Color Configuration	R.G.B. Delta		
Display Mode	Normally white		
Overall dimension(Panel only)	96.0(W) x 76.0(H) x 6.5(D)		
Back light technology	CCFL x 1		
Brightness(Luminance)	250 nit	Note 1	
Front surface	Anti-glare hard coating		
View Angle	Top 10° Bottom 30° Left 45°	Note 2	
CR(Contrast Ratio)	Right 45°	Note 3	
CK(Colitiast Katio)	Min. Typ. Max.	Note 3	
White chromaticity	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	Note 4	
Response time	$ \begin{array}{ c c c c c c }\hline Rise \\ \hline Fall \\ \hline \end{array} \begin{array}{ c c c c c c c }\hline - & 25ms & 50ms \\ \hline - & 30ms & 60ms \\ \hline \end{array} $	Note 2	
Storage Temperature	- 25°C ∼ 80°C		
Operating Temperature	0°C ∼ 60°C		
Reliability test	Note 5		
Back light electrical characteristics	Note 5		
LCD electrical characteristics	Note 5		

Note 1. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

Note 2. Definition of "Viewing Angle" (This example panel is UP068D01.):



Note 3: Definition of "Contrast Ratio":

Contrast ratio is calculated with the following formula.

Photodetector output when LCD is at "White" state

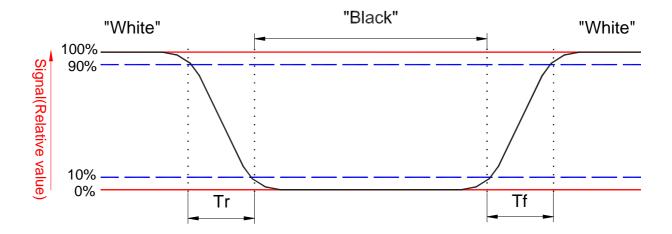
Contrast ratio (CR)=

Photodetector output when LCD is at "Black" state

Note 4: Definition of "Response Time":

The output signals of photodetector are measured when the input signals are changed from "black" to "white (falling time) and from "white to "black" (rising time), respectively.

The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.

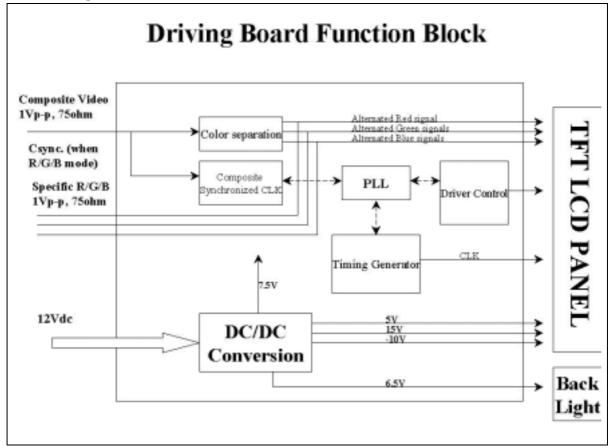


Note 5: Please check the Appendix II-UP040D01 COLOR TFT-LCD PRELIMINARY SPECIFICATION (SPEC. NO. 233-220-075)

C-2. Driving board Electrical Specification

Item	Specification	Remark
Decoding TV system	NTSC/PAL auto-switching	
Video Input	1Vp-p, 75Ω	
Video Interface	Standard Composite Video, Analog R/G/B	
Video Direct Selection	Up/Down scanning, Left/Right scanning	
OSD(On Screen Display) Functions	Brightness/Contrast/Color/Tint/Sharpness	
DC Voltage Input	12 V ± 2V	
Power Consumption	< 3 Watt	Including 4' panel
Operating Temperature	0°C ~60°C	
Storage Temperature	- 20°C ~ 80°C	

C-3. Driving board function block



D. Pin Assignment & Interface description

D-1. LCD panel driving section (FPC 26pin)

Pin no.	Symbol	i/o	Description	Remark
1	GND	-	Ground for logic circuit	
2	VCC	I	Supply voltage for control circuit of scan driver	
3	VGL	I	Negative power for scan driver	
4	VGH	I	Positive power for scan driver	
5	STVR	I/O	Vertical start pulse	Note 1
6	STVL	I/O	Vertical start pulse	Note 1
7	CKV	I	Shift clock input for scan driver	
8	U/D	I	UP/DOWN scan control input	Note 1, 2
9	OEV	I	Output enable input for scan driver	
10	VCOM	I	Common electrode driving signal	
11	VCOM	I	Common electrode driving signal	
12	L/R	I	LEFT/RIGHT scan control input	Note 1,2
13	Q1H	I	Analog signal rotate input	
14	OEH	I	Output enabler input for data driver	
15	STHL	I/O	Start pulse for horizontal scan line	Note 1
16	STHR	I/O	Start pulse for horizontal scan line	Note 1
17	СРН3	I	Sampling and shifting clock pulse for data driver	
18	CPH2	I	Sampling and shifting clock pulse for data driver	
19	CPH1	I	Sampling and shifting clock pulse for data driver	
20	VCC	I	Supply voltage of control circuit for data driver	
21	GND	-	Ground for logic circuit	
22	VR	I	Alternated video signal input (Red)	
23	VG	I	Alternated video signal input (Green)	
24	VB	I	Alternated video signal input (Blue)	
25	AVDD	I	Supply voltage for analog circuit	
26	AVSS	-	Ground for analog circuit	

Note 1 : Selection of scanning mode – refer to page 5 of UP040D01 spec.

Note 2 : Definition of scanning direction – refer to page 5 of UP040D01 spec.

D-2. CCFL High Voltage

Pink	High Voltage Line	
White	Ground Line	

D-3. Driving board

D-3-1. Pin assignment of User Control Interface on driving board:

Pin	Pin	I/O	Pin Description	Remark
No.	Assignment		•	
1	/Hsync	O	Horizontal Sync. Output for specific Application.	
2	/Vsync	О	Vertical Sync. Output for specific Application.	
3	NPC	I/O	NTSC/PAL mode selection.	Note 1
4	SW	I	Composite/Analog RGB Signal Selection.	Note 2
5	CVS	I	Composite Video Signal Input.	
6	Bin	I	Separated Blue Video Signal Input.	
7	Rin	I	Separated Red Video Signal Input.	
8	Gin	I	Separated Green Video Signal Input.	
9	GND	-	Ground	
10	GNDS	-	Ground	
11	Vin	I	12 Voltage DC Input	
12	GNDS	-	Ground	
13	COL	I	Color terminal adjustment	
14	LR	I	Left/Right scan selection	Note 3
15	UD	I	Up/Down scan selection	Note 4

Default means Factory Setting.

Note 1: Default is auto detect for NTSC and PAL system.

(Controlled by internal UPS015)

High for NTSC mode and Low for PAL mode.

Note 2: Default is Composite Signal (Low);

High is for separated Analog RGB signals.

Note 3: Default is reversed scanning (High) and Low is for normal scanning ($L \rightarrow R$)

Please do force this pin to ground for normal scan, from left to right.

Note 4: Default is reversed scanning (High) and Low is for normal scanning ($U \rightarrow D$)

Please do force this pin to ground for normal scan from up to down.

D-3-2. User's control function:

VR	Description
BRT	Brightness
COL	Hue
TIN	Tint
CNT	Contrast
PIC	Sharpness

D-3-3. Updated Information of "PMPCB015-Ver0. 5"

The Ver 0.5 increases many useful interfaces for **OSD control function** to all customers!

Described as below:

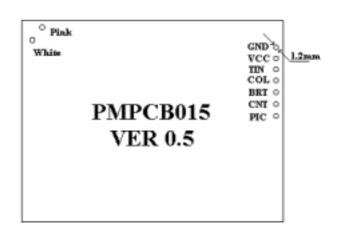
Pin Assignment	Minimum	Typical	Maximum	Remark
TIN	3V	3.3V	4V	Note 1
COL	2.6V	3.2V	4V	
BRT	2.05V	2.11V	2.16V	
CNT	2.13V	3.03V	3.13V	Note 2
PIC	1.0V	1.5V	2.5V	

Note 1: The TIN is only operating in NTSC system.

Note 2: We don't suggest to adjust" CNT' from Variable resistor on the driving board.

Note: VCC Spec. \rightarrow +5V(\pm 3%)/20mA

Location as below:



Appendix I: Mock-up molding drawing

