

## SILICON RFIC HI-IP3 FREQUENCY UP-CONVERTER FOR WIRELESS TRANSCEIVER

## **UPC8187TB**

#### **FEATURES**

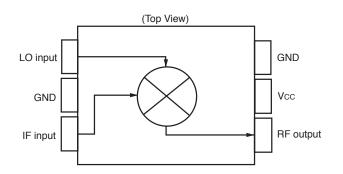
- HIGH OUTPUT FREQUENCY: fRFout = 0.8 to 2.5 GHz
- SUPPLY VOLTAGE: VCC = 2.7 to 3.3 V
- HIGH IP3 AND CONVERSION GAIN:
   OIP3 = +10 dBm typ at fRFout = 0.9 GHz
   CG = +11 dBm typ at fRFout = 0.9 GHz
- HIGH-DENSITY SURFACE MOUNTING: 6-pin super minimold package

#### DESCRIPTION

NEC's UPC8187TB is a silicon monolithic integrated circuit designed as a frequency up-converter for wireless transceivers. This IC has higher operating frequency, lower distortion and higher conversion gain than the conventional UPC8163TB. This device is manufactured using NEC's 30 GHz f<sub>max</sub> UHS0 (Ultra High Speed Process) silicon bipolar process.

NEC's stringent quality assurance and test procedures ensure the highest reliability and performance.

#### **BLOCK DIGRAM**



#### **APPLICATIONS**

- · TDMA, PCS, CDMA
- · Digital Cellular/Cordless Phones
- Wireless Tranceivers

#### **ELECTRICAL CHARACTERISTICS**

(TA = 25°C, VCC = VRFOUT = 2.8 V, fIFin = 150 MHz, PLOin = -5 dBm)

PART NUMBER PACKAGE OUTLINE							UPC8187TB S06		
SYMBOLS	ı	PARAMET	ERS AND COND	UNITS	MIN	TYP	MAX		
Icc	Circuit Current (no signal)					11	15	19	
CG1		fRFout = 0.	83 GHz, PIFin = -2	20 dBm	dB	8	11	14	
CG2	Conversion Gain,	fRFout = 1.	9 GHz, PIFin = -20	) dBm	dB	8	11	14	
CG3		fRFout = 2.	4 GHz, PIFin = -20	) dBm	dB	7	10	13	
Po(SAT)1			fRFout = 0.83 GH	z, PıFin = 0 dBm	dBm	+1.5	+4	-	
Po(SAT)2	Saturated RF Output Power,		fRFout = 1.9 GHz	, PIFin = 0 dBm	dBm	0	+2.5	_	
Po(SAT)3			fRFout = 2.4 GHz	, PIFin = 0 dBm	dBm	-1.5	+1	_	
	Output Third-Order Distortion Intercept Point,								
OIP <sub>3</sub> 1		fRFout = 0.8	83 GHz	fIFin1 = 150 MHz	dBm	_	10	_	
OIP <sub>3</sub> 2		fRFout = 1.9	9 GHz	fIFin2 = 151 MHz	dBm	_	10	_	
OIP33		fRFout = 2.4	4 GHz		dBm	_	8.5	_	
	Input Third-Order I	Distortion I	ntercept Point,						
IIP31		fRFout = 0.8	33 GHz	fIFin1 = 150 MHz	dBm	_	-1.0	_	
IIP32	fRFout = 1.9 GHz		fıFin2 = 151 MHz	dBm	_	-1.0	_		
IIP33	fRFout = 2.4 GHz				dBm	-	-1.5	_	
SSB•NF1		fRFout	= 0.83 GHz		dB	-	11	_	
SSB•NF2	SSB Noise Figure,	fRFout	= 1.9 GHz	fiFin1 = 150 MHz	dB	_	12	_	
SSB•NF3		fRFout	= 2.4 GHz		dB	_	12.5	_	

#### Note:

1. fRFout < fLOin @ fRFout = 0.83 GHz fLOin < fRFout @ fRFout = 1.9 GHz/2.4 GHz

#### **ABSOLUTE MAXIMUM RATINGS<sup>1</sup>**

(TA = +25°C unless otherwise specified)

(TA = T25 G difference openined)					
SYMBOLS	PARAMETERS	UNITS	RATINGS		
Vcc	Supply Voltage	V	3.6		
PD	Power Dissipation <sup>2</sup>	mW	270		
Та	Operating Ambient Temperature	°C	-40 to +85		
Тѕтс	Storage Temperature	°C	-55 to +150		
Pin	Maximum Input Power	dBm	+10		

#### Notes:

- Operation in excess of any one of these conditions may result in permanent damage.
- Mounted on a double-sided copper clad 50x50x1.6 mm epoxy glass PWB, TA = +85°C.

# RECOMMENDED OPERATING CONDITIONS

SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
Vcc	Supply Voltage <sup>1</sup>	V	2.7	2.8	3.3
Та	Operating Ambient Temperature	°C	-40	+25	+85
PLOin	Local Input Level <sup>2</sup>	dBm	-10	-5	0
fRFout	RF Output Frequency <sup>3</sup>	GHz	0.8	-	2.5
fIFin	IF Input Frequency	MHz	50	_	400

#### Notes:

- 1. Same voltage applied to pins 5 and 6.
- 2.  $Zs = 50 \Omega$  (without matching).
- 3. With external matching circuit.

## SERIES PRODUCTS<sup>1</sup> (TA = +25°C, VCC = VPS = VRFout = 3.0 V, ZS = ZL = $50 \Omega$ )

Part Number	Icc	fRFout	CG (dB		OIP3 (dBm)		Po(sat)				
	(mA)	(GHz)	@RF 0.9 GHz <sup>2</sup>	@RF 1.9 GHz	@RF 2.4 GHz	@RF 0.9 GHz <sup>2</sup>	@RF 1.9 GHz	@RF 2.4 GHz	@RF 0.9 GHz <sup>2</sup>	@RF 1.9 GHz	@RF 2.4 GHz
UPC8187TB	15	0.8 to 2.5	11	11	10	+10	+10	+8.5	+4	+2.5	+1
UPC8106TB	9	0.4 to 2.0	9	7	_	+5.5	+2.0	-	-2	-4	_
UPC8172TB	9	0.8 to 2.5	9.5	8.5	8.0	+7.5	+6.0	+4.0	+0.5	0	-0.5
UPC8109TB	5	0.4 to 2.0	6	4	-	+1.5	-1.0	_	-5.5	-7.5	-
UPC8163TB	16.5	0.8 to 2.0	9	5.5	_	+9.5	+6.0	-	+0.5	-2	_

#### Notes:

- 1. Typical performance.
- 2. fRFout = 0.83 GHz @ UPC8163TB and UPC8187TB.

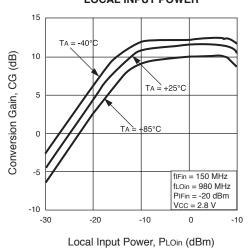
#### PIN FUNCTIONS (Pin Voltage is measured at Vcc = Vps = VRFOUT = 2.8V)

Pin No.	Pin Name	Applied Voltage (V)	Pin Voltage (V)	Function and Explanation	Equivalent Circuit
1	IFinput	_	1.2	This pin is the IF input pin to the double balanced mixer (DBM). The input is designed as a high impedance. The circuit helps suppress spurious signals. Also this symmetrical circuit can keep specified performance insensitive to process-condition distribution. For that reason, a double balanced mixer is adopted.	(5) (6)
2 4	GND	GND	_	GND pin. Ground pattern on the board should be formed as wide as possible. Track length should be kept as short as possible to minimize ground inductance.	
3	LOinput	_	2.1	Local input pin. Recommended input level is -10 to 0 dBm.	
5	Vcc	2.7 to 3.3	_	Supply voltage pin.	2
6	RFoutput	Same bias as Vcc through external inductor	_	This pin is the RF output from the double balanced mixer. This pin is designed as an open collector. Due to the high impedance output, this pin should be externally equipped with an LC matching circuit to the next stage.	

## TYPICAL PERFORMANCE CURVES (Unless otherwise specified, TA = 25°C)

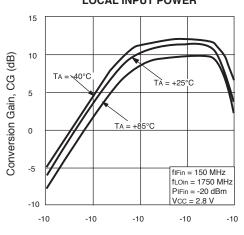
fRFout = 0.83 GHz

#### **CONVERSION GAIN vs. LOCAL INPUT POWER**



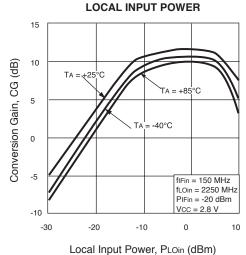
#### fRFout = 1.9 GHz

#### **CONVERSION GAIN vs. LOCAL INPUT POWER**

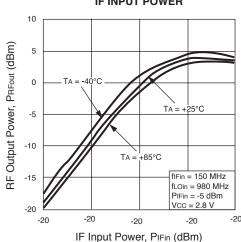


## Local Input Power, PLOin (dBm)

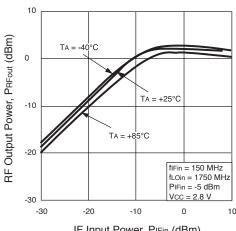
#### fRFout = 2.4 GHz**CONVERSION GAIN vs.**



#### RF OUTPUT POWER vs. IF INPUT POWER

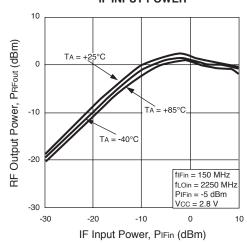


#### RF OUTPUT POWER vs. IF INPUT POWER

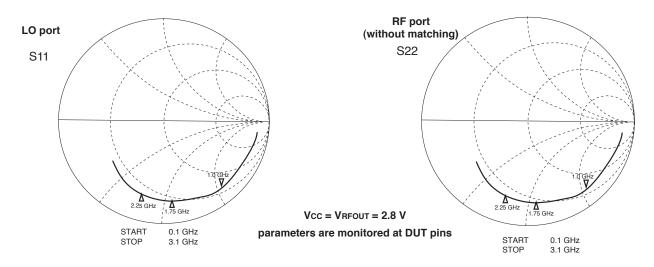


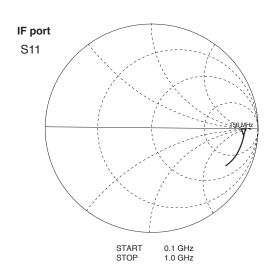
IF Input Power, PIFin (dBm)

#### RF OUTPUT POWER vs. IF INPUT POWER

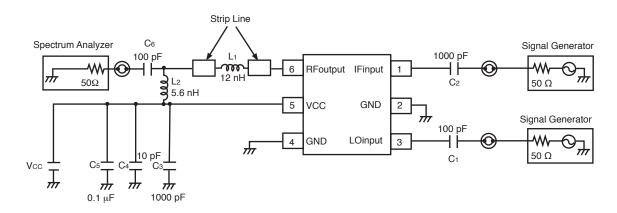


## TYPICAL SCATTERING PARAMETERS (TA = 25°C)

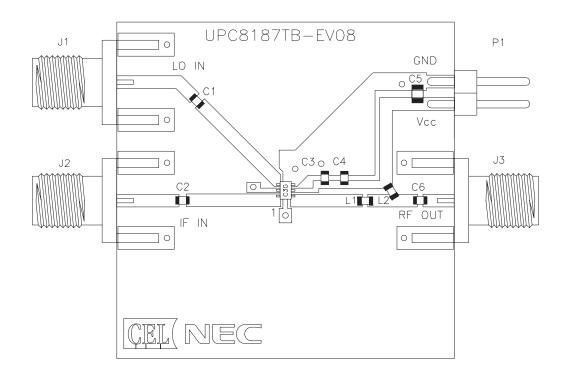




## TEST CIRCUIT 1 (fRFout = 0.83 GHz)



#### ILLUSTRATION OF THE TEST CIRCUIT 1 ASSEMBLED ON EVALUATION BOARD

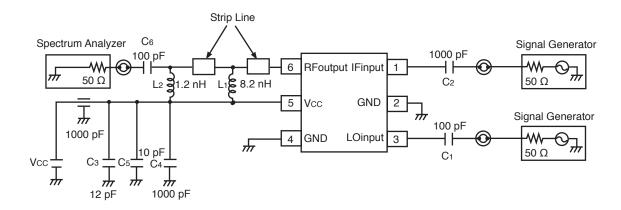


#### **COMPONENT LIST**

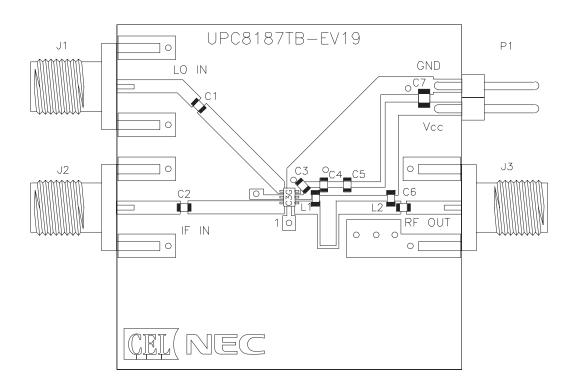
FORM	SYMBOL	VALUE
Chip Capacitor	C1, C6	100 pF
	C4	10 pF
	C2, C3	1000 pF
	C5	0.1μF
Chip Inductor	L <sub>1</sub>	12 nH
	L2	5.6nH

- 1. 1.5 x 1.5 x 0.028", Getek laminate, double sided copper
- 2. Ground pattern on rear board
- 3. Solder plated patterns
- 4. OThrough holes

## TEST CIRCUIT 2 (fRFout = 1.9 GHz)



## **ILLUSTRATION OF TEST CIRCUIT 2 ASSEMBLED ON EVALUATION BOARD**

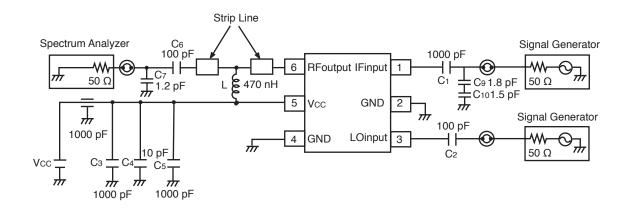


#### **COMPONENT LIST**

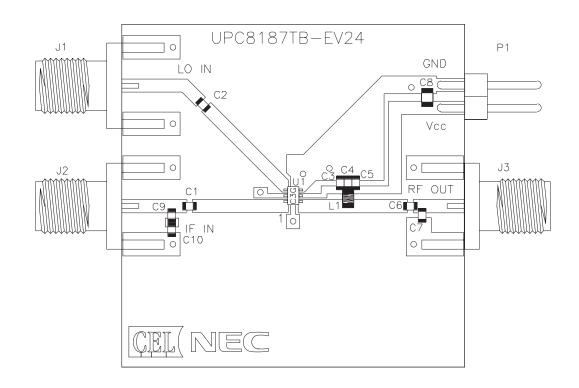
FORM	SYMBOL	VALUE
Chip Capacitor	C1, C2, C4	1000 pF
	C7	0.1μF
	C6	100 pF
	Сз	12 pF
	C5	10 pF
Chip Inductor	L1	8.2 nH
	L2	1.2 nH

- 1. 1.5 x 1.5 x 0.028", Getek laminate, double sided copper
- 2. Ground pattern on rear board
- 3. Solder plated patterns
- 4. OThrough holes

#### **TEST CIRCUIT 3** (fRFout = 2.4 GHz)



#### **ILLUSTRATION OF TEST CIRCUIT 3 ASSEMBLED ON EVALUATION BOARD**



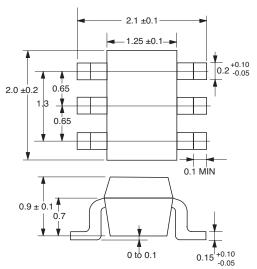
#### **COMPONENT LIST**

FORM	SYMBOL	VALUE
Chip Capacitor	C1, C3, C5	1000 pF
	C2, C6	100 pF
	C4	10 pF
	C7	1.2 pF
	C9	1.8 pF
	C10	1.5 pF
Chip Inductor	L	470 nH

- 1. 1.5 x 1.5 x 0.028", Getek laminate, double sided copper
- 2. Ground pattern on rear board
- 3. Solder plated patterns
- 4. O Through holes

#### **OUTLINE DIMENSIONS** (Units in mm)

#### **PACKAGE OUTLINE S06**



Note:

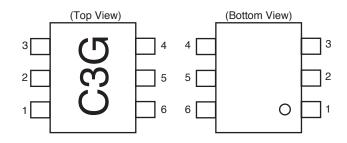
All dimensions are typical unless otherwise specified.

#### ORDERING INFORMATION

Part Number	Quantity
UPC8187TB-E3-A	3 K pcs/reel

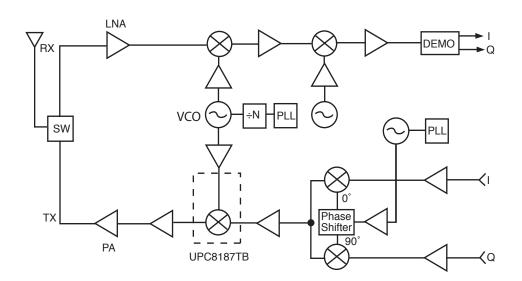
Note: Embossed tape, 8 mm wide. Pins 1, 2 and 3 face the tape perforation side.

#### **PIN CONNECTIONS**



PIN NO.	PIN NAME
1	<b>IF</b> input
2	GND
3	LOinput
4	GND
5	Vcc
6	RFoutput

### SYSTEM APPLICATION EXAMPLE (Schematic of IC location in the system)



Life Support Applications

These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.





Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (\*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices	
Lead (Pb)	< 1000 PPM	-A -AZ Not Detected (*)	
Mercury	< 1000 PPM	Not Detected	
Cadmium	< 100 PPM	Not Detected	
Hexavalent Chromium	< 1000 PPM	Not Detected	
PBB	< 1000 PPM	Not Detected	
PBDE	< 1000 PPM	Not Detected	

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

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