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Silicon N Channel MOS FET High Speed Power Switching



ADE-208-520 (Z) 1st. Edition Jun 1997

Features

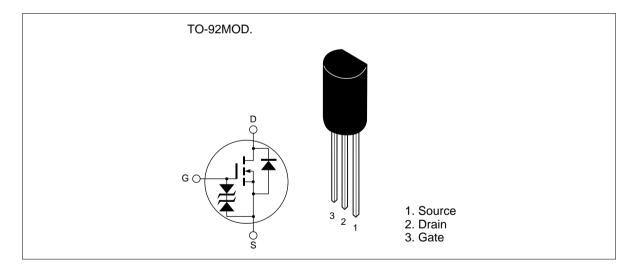
Low on-resistance

 $R_{\text{DS(on)}}$ = 0.04 Ω typ (at V_{GS} = 10 V, I_{D} = 2.5 A)

- 4V gate drive devices.
- Large current capacitance

 $I_D = 5 A$

Outline



Absolute Maximum Ratings (Ta = 25° C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	30	V	
Gate to source voltage	V _{GSS}	±20	V	
Drain current	I _D	5	А	
Drain peak current	↓ D(pulse) *1	20	А	
Body to drain diode reverse drain current	I _{DR}	5	А	
Channel dissipation	Pch	0.9	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Note: 1. $PW \le 10\mu s$, duty cycle $\le 1 \%$

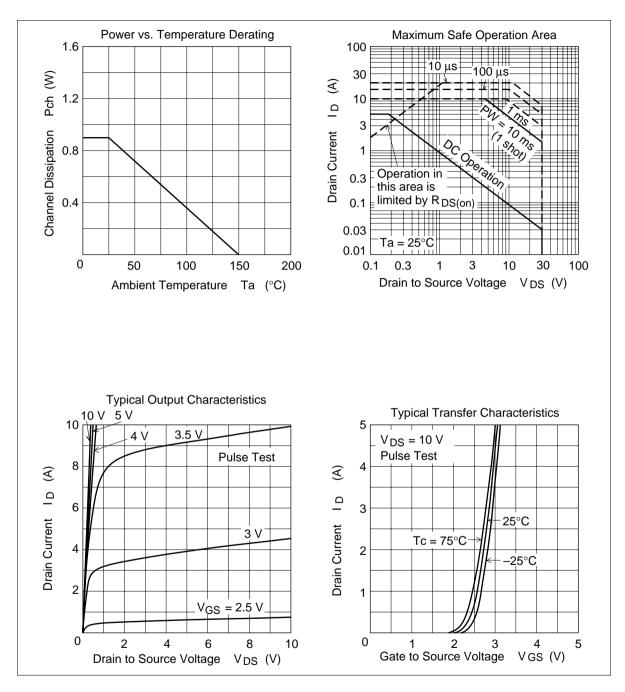
Electrical Characteristics (Ta = 25°C)

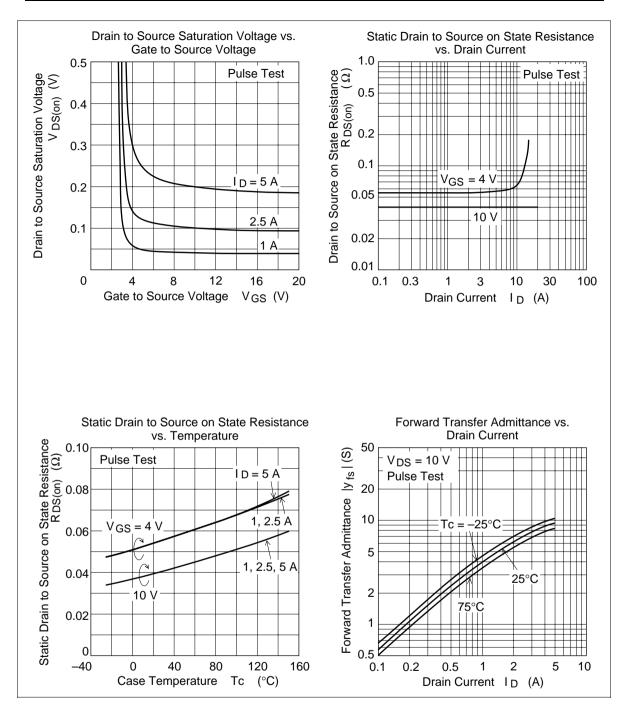
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	—	_	V	$I_{\rm D} = 10 {\rm mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	—	_	V	$I_{g} = \pm 100 \mu A, V_{DS} = 0$
Zero gate voltege drain current	I _{DSS}	_		10	μΑ	$V_{\rm DS} = 30$ V, $V_{\rm GS} = 0$
Gate to source leak current	I _{GSS}	—		±10	μΑ	$V_{GS} = \pm 16V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0		2.0	V	$I_{\rm D} = 1$ mA, $V_{\rm DS} = 10$ V
Static drain to source on state	$R_{DS(on)}$	_	0.04	0.055	Ω	$I_{\rm D} = 2.5 \text{A}, V_{\rm GS} = 10 \text{V}^{*1}$
resistance	R _{DS(on)}	—	0.055	0.08	Ω	$I_{\rm D} = 2.5 \text{A}, V_{\rm GS} = 4 V^{*1}$
Forward transfer admittance	y _{fs}	4	7	—	S	$I_{\rm D} = 2.5 \text{A}, V_{\rm DS} = 10 \text{V}^{*1}$
Input capacitance	Ciss	_	550	_	pF	V _{DS} = 10V
Output capacitance	Coss	—	380	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	155	—	pF	f = 1MHz
Turn-on delay time	t _{d(on)}	—	14	—	ns	$V_{GS} = 10V, I_{D} = 2.5A$
Rise time	t _r	_	80	—	ns	$R_{L} = 4\Omega$
Turn-off delay time	t _{d(off)}	—	80	—	ns	
Fall time	t _f	_	65	_	ns	
Body to drain diode forward voltage	V_{DF}	_	1.0	_	V	$I_{F} = 5A, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	_	40	_	ns	$I_F = 5A$, $V_{GS} = 0$ $di_F/ dt = 50A/\mu s$
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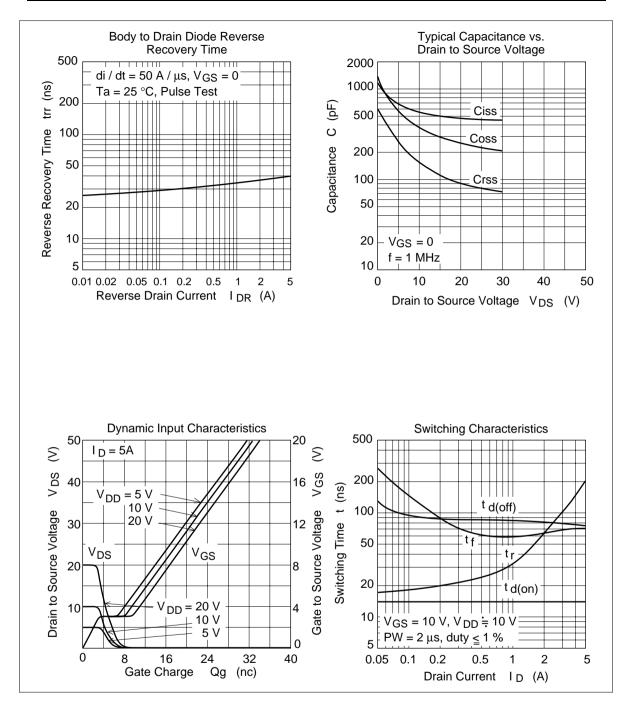
Note: 1. Pulse test

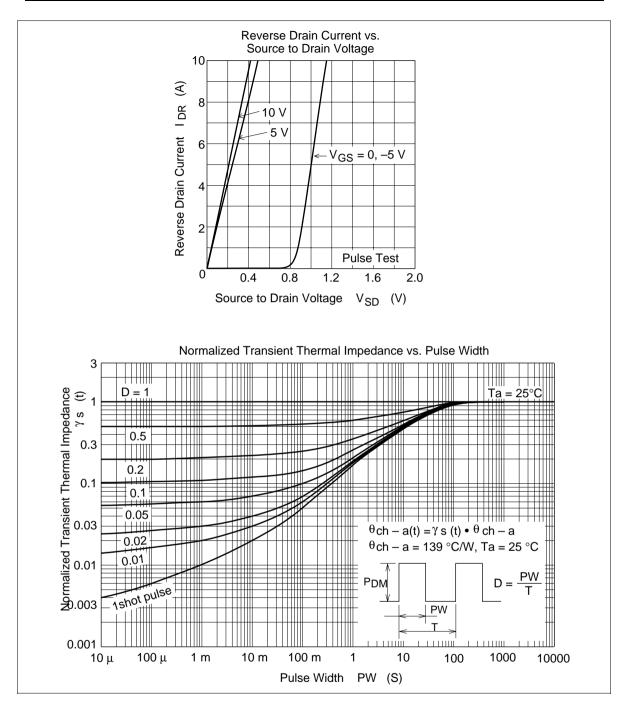


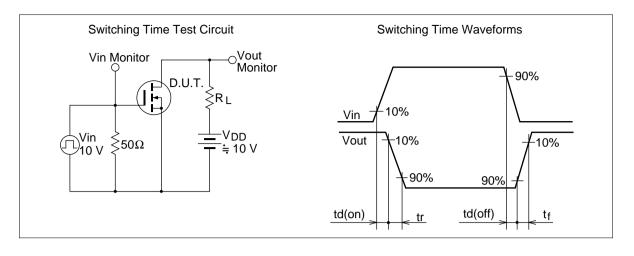
Main Characteristics



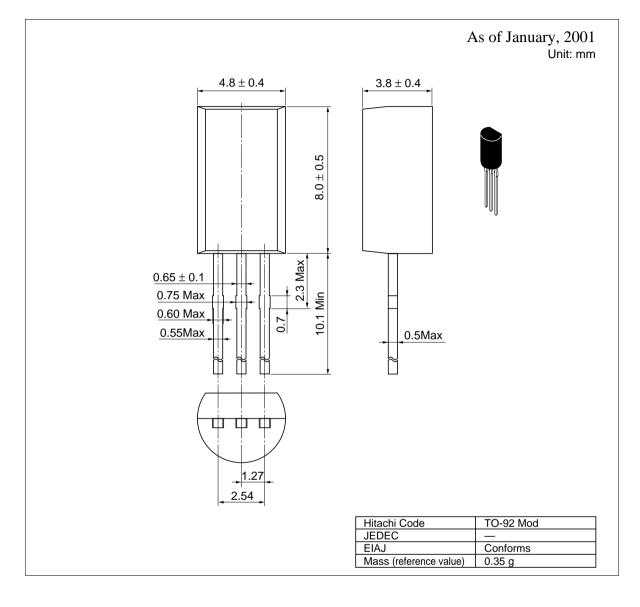








Package Dimensions



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