Silicon N Channel DV–L MOS FET High Speed Power Switching



ADE-208-544 1st. Edition

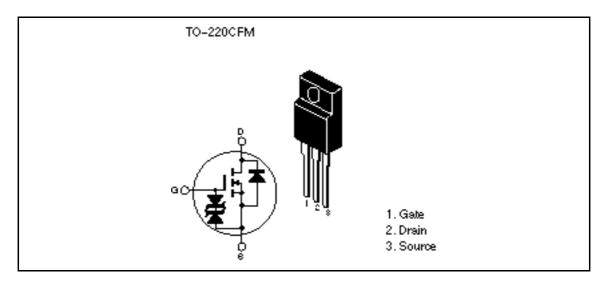
Features

Low on-resistance

 $R_{DS(on)} = 20 \text{ m}$ typ. ($V_{GS} = 10V$, $I_D = 15 \text{ A}$)

- 4V gate drive devices.
- High speed switching

Outline





Absolute Maximum Ratings (Ta = 25° C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V _{DSS}	30	V	
Gate to source voltage	V _{GSS}	±20	V	
Drain current	I _D	30	A	
Drain peak current	L _{D(pulse)} *1	120	А	
Body to drain diode reverse drain current	I _{DR}	30	А	
Channel dissipation	Pch*2	25	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1. PW 10µs, duty cycle 1 %

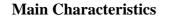
2. Value at $Tc = 25^{\circ}C$

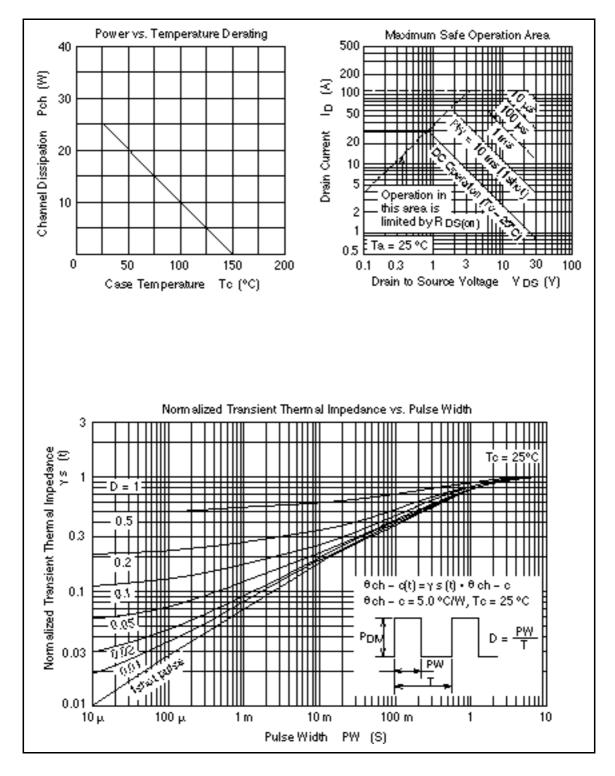
Electrical Characteristics (Ta = 25°C)

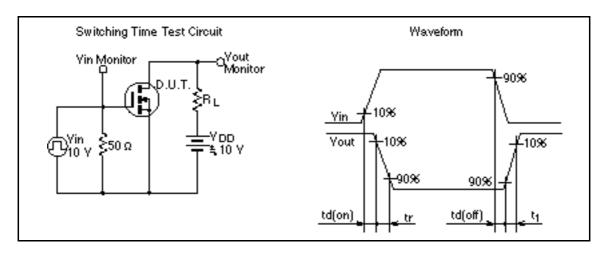
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	30	_	—	V	$I_{\rm D} = 10 {\rm mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(\text{BR})\text{GSS}}$	±20	_	—	V	$I_{G} = \pm 100 \mu A, V_{DS} = 0$
Zero gate voltege drain current	I _{DSS}	_	—	10	μA	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0$
Gate to source leak current	I _{GSS}		_	±10	μA	$V_{GS} = \pm 16V, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.0	V	$I_{\rm D} = 1$ mA, $V_{\rm DS} = 10$ V
Static drain to source on state	R _{DS(on)}		20	28	m	$I_{D} = 15A, V_{GS} = 10V^{*1}$
resistance	R _{DS(on)}		35	50	m	$I_{D} = 15A, V_{GS} = 4V^{*1}$
Forward transfer admittance	y _{fs}	12	18	_	S	$I_{\rm D} = 15$ A, $V_{\rm DS} = 10$ V ^{*1}
Input capacitance	Ciss	_	750	_	pF	$V_{\rm DS} = 10V$
Output capacitance	Coss	—	520	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	210	_	pF	f = 1MHz
Turn-on delay time	t _{d(on)}	—	16	—	ns	$V_{GS} = 10V, I_{D} = 15A$
Rise time	t,	_	260	—	ns	$R_{L} = 0.67$
Turn-off delay time	t _{d(off)}	_	85	_	ns	
Fall time	t _f	_	90	_	ns	
Body to drain diode forward voltage	V_{DF}	_	1.0	—	V	$I_{F} = 30A, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}		45	—	ns	$I_F = 30A$, $V_{GS} = 0$ diF/ dt = 50A/µs
Note: 1 Pulse test						

Note: 1. Pulse test

See characteristics curves of 2SK2684

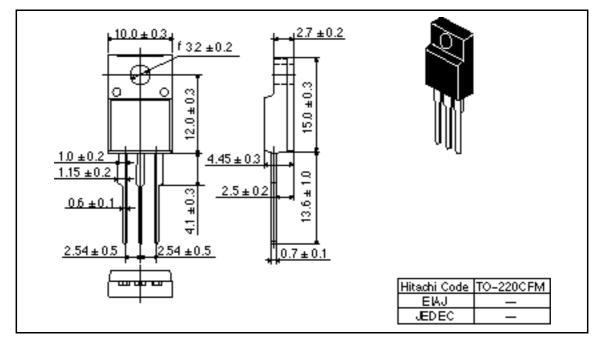






Package Dimensions





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