REFERENCE DESIGN ERRATA PMC-1980370

ISSUE 2

PM5350 S/UNI-ULTRA

SUNI-ULTRA REFERENCE DESGIN

PM5350

S/UNI-ULTRA

REFERENCE DESIGN ERRATA

ISSUE 2: DEC. 2000

CONTENTS

1	NEW ISSUE OF THE S/UNI-ULTRA REFERENCE DESIGN ERRATA	1
2	SUMMARY OF CHANGES TO S/UNI-ULTRA REFERENCE DESIGN	2
	2.1 RESISTORS R6 AND R7	2
	2.2 TVREF SIGNAL	2
	2.3 RAVD2 CIRCUITRY	2
	2.4 FERRITE BEADS	2
	2.5 TAVD4 CIRCUITRY	2



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SUNI-ULTRA REFERENCE DESGIN

1 NEW ISSUE OF THE S/UNI-ULTRA REFERENCE DESIGN ERRATA

This document is the errata notice for Issue 3 of the S/UNI-ULTRA Reference Design (document number PMC-1961062).

2 SUMMARY OF CHANGES TO S/UNI-ULTRA REFERENCE DESIGN

2.1 Resistors R6 and R7

The values for resistors R6 and R7 in the BOM and schematics of the reference design issue 3 are not the same. The schematics show the correct values:

R6 = 929 Ω

R7 = 1.3 kΩ

2.2 TVREF Signal

The TVREF pin on page 4 of the schematics is shown to be unconnected (floating). This signal should be tied to ground. Since we recommend only one ground plane, TVREF should be just grounded.

2.3 RAVD2 Circuitry

The RAVD2 circuit on page 5 of the schematics does not show the MOSFET that is required as per the data sheet errata. For the correct circuitry, please refer to the page 6 of PMC-199107 issue 3.

Also, in Figure 13 of the reference design, RAVD2 should be removed from the list of analog pins to be decoupled and replaced with TAVD3.

In Figure 14 of the reference design, the 0.1uF capacitor on the left side of MOSFET should be replaced with a 68uF capacitor.

2.4 Ferrite Beads

The circuits for TAVD3, TAVD4, QAVD1 and QAVD2 on the page 5 of the schematics show ferrite beads. We no longer recommend ferrite beads. The ferrite beads can be replaced with 1 or 2.7 Ω resistors.

2.5 TAVD4 Circuitry

The TAVD4 pin needs a 68uF capacitor added on the device side of the resistor.

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PM5350 S/UNI-ULTRA

ISSUE 2

SUNI-ULTRA REFERENCE DESGIN

<u>NOTES</u>



PM5350 S/UNI-ULTRA

SUNI-ULTRA REFERENCE DESGIN

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