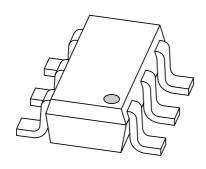
## **DISCRETE SEMICONDUCTORS**

## DATA SHEET



# **1PS74SB23**Schottky barrier diode

Product specification Supersedes data of 1999 Apr 26 2001 Aug 27





1PS74SB23

#### **FEATURES**

- · Ultra fast switching speed
- · Low forward voltage
- · Fast recovery time
- · Guard ring protected
- Small plastic SMD package
- Capability of absorbing very high surge current.

### **APPLICATIONS**

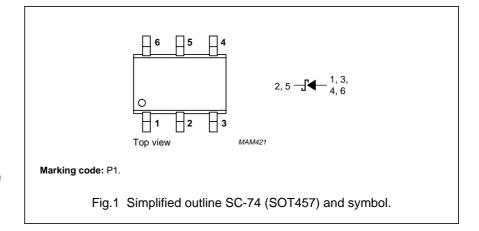
- Rectification
- · Circuit protection
- · Polarity protection
- Switched-mode power supplies.

## **DESCRIPTION**

Planar Schottky barrier diode encapsulated in an SC-74 (SOT457) small plastic SMD package.

#### **PINNING**

PIN	DESCRIPTION
1	anode
2	cathode
3	anode
4	anode
5	cathode
6	anode



#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>R</sub>	continuous reverse voltage		_	25	V
I <sub>F</sub>	continuous forward current		_	1	Α
I <sub>FSM</sub>	non-repetitive peak forward current	t <sub>p</sub> = 8.3 ms; half sinewave; JEDEC method; note 1	_	25	А
I <sub>RSM</sub>	non-repetitive peak reverse current	t <sub>p</sub> = 100 μs	_	0.5	Α
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	125	°C

#### Note

1. Pins 1, 3, 4 and 6 are connected in parallel; pins 2 and 5 are connected in parallel.

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## Schottky barrier diode

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## **ELECTRICAL CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 100 mA	260	300	mV
		I <sub>F</sub> = 1 A	400	450	mV
I <sub>R</sub>	reverse current	V <sub>R</sub> = 20 V; note 1; see Fig.3	80	500	μΑ
		V <sub>R</sub> = 25 V; note 1; see Fig.3	_	1	mA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 4 V; see Fig.4	100	_	pF

## Note

1. Pulse test:  $t_p$  = 300  $\mu$ s;  $\delta$  = 0.02.

## THERMAL CHARACTERISTICS

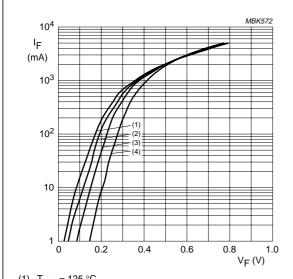
SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	250	K/W

#### Note

1. Refer to SC-74 (SOT457) standard mounting conditions.

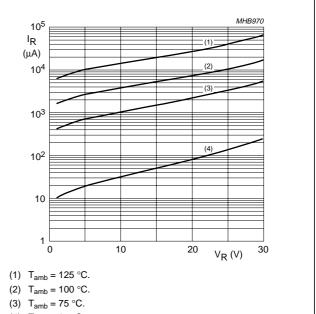
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## **GRAPHICAL DATA**



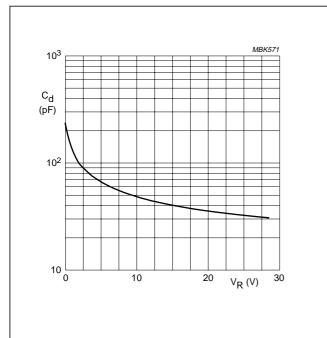
- (1)  $T_{amb} = 125 \,^{\circ}C$ .
- (2)  $T_{amb} = 100 \, ^{\circ}C$ .
- (3)  $T_{amb} = 75 \, ^{\circ}C$ .
- (4)  $T_{amb} = 25 \, ^{\circ}C$ .

Fig.2 Forward current as a function of forward voltage; typical values.



(4)  $T_{amb} = 25 \, ^{\circ}C$ .

Fig.3 Reverse current as a function of reverse voltage; typical values.



f = 1 MHz;  $T_{amb} = 25$  °C.

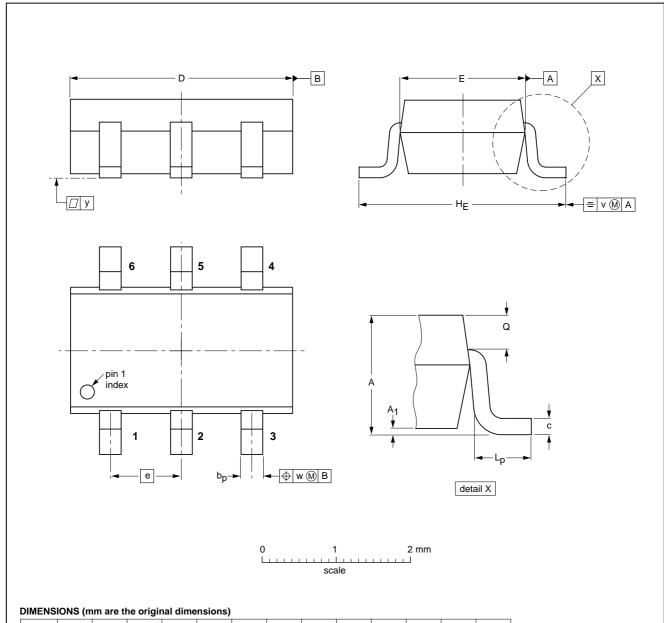
Fig.4 Diode capacitance as a function of reverse voltage; typical values.

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## **PACKAGE OUTLINE**

## Plastic surface mounted package; 6 leads

**SOT457** 



UI	NIT	A	A <sub>1</sub>	bp	С	D	E	е	HE	Lp	Q	v	w	у
m	nm	1.1 0.9	0.1 0.013	0.40 0.25	0.26 0.10	3.1 2.7	1.7 1.3	0.95	3.0 2.5	0.6 0.2	0.33 0.23	0.2	0.2	0.1

OUTLINE		REFER	EUROPEAN ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE
SOT457			SC-74		<del>97-02-28</del> 01-05-04

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#### **DATA SHEET STATUS**

DATA SHEET STATUS(1)	PRODUCT STATUS <sup>(2)</sup>	DEFINITIONS
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification.  Supplementary data will be published at a later date. Philips  Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Changes will be communicated according to the Customer Product/Process Change Notification (CPCN) procedure SNW-SQ-650A.

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