HN62W5432N Series

1048576-word × 32-bit/2097152-word × 16-bit CMOS Mask Programmable ROM

HITACHI

Under development

Description

The HN62W5432N is a 32 Mbit CMOS Mask programmable ROM organized either as 1,048,576 word by 32 bit or as 2,097,152 word by 16 bit. Realizing low power consumption, this memory is allowed for batery operation. And high speed access of 100/120 ns is the most suitable to the system using a high speed micro-computer by 32 bits.

Features

- Low voltage operation: 3.3 V ± 0.3 V
- High speed
  Normal access time: 100/120 ns
  Page access time: 40/50 ns
- Low power consumption
  Active: 120 mA (max)
  Standby: 200 µA (max)
- Double word-wide or word-wide data organization with DW/W
- 4 double-word page access on double word-wide mode 8 word page access on word-wide mode
- Three-state data output for or-tying
- LVTTL compatible

Ordering Information

Type No.	Access time	Package
HN62W5432NF-10	100 ns	70-pin plastic SSOP (FP-70DS)
HN62W5432NF-12	120 ns	

Note: This document contains information on a product under development. Hitachi reserves the right to change or discontinue the product without notice.