

NPN SILICON RF POWER TRANSISTOR

DESCRIPTION:

The **ASI AVD550** is Designed for Class C, DME/TACAN Applications up to 1150 MHz.

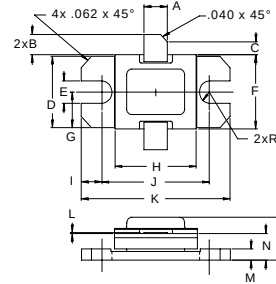
FEATURES:

- Internal Input/Output Matching Networks
- $P_G = 5.6$ dB at 550 W/1150 MHz
- **Omnigold™** Metalization System

MAXIMUM RATINGS

I_C	40 A
V_{CC}	55 V
P_{DISS}	1350 W @ $T_C \leq 80^\circ C$
T_J	$-65^\circ C$ to $+250^\circ C$
T_{STG}	$-65^\circ C$ to $+200^\circ C$
θ_{JC}	0.06 $^\circ C/W$

PACKAGE STYLE .400 2L FLG (A)



DIM	MINIMUM inches / mm	MAXIMUM inches / mm
A	.135 / 3.43	.145 / 3.68
B	.100 / 2.54	.120 / 3.05
C	.050 / 1.27	
D	.376 / 9.55	.396 / 10.06
E	.110 / 2.79	.130 / 3.30
F	.395 / 10.03	.407 / 10.34
G	.193 / 4.90	
H	.490 / 12.45	.510 / 12.95
I	.100 / 2.54	
J	.690 / 17.53	.710 / 18.03
K	.890 / 22.61	.910 / 23.11
L	.003 / 0.08	.006 / 0.18
M	.052 / 1.32	.072 / 1.83
N	.118 / 3.00	.131 / 3.33
P		.230 / 5.84

ORDER CODE: ASI10568

CHARACTERISTICS $T_C = 25^\circ C$

SYMBOL	TEST CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
BV_{CBO}	$I_C = 25$ mA	65			V
BV_{CER}	$I_C = 50$ mA $R_{BE} = 10 \Omega$	65			V
BV_{EBO}	$I_E = 5.0$ mA	3.5			V
I_{CES}	$V_{CE} = 50$ V			60	mA
h_{FE}	$V_{CE} = 5.0$ V $I_C = 2.0$ A	10		250	---
P_G η_c	$V_{CC} = 50$ V $P_{OUT} = 550$ W $f = 1025 - 1150$ MHz	5.6 40			dB %