

Preliminary

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC7SB66FU

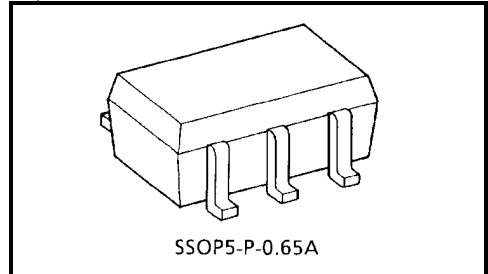
Single Bus Switch

The TC7SB66FU is a low on-resistance, high-speed CMOS single-bit bus switch. This bus switch allows the connections or disconnections to be made with minimal propagation delay while maintaining Low power dissipation which is the feature of CMOS.

When output enable (OE) is at High level, the switch is on; when at Low level, the switch is off.

P-MOS and N-MOS channel block means the device is suitable for analog signal transmission.

All inputs are equipped with protector circuits to protect the device from static discharge.

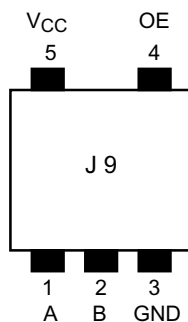


Weight: 0.006 g (typ.)

Features

- Operating voltage: $V_{CC} = 2 \sim 5.5 \text{ V}$
- High speed operation: $t_{pd} = 0.25 \text{ ns (max)}$
- Ultra-low on resistance: $R_{ON} = 5 \Omega \text{ (typ.)}$
- Electro-static discharge (ESD) performance: $\pm 200 \text{ V}$ or more (EIAJ)
 $\pm 2000 \text{ V}$ or more (MIL)
- High noise margin: $V_{NIL} = V_{NIH} = 28\% V_{CC} \text{ (min)}$
- Power-down protection for inputs (control inputs only)
- Package: USV

Pin Assignment (top view)



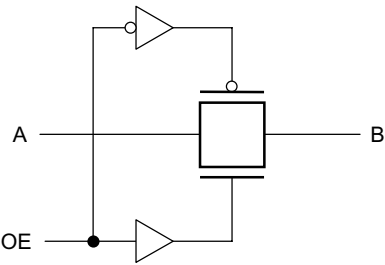
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Truth Table

Inputs	Function
OE	
H	A port = B port
L	Disconnect

System Diagram



Maximum Ratings

Characteristics		Symbol	Rating	Unit
Power supply voltage		V_{CC}	-0.5~7.0	V
Control pin input voltage		V_{IN}	-0.5~7.0	V
Switch terminal I/O voltage		V_S	-0.5~ $V_{CC} + 0.5$	V
Clump diode current	Control input pin	I_{IK}	-50	mA
	Switch terminal		± 50	
Switch I/O current		I_S	128	mA
Power dissipation		P_D	200	mW
DC V_{CC} /GND current		I_{CC}/I_{GND}	± 100	mA
Storage temperature		T_{stg}	-65~150	$^{\circ}C$

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Power supply voltage	V_{CC}	2~5.5	V
Control pin input voltage	V_{IN}	0~5.5	V
Switch I/O voltage	V_S	0~ V_{CC}	V
Operating temperature	T_{opr}	-40~85	$^{\circ}C$
Control pin input rise/fall time	dt/dv	0~10	ns/V

Electrical Characteristics

DC Characteristics (Ta = -40~85°C)

Characteristics		Symbol	Test Condition	V _{CC} (V)	Min	Typ. (Note1)	Max	Unit
Control pin input voltage	“H” level	V _{IH}	—	2~5.5	0.7 × V _{CC}	—	—	V
	“L” level	V _{IL}	—	2~5.5	—	—	0.3 × V _{CC}	
Switch terminal I/O leakage current		I _{IN}	V _{IN} = 0~5.5 V	5.5	—	—	±1.0	μA
Off-state leakage current (switch off)		I _{SZ}	A, B = 0~5.5 V, OE = GND	5.5	—	—	±1.0	μA
ON resistance (Note2)	R _{ON}	V _{IS} = 0 V, I _{IS} = 30 mA	4.5	—	3	7	Ω	
		V _{IS} = 4.5 V, I _{IS} = 30 mA	4.5	—	7	15		
		V _{IS} = 2.4 V, I _{IS} = 15 mA	4.5	—	5	12		
		V _{IS} = 0 V, I _{IS} = 24 mA	3.0	—	4	9		
		V _{IS} = 3 V, I _{IS} = 24 mA	3.0	—	10	20		
		V _{IS} = 0 V, I _{IS} = 8 mA	2.0	—	—	—		
		V _{IS} = 2 V, I _{IS} = 8 mA	2.0	—	—	—		
Quiescent supply current		I _{CC}	V _{IN} = V _{CC} or GND, I _{OUT} = 0	5.5	—	—	10	μA

Note1: The typical values are at V_{CC} = 5 V, Ta = 25°C.

Note2: Apply the specified current to the switch, then measure the voltages on pins A and B. The on-resistance is the lower of the two.

AC Characteristics (Ta = -40~85°C)

Characteristics		Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time (bus to bus)		t _{pLH} t _{pHL}	Figure 1, Figure 2 (Note3)	4.5	—	0.25	ns
Output enable time		t _{pZL} t _{pZH}	Figure 1, Figure 3	4.5	—		ns
Output disable time		t _{pLZ} t _{pHZ}	Figure 1, Figure 3	4.5	—		ns

Note3: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

Capacitive Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	V _{CC} (V)	Typ.	Unit
Control pin input capacitance		C _{IN}	(Note4)	5.0	3	pF
Switch terminal capacitance		C _{I/O}	OE = GND (Note4)	5.0	10	pF

Note4: Guaranteed by design.

AC Test Circuit

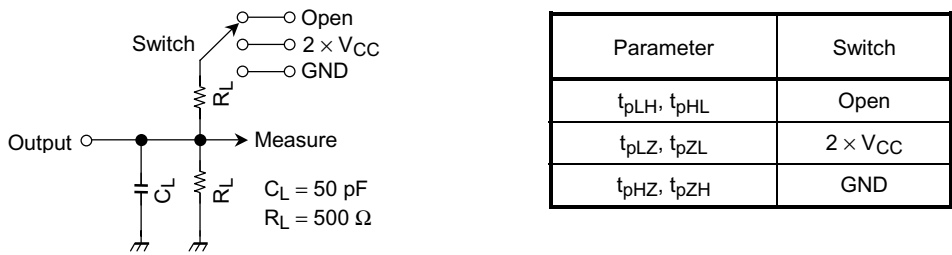


Figure 1

AC Waveform

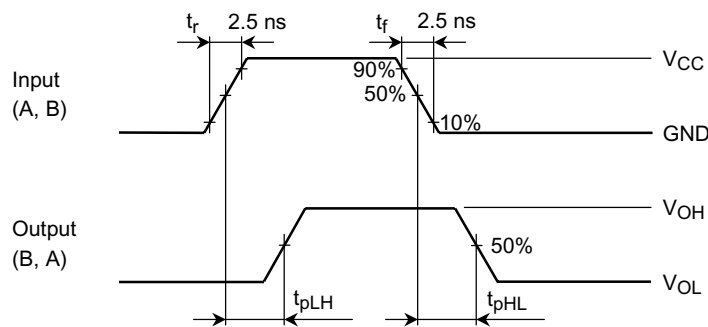


Figure 2 t_{pLH} , t_{pHL}

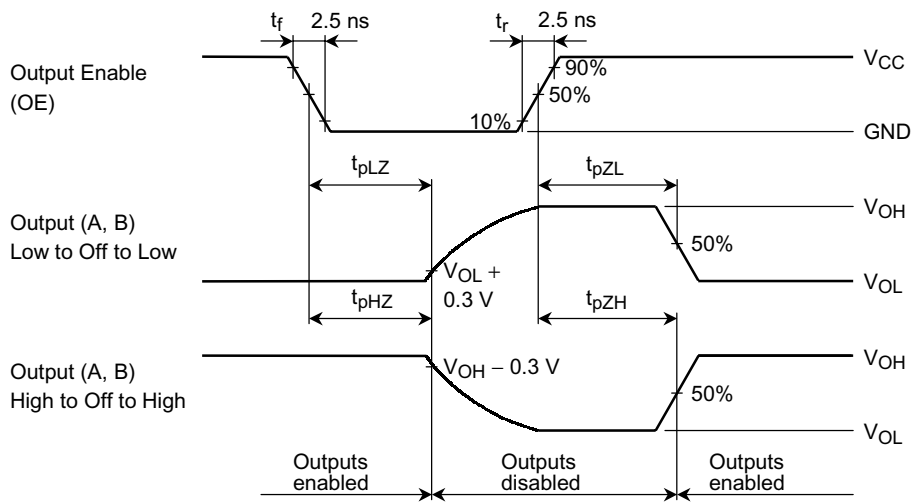
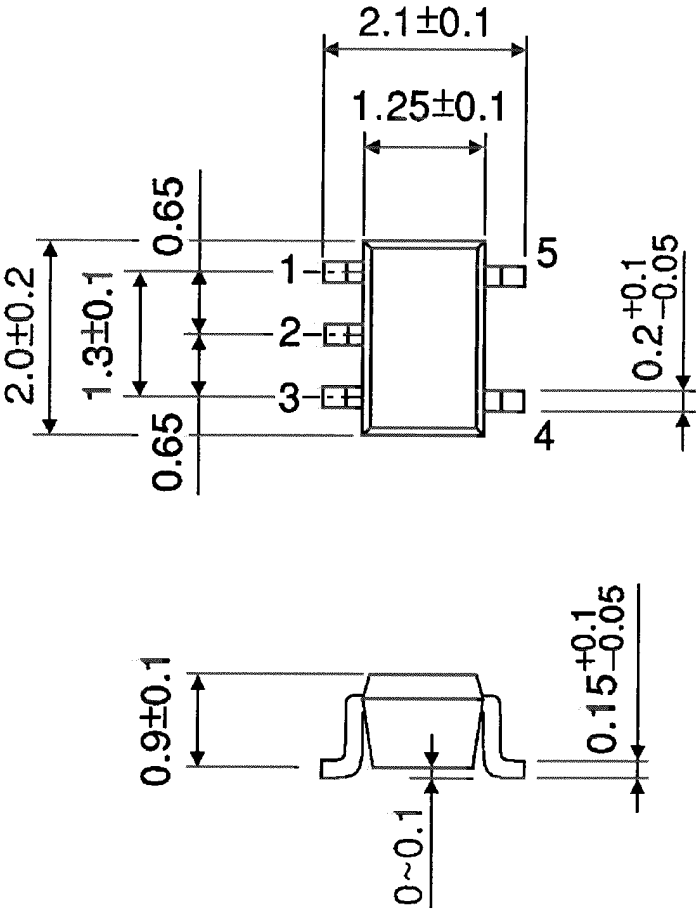


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

Package Dimensions

SSOP5-P-0.65A

Unit : mm



Weight: 0.006 g (typ.)