

---

# HRB0103B

Silicon Schottky Barrier Diode  
for Low Voltage High Speed Switching , Rectifying

**HITACHI**

ADE-208-491(Z)  
Rev 0

---

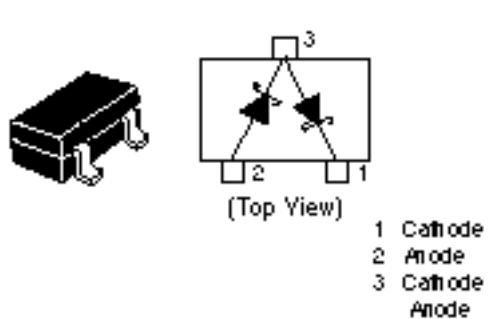
## Features

- Low forward voltage drop and suitable for high efficiency forward current.
- CMPAK package is suitable for high density surface mounting and high speed assembly.

## Ordering Information

Type No.	Laser Mark	Package Code
HRB0103B	E2	CMPAK

## Outline



---

## **HRB0103B**

---

### **Absolute Maximum Ratings (Ta = 25°C) <sup>\*1</sup>**

<b>Item</b>	<b>Symbol</b>	<b>Value</b>	<b>Unit</b>
Repetitive peak reverse voltage	V <sub>RRM</sub>	30	V
Average rectified current	I <sub>o</sub> <sup>*2</sup>	100	mA
Non-Repetitive peak forward surge current	I <sub>FSM</sub> <sup>*3</sup>	3	A
Junction temperature	T <sub>j</sub>	125	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

- Notes:
1. Per one device
  2. See Fig.5, Two device total
  3. 10msec sine wave 1 pulse

### **Electrical Characteristics (Ta = 25°C)**

<b>Item</b>	<b>Symbol</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>	<b>Test Condition</b>
Forward voltage	V <sub>F</sub>	—	—	0.44	V	I <sub>F</sub> = 100 mA
Reverse current	I <sub>R</sub>	—	—	50	μA	V <sub>R</sub> = 30V

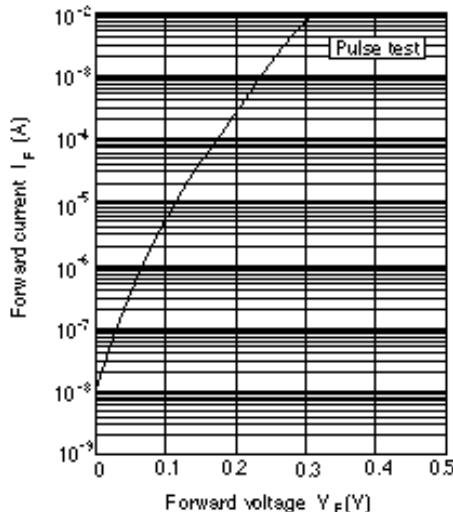
**Main Characteristic**

Fig.1 Forward current Vs. Forward voltage

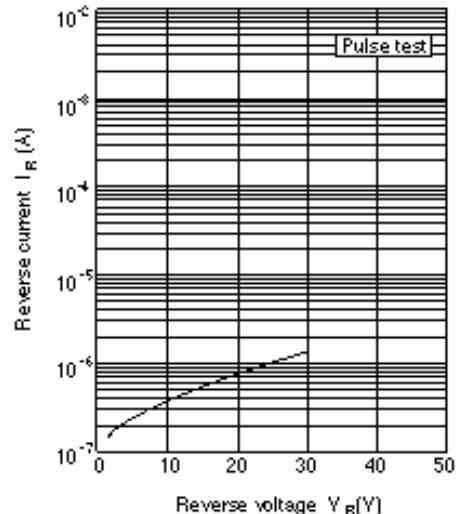


Fig.2 Reverse current Vs. Reverse voltage

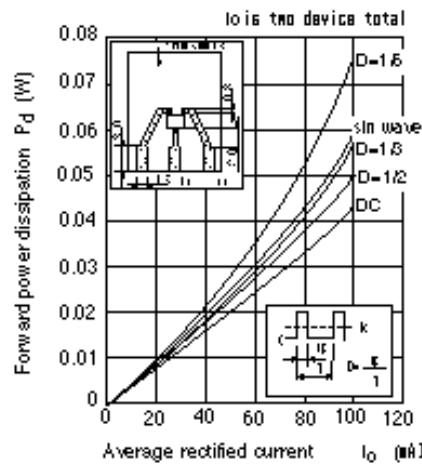


Fig.3. Forward power dissipation Vs. Average rectified current

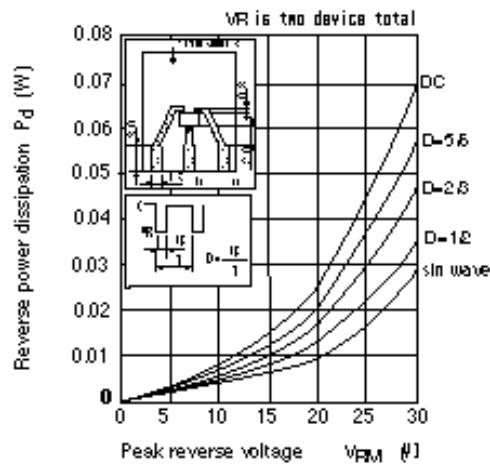


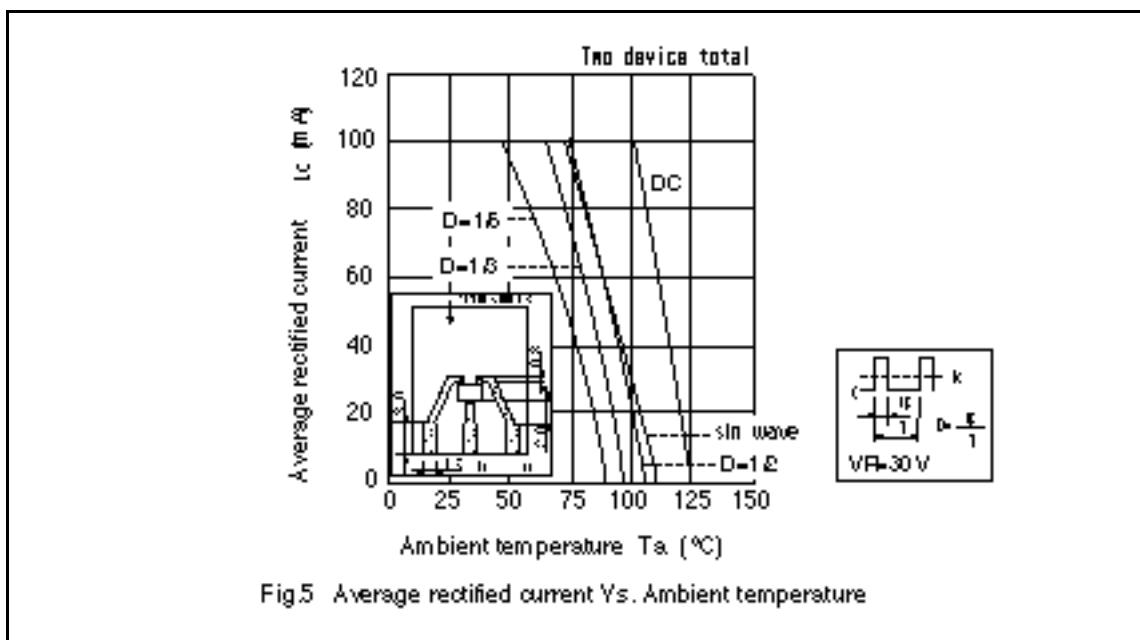
Fig.4. Reverse power dissipation Vs. Peak reverse voltage

---

## HRB0103B

---

### Main Characteristic



---

## **HRB0103B**

---

### **Package Dimensions**

**Unit : mm**

