
HVB27WK

Variable Capacitance Diode for FM tuner

HITACHI

ADE-208-594 (Z)
Product Preview
Rev 0

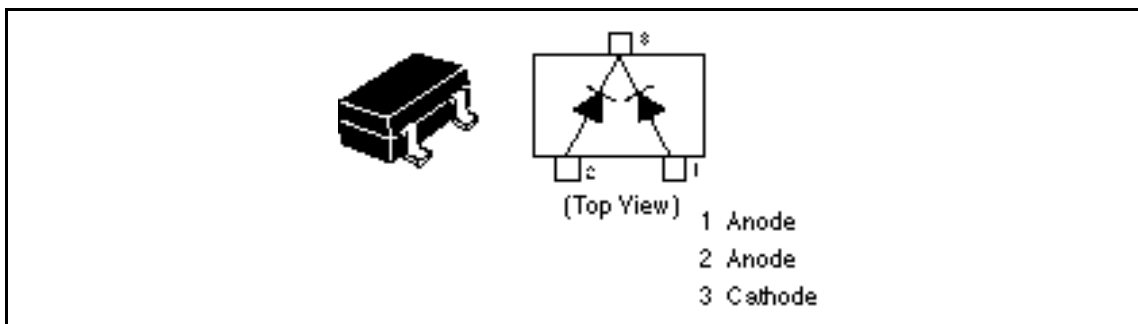
Features

- Σ High capacitance ratio.
- Σ Low series resistance. ($r_s = 0.4W_{max}$)
- Σ Good linearity of C-V curve.
- Σ To be usable at low voltage.
- Σ CMPAK package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Code
HVB27WK	T5	CMPAK

Outline



HVB27WK

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	15	V
Junction temperature	T_j	125	°C
Storage temperature	T_{stg}	-55 to +125	°C

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_{R1}	—	—	10	nA	$V_R = 9\text{ V}$
	I_{R2}	—	—	100		$V_R = 9\text{ V}, T_a = 60^\circ\text{C}$
Capacitance	C_1	52.0	—	62.0	pF	$V_R = 1\text{ V}, f = 1\text{ MHz}$
	C_2	43.0	—	48.0		$V_R = 2\text{ V}, f = 1\text{ MHz}$
	C_8	24.0	—	28.0		$V_R = 8\text{ V}, f = 1\text{ MHz}$
Capacitance ratio	n_1	1.80	—	—	—	C_1 / C_8
	n_2	1.70	—	—	—	C_2 / C_8
Series resistance	r_s	—	—	0.40	W	$V_R = 2\text{ V}, f = 100\text{ MHz}$
Matching error	DC/C^{*1}	—	—	3.0	%	$V_R = 1\text{ to }8\text{ V}, f = 1\text{ MHz}$

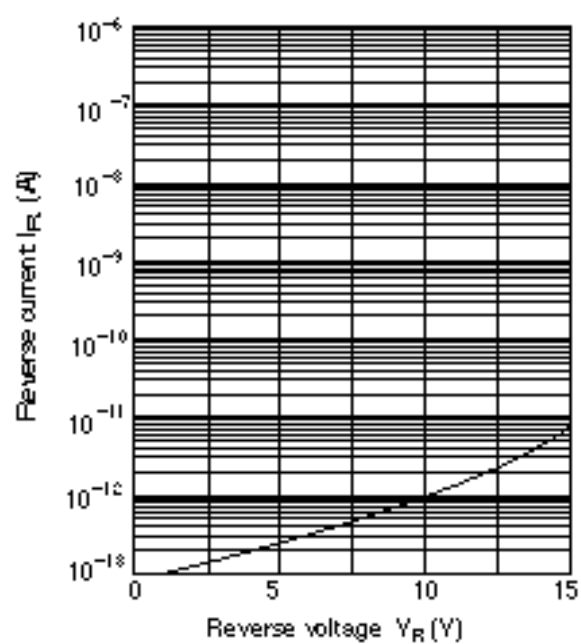
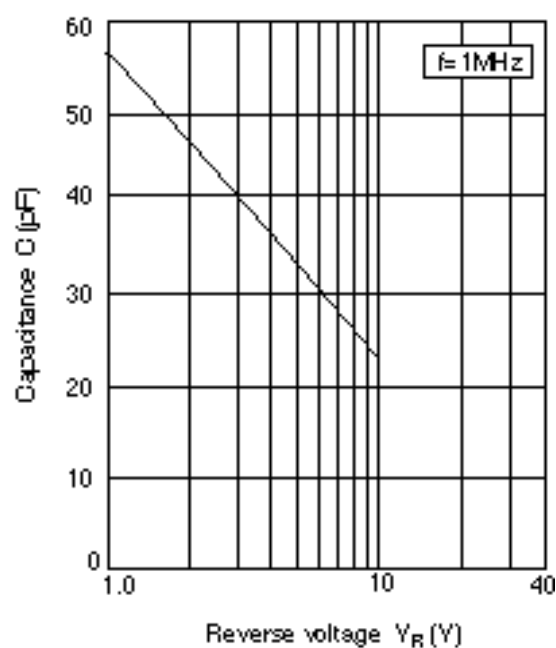
Notes: 1. A set of HVB27WK is of uniform C-V characteristics.

Measure max. value and min. value of capacitance at each bias point of $V_R = 1\text{ V}$ through 8 V .

Calculate Matching Error, $\Delta C/C = \frac{(C_{max} - C_{min})}{C_{min}} \times 100 (\%)$

2. Each group shall uniform a multiple of 4 diodes.

Main Characteristic

Fig.1 Reverse current I_R vs. Reverse voltageFig.2 Capacitance C vs. Reverse voltage

HVB27WK

Package Dimensions

Unit : mm

