



MICROCIRCUIT DATA SHEET

MNLM7709-X REV 0BL

Original Creation Date: 08/08/95
Last Update Date: 10/23/97
Last Major Revision Date: 08/08/95

HIGH PERFORMANCE OPERATIONAL AMPLIFIER

Industry Part Number

LM7709

NS Part Numbers

LM7709H-MIL

Prime Die

LM7709

Processing

MIL-STD-883, Method 5004

Quality Conformance Inspection

MIL-STD-883, Method 5005

Subgrp	Description	Temp (°C)
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55

Electrical Characteristics

DC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)

DC: $V_{CC} = \pm 15V$, $V_{CM} = 0V$, $R_S = 50\ \Omega$

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN-NAME	MIN	MAX	UNIT	SUB-GROUPS
Vio	Input Offset Voltage				-5	5	mA	1
					-6	6	mA	2, 3
		$R_S = 10K\ \Omega$			-5	5	mA	1
					-6	6	mA	2, 3
		$V_{CC} = \pm 9V$			-5	5	mA	1
		$V_{CC} = \pm 9V$			-6	6	mA	2, 3
		$V_{CC} = \pm 9V$, $R_S = 10K\ \Omega$			-5	5	mA	1
		$V_{CC} = \pm 9V$, $R_S = 10K\ \Omega$			-6	6	mA	2, 3
Iio	Input Offset Current				-200	200	nA	1, 2
					-500	500	nA	3
		$V_{CC} = \pm 9V$			-200	200	nA	1, 2
		$V_{CC} = \pm 9V$			-500	500	nA	3
Iib+	Input Bias Current					340	nA	1
						950	nA	3
		$V_{CC} = \pm 9V$				500	nA	1
		$V_{CC} = \pm 9V$				600	nA	3
Iib-	Input Bias Current					340	nA	1
						950	nA	3
		$V_{CC} = \pm 9V$				500	nA	1
		$V_{CC} = \pm 9V$				600	nA	3
Rin	Input Resistance				150		K Ω	1
					40		K Ω	3
Idiff	Differential Leakage Current	$\Delta V_{in} = -5V$			-1		μA	1
		$\Delta V_{in} = 5V$			-1		μA	1
PSRR+	Power Supply Rejection Ratio	$+V_{CC} = 18V$ to $9V$, $-V_{CC} = -18V$, $R_S = 10K\ \Omega$	1			150	$\mu V/V$	1, 2, 3
PSRR-	Power Supply Rejection Ratio	$-V_{CC} = -18V$ to $-9V$, $+V_{CC} = 18V$, $R_S = 10K\ \Omega$	1			150	$\mu V/V$	1, 2, 3
CMRR	Common Mode Rejection Ratio	$-8V \leq V_{CM} \leq 8V$			70		dB	1, 2, 3
		$-8V \leq V_{CM} \leq 8V$, $R_S = 10K\ \Omega$			70		dB	1, 2, 3

Electrical Characteristics

DC PARAMETERS(Continued)

(The following conditions apply to all the following parameters, unless otherwise specified.)

DC: $V_{CC} = \pm 15V$, $V_{CM} = 0V$, $R_S = 50\ \Omega$

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN-NAME	MIN	MAX	UNIT	SUB-GROUPS
I _{CC}	Power Supply Current	$V_{out} = 0V$				5.5	mA	1
V _{IN}	Input Voltage Range		2		-8	8	V	1, 2, 3
P _C	Power Consumption		3			165	mW	1
V _{OP+}	Output Voltage Swing	$R_L = 10K\ \Omega$			12		V	4, 5, 6
		$R_L = 2K\ \Omega$			10		V	4, 5, 6
V _{OP-}	Output Voltage Swing	$R_L = 10K\ \Omega$				-12	V	4, 5, 6
		$R_L = 2K\ \Omega$				-10	V	4, 5, 6
A _{VS+}	Large Signal Voltage Gain	$R_L = 2K$, $V_O = 10V$			25	70	V/mV	4, 5, 6
A _{VS-}	Large Signal Voltage Gain	$R_L = 2K$, $V_O = -10V$			25	70	V/mV	4, 5, 6

AC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)

AC: $V_{CC} = \pm 15V$

t _r	Rise Time	$V_i = 20mV$, $R_L = 2K\ \Omega$, $A_V = 1$, $C_L = 100pF$				1	uS	7
os	Overshoot	$V_i = 20mV$, $R_L = 2K\ \Omega$, $A_V = 1$, $C_L = 100pF$				30	%	7

Note 1: Datalog in $\mu V = \mu V/V$.

Note 2: Guaranteed by CMRR tests.

Note 3: Guaranteed by I_{CC} test: $P_C = 30I_{CC}$.

Revision History

Rev	ECN #	Rel Date	Originator	Changes
0BL	M0002518	10/23/97	Barbara Lopez	Update MDS: MNLM7709-X Rev. 0AL to MNLM7709-X Rev. 0BL for Lifetime Buy.