



Weltrend Semiconductor, Inc.

WT61P5

MCU for TV with 128K Flash

Data Sheet

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GENERAL DESCRIPTION

The WT61P5 is a highly integrated Micro controller for LCD monitor or LCD TV with VBI decoder and TTL OSD output.

FEATURES

- 8-bit 80C51 compatible CPU with 24MHz operating frequency
- 128K bytes flash memory, 256 internal SRAM, 1024 bytes on-chip auxiliary (external) SRAM
- 4K EEPROM support
- Build-In 64k bytes downloadable OSD Font Table
- Semi-Transparent background OSD signal output for CC application
- 4 channels PWM outputs (3x 8-bit + 1x 16-bit) and 2 channels low frequency PWM outputs (2x 8-bits, under1KHz)
- Sync signal processor with H+V separation, H/V frequency counter, H/V polarity detection/control and clamp pulse output
- Programmable H and V mode change interrupt for fast blanking
- DMA DDC1/2B module for EDID1.3, EDID2.0 (A2/A3, A6/A7) and Enhance EDID (60/61) 512 byte
- Fast mode master/slave I²C interface (up to 400KHz)
- 8-bit A/D converter with 4 selectable inputs
- Watchdog timer
- Build-In IR decoder (NEC, RC5, TOSHIBA)
- Standard UART port support
- Three timers compatible to 8051
- Two external interrupt request input (8031 INT0/INT1)
- Parallel bus support for fast communication with external device
- Power down mode supported
- ISP function supported
- on-chip S/W ICE supported
- C compiler supported
- Programmable I/O pins
- 5 volt IO, 2.5V core operating voltage supported
- Low VDD reset supported (guard 2.0V, power-off-reset / power-on-reset)