



## Multi-System Power Supply for Audio Equipment

#### Overview

The LA5618 is a multi-system power supply IC with a built-in on/off control function. It is optimal for use as the power supply IC in CD players, mini-component stereo systems, and other microcontroller controlled audio equipment.

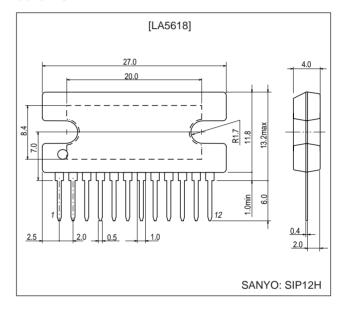
#### **Functions**

- Power supply IC with  $\pm 7.5$  V outputs ( $\pm 1.5$  A) and an on/off control function.
- The LA5618 is pin compatible with the LA5617.

# **Package Dimensions**

unit: mm

#### 3049B-SIP12H



## **Specifications**

Maximum Ratings at  $Ta = 25^{\circ}C$ 

Parameter	Symbol	Conditions	Ratings	Unit
Input voltage	V <sub>CC</sub> /V <sub>EE</sub> max		±18	V
Allowable power dissipation	Pd max	With no heat sink	2.4	W
Operating temperature	Topr		-20 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

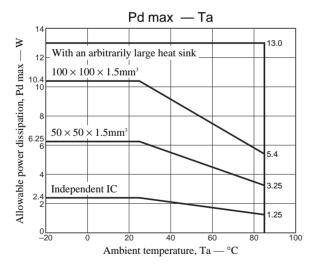
#### Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Input voltage	V <sub>CC</sub> /V <sub>EE</sub>		±9.5 to ±16	V
Output ourront	I <sub>OUT</sub> 1		0 to 1.5	Α
Output current	I <sub>OUT</sub> 2		-1.5 to 0	Α

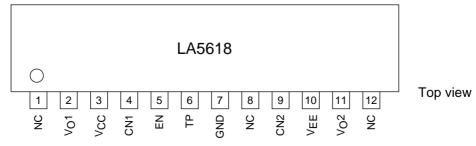
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# Operating Characteristics at Ta = 25 $^{\circ}C,\,V_{CC}/V_{EE}$ = ±9.5 V, in the specified test circuit.

Parameter	Symbol	Conditions	Ratings			1.1-14
Parameter	Symbol	Conditions	min	typ	max	Unit
[+7.5 V Power Supply Block] I <sub>OUT</sub> 1	= 500 mA, C	<sub>OUT</sub> 1 = 100 μF				
Output voltage	V <sub>O</sub> 1		7.0	7.5	8.0	V
Dropout voltage	V <sub>DROP</sub> 1-1			1.5	2.0	V
	V <sub>DROP</sub> 1-2	I <sub>OUT</sub> 1 = 300 mA		1.0	1.5	V
Line regulation	ΔV <sub>OLN</sub> 1	9 V ≤ V <sub>CC</sub> ≤ 16 V		20	100	mV
Load regulation	ΔV <sub>OLD</sub> 1	5 mA ≤ I <sub>OUT</sub> 1 ≤ 1 A		80	200	mV
Peak output current	I <sub>OP</sub> 1	$V_{CC}/V_{EE} = \pm 12 \text{ V}$	1.5	1.8		А
Output short current	I <sub>OSC</sub> 1			1.0		А
Output off voltage	V <sub>O</sub> 1 <sub>OFF</sub>	V <sub>EN</sub> = 0.4 V			0.3	V
Ripple rejection	Rrej1	$f = 120 \text{ Hz}, 8.5 \text{ V} \le \text{V}_{CC} \le 16 \text{ V}, \text{CN1} = 1  \mu\text{F}$		65		dB
[–7.5 V Power Supply Block] $I_{OUT}2$	= 500 mA, C	<sub>OUT</sub> 2 = 100 μF				
Output voltage	V <sub>O</sub> 2		-8.0	-7.5	-7.0	V
Dropout voltage	V <sub>DROP</sub> 2-1			1.5	2.0	V
	V <sub>DROP</sub> 2-2	$I_{OUT}2 = -300 \text{ mA}$		1.0	1.5	V
Line regulation	ΔV <sub>OLN</sub> 2	-16 V ≤ V <sub>EE</sub> ≤ -9 V		200	300	mV
Load regulation	ΔV <sub>OLD</sub> 2	-1 A ≤ I <sub>OUT</sub> 2 ≤ -5 mA		80	200	mV
Peak output current	I <sub>OP</sub> 2	$V_{CC}/V_{EE} = \pm 12 \text{ V}$		-1.8	-1.5	А
Output short current	I <sub>OSC</sub> 2			-1.0		А
Output off voltage	V <sub>O</sub> 2 <sub>OFF</sub>	V <sub>EN</sub> = 0.4 V	-0.3			V
Ripple rejection	Rrej2	$f = 120 \text{ Hz}, 16 \text{ V} \le \text{V}_{\text{EE}} \le -8.5 \text{ V}, \text{CN2} = 1 \mu\text{F}$		50		dB
[Common Circuit Block] $C_{OUT}1 = 10$	00 μF, C <sub>OUT</sub> 2	2 = 100 μF				
Output off control voltage	V <sub>ENL</sub>	V <sub>O</sub> 1, V <sub>O</sub> 2: Off			0.4	V
Current drain (positive voltage power supply block)	I <sub>QP</sub> 1	$I_{OUT}1 = 0, I_{OUT}2 = 0$		5.0		mA
	I <sub>QP</sub> 2	I <sub>OUT</sub> 1 = 1.5 A, I <sub>OUT</sub> 2 = 0		7.0		mA
Current drain (negative voltage power supply block)	I <sub>QM</sub> 1	$I_{OUT}1 = 0, I_{OUT}2 = 0$		-5.0		mA
	I <sub>QM</sub> 2	$I_{OUT}1 = 0$ , $I_{OUT}2 = -1.5$ A		-12.0		mA

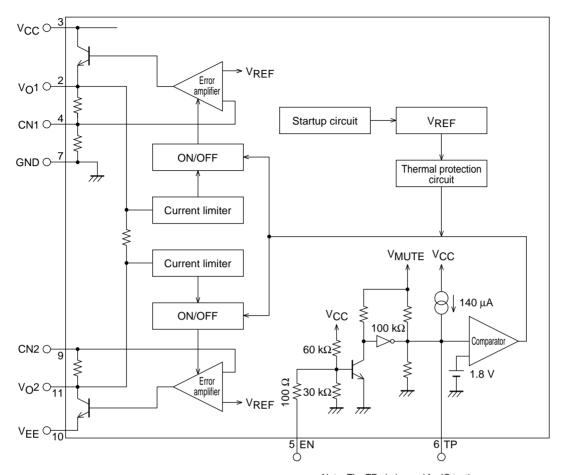


### **Pin Assignment**



Note: The TP pin is used for IC testing. It must be left open during normal operation. The NC pins must also be left open during normal operation.

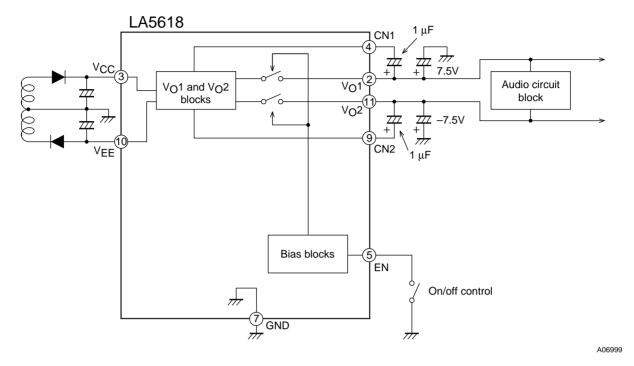
## **Equivalent Circuit Block Diagram**



Note: The TP pin is used for IC testing. It must be left open during normal operation.

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#### Sample Application Circuit: Mini-component stereo system power supply



Notes: 1. The  $V_01$  and  $V_02$  output capacitors must have values of at least 100  $\mu$ F and capacitors with low temperature coefficients must be used to prevent oscillation at low temperatures.

- 2. External noise can be suppressed and ripple rejection improved by adding capacitors between CN1 and  $V_{O}1$  and between CN2 and  $V_{O}2$ .
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