Electronics

## Features

- Balanced (symmetrical) RF Ports
- Low Cross Modulation
- Low Insertion Loss: 0.55 dB at 1 GHz
- High Isolation: 21 dB at 2 GHz
- Miniature Package: 3 mm 12-Lead PQFN
- 0.5 micron GaAs PHEMT Process


## Description

M/A-COM's MASWSS0060 is an industry leading GaAs PHEMT MMIC single pole three throw (SP3T) CDMA-GPS switch in a low cost 3 mm 12-lead PQFN package. The MASWSSOO60 is uniquely configured to enable switching from a common antenna port to CDMA cellular, CDMA PCS, or GPS ports.

The design is symmetric and has been fully optimized for excellent cross modulation performance in all three paths while still maintaining excellent insertion loss and isolation. Note that since the design is symmetric, the user can assign CDMA cellular, CDMA PCS and GPS to ports RF1, RF2 or RF3 at his or her discretion.

The MASWSS0060 is fabricated using a 0.5 micron gate length GaAs PHEMT process. The process features full passivation for performance and reliability.

## Ordering Information

| Part Number | Package |
| :---: | :---: |
| MASWSS0060TR | 1000 piece reel |
| MASWSS0060TR-3000 | 3000 piece reel |
| MASWSS0060SMB | Sample on Evaluation Board |

Note: Reference Application Note M513 for reel size
information. information.

Functional Schematic


## Pin Configuration

| Pin No. | Function | Description |
| :---: | :---: | :---: |
| 1 | V1 | Control 1 |
| 2 | RF1 | RF Port 1 |
| 3 | GND | Ground |
| 4 | GND | Ground |
| 5 | RF2 | RF Port 2 |
| 6 | V2 | Control 2 |
| 7 | GND | RF Ground |
| 8 | RF3 | RF Port 3 |
| 9 | V3 | Control 3 |
| 10 | GND | RF Ground |
| 11 | GNT | Antenna Port |
| 12 | GND (paddle) | RF Ground |
| 13 | RF Ground |  |

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GaAs SP3T 2.7 V CDMA-GPS Switch

Electrical Specifications: $\mathrm{T}_{\mathrm{A}}=\mathbf{2 5}{ }^{\circ} \mathrm{C}, \mathrm{Z}_{\mathbf{0}}=\mathbf{5 0} \mathbf{\Omega}^{1}$

| Parameter | Test Conditions | Units | Min. | Typ. | Max. |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss ${ }^{2}$ <br> (All Paths) | $\begin{aligned} & 1.0 \mathrm{GHz} \\ & \text { 1.5 GHz } \\ & 2.0 \mathrm{GHz} \end{aligned}$ | dB <br> dB <br> dB | — | $\begin{aligned} & 0.55 \\ & 0.60 \\ & 0.70 \end{aligned}$ | $\begin{aligned} & 0.70 \\ & 0.75 \\ & 0.90 \end{aligned}$ |
| Isolation | $\begin{aligned} & \mathrm{DC}-1.0 \mathrm{GHz} \\ & 1.0-2.0 \mathrm{GHz} \\ & 2.0-2.5 \mathrm{GHz} \end{aligned}$ | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ | $\begin{aligned} & 23 \\ & 18 \\ & 16 \end{aligned}$ | $\begin{aligned} & 27 \\ & 21 \\ & 20 \end{aligned}$ | $\begin{aligned} & - \\ & - \end{aligned}$ |
| Return Loss (All RF ports) | DC - 2.5 GHz | dB | - | 20 | - |
| IP3 | Two Tone, $+22 \mathrm{dBm} /$ tone, 1 MHz Spacing, 820 MHz Two Tone, $+22 \mathrm{dBm} /$ tone, 1 MHz Spacing, 1880 MHz | dBm dBm | — | $\begin{aligned} & 62 \\ & 61 \end{aligned}$ | — |
| Cross Modulation | For Cell Band: Two-tone signal input: $\text { Tx1 = +22 dBm @ } 820 \mathrm{MHz}, \mathrm{Tx2}=+22 \mathrm{dBm} @ 821 \mathrm{MHz} \text {, }$ <br> RX interfere = -23 dBm @ 869 MHz | dBm | - | -101 | - |
|  | For PCS Band: Two-tone signal input: $\begin{gathered} \mathrm{Tx} 1=+18 \mathrm{dBm} @ 1880 \mathrm{MHz}, \mathrm{Tx} 2=+18 \mathrm{dBm} @ 1881 \mathrm{MHz}, \\ \text { RX interfere = -23 dBm @ } 1960 \mathrm{MHz} \end{gathered}$ | dBm | - | -102 | - |
| P.1dB | $\mathrm{Vc}=0 \mathrm{~V} / 2.7 \mathrm{~V}, 1 \mathrm{GHz}$ | dBm | - | 36 | - |
| Trise, Tfall | 10\% to $90 \% \mathrm{RF}, 90 \%$ to $10 \% \mathrm{RF}, 900 \mathrm{MHz}$ | $\mu \mathrm{S}$ | - | 0.06 | - |
| Ton, Toff | 50\% control to 90\% RF, and 50\% control to 10\% RF, 900 MHz | $\mu \mathrm{S}$ | - | 0.09 | - |
| Transients | In Band | mV | - | 70 | - |
| Control Current | $\mathrm{Vc}=0 \mathrm{~V} / 2.7 \mathrm{~V}$ | $\mu \mathrm{A}$ | - | - | 30 |

1. External DC blocking capacitors are required on all RF ports.
2. Insertion Loss can be optimized by varying the DC blocking capacitor value, e.g. 1000 pF for $100 \mathrm{MHz}-500 \mathrm{MHz}, 100 \mathrm{pF}$ for $0.5-2.5 \mathrm{GHz}$.

## Absolute Maximum Ratings ${ }^{3,4}$

| Parameter | Absolute Maximum |
| :---: | :---: |
| Input Power | +36 dBm |
| $(0.5-2.5 \mathrm{GHz}, 2.7 \mathrm{~V}$ Control $)$ |  |
| Control Voltage | $\pm 8.5$ volts |
| Operating Temperature | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |

3. Exceeding any one or combination of these limits may cause permanent damage to this device.
4. $\mathrm{M} / \mathrm{A}-\mathrm{COM}$ does not recommend sustained operation near these survivability limits.

Truth Table ${ }^{5,6}$

| V1 | V2 | V3 | ANT-RF1 | ANT-RF2 | ANT-RF3 |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 0 | 0 | On | Off | Off |
| 0 | 1 | 0 | Off | On | Off |
| 0 | 0 | 1 | Off | Off | On |

5. Differential voltage, V (state 1 ) -V (state 0 ) must be 2.6 V minimum and must not exceed 8.5 V .
6. $0=-5 \mathrm{~V}$ to $2.4 \mathrm{~V}, 1=-2.4 \mathrm{~V}$ to 5 V .

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Visit www.macom.com for additional data sheets and product information.

Typical Performance Curves @ +25 ${ }^{\circ} \mathrm{C}$


Insertion Loss


Isolation


## Qualification

Qualified to M/A-COM specification REL-201, Process Flow -2.

## Handling Procedures

Please observe the following precautions to avoid damage:

## Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

## 3 mm 12-Lead PQFN



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