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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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2SK3151

Silicon N Channel MOS FET High Speed Power Switching

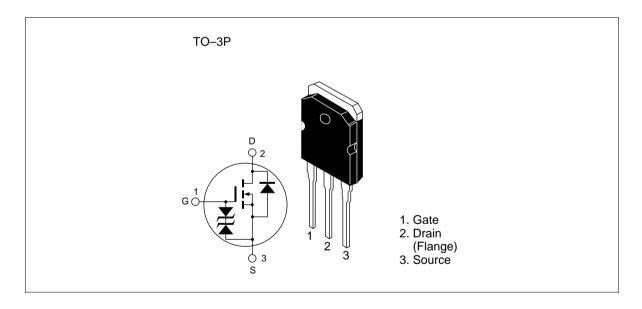


ADE-208-747B (Z) 3rd. Edition Mar. 2001

Features

- Low on-resistance $R_{DS (on)} = 11.5 \text{m}\Omega$ typ.
- High speed switching
- 4V gate drive device can be driven from 5V source

Outline



2SK3151

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	100	V
Gate to source voltage	$V_{\sf GSS}$	±20	V
Drain current	I _D	50	A
Drain peak current	Note1 D(pulse)	200	A
Body-drain diode reverse drain current	I _{DR}	50	A
Avalanche current	I _{AP} Note3	50	A
Avalanche energy	E _{AR} Note3	250	mJ
Channel dissipation	Pch Note2	125	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: 1. PW \leq 10 μ s, duty cycle \leq 1 %

- 2. Value at Tc = 25°C
- 3. Value at Tch = 25° C, Rg $\geq 50\Omega$

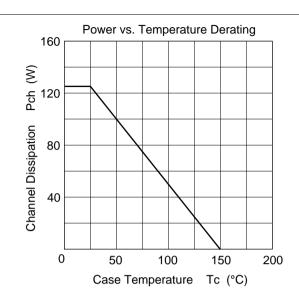
Electrical Characteristics ($Ta = 25^{\circ}C$)

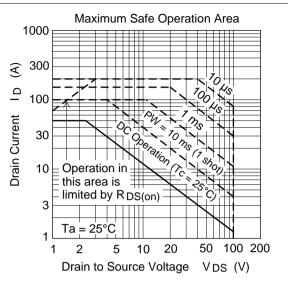
		_	±10	μA μA	$I_D = 10 \text{mA}, \ V_{GS} = 0$ $I_G = \pm 100 \mu \text{A}, \ V_{DS} = 0$ $V_{GS} = \pm 16 \text{V}, \ V_{DS} = 0$ $V_{DS} = 100 \ \text{V}, \ V_{GS} = 0$
	0	_	±10	μΑ μΑ	$V_{GS} = \pm 16V, V_{DS} = 0$
')		_	10	μA	
')				•	$V_{DS} = 100 \text{ V}, V_{GS} = 0$
')		_	0.5		
			2.5	V	$I_D = 1 \text{mA}, V_{DS} = 10 \text{V}$
•,	_	11.5	15	mΩ	$I_{\rm D} = 25 {\rm A}, \ V_{\rm GS} = 10 {\rm V}^{\rm Note4}$
n)	_	16	25	mΩ	$I_D = 25A$, $V_{GS} = 4V^{Note4}$
30	0	50	_	S	$I_D = 25A, V_{DS} = 10V^{Note4}$
_	_	4000	_	pF	V _{DS} = 10V
; –	_	1650	_	pF	$V_{GS} = 0$
_	-	590	_	pF	f = 1MHz
_	- :	30	_	ns	$I_D = 25A, V_{GS} = 10V$
_	- :	280	_	ns	$R_L = 1.2\Omega$
_	_	830	_	ns	
_		450	_	ns	
_	_ (0.95	_	V	$I_F = 50A, V_{GS} = 0$
_	_	100	_	V	$I_F = 50A$, $V_{GS} = 0$ diF/ dt =50A/ μ s
	3 - s -	30 — 30 — 30 — 30 — 30 — 30 — 30 — 30 —	- 11.5 - 16 30 50 - 4000 - 1650	- 11.5 15 - 16 25 - 30 50 — - 4000 — - 1650 — - 590 — - 30 — - 280 — - 830 — - 450 — - 0.95 —	$\begin{array}{cccccccccccccccccccccccccccccccccccc$

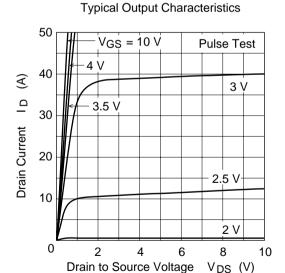
Note: 4. Pulse test

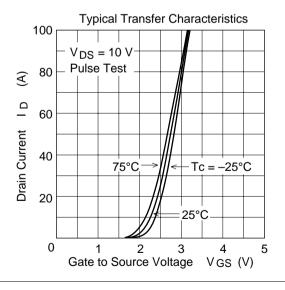
2SK3151

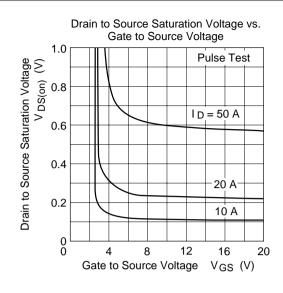
Main Characteristics

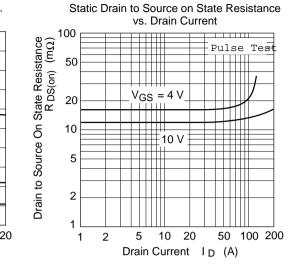


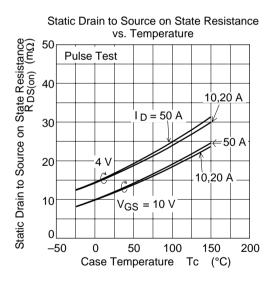


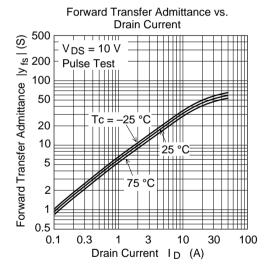


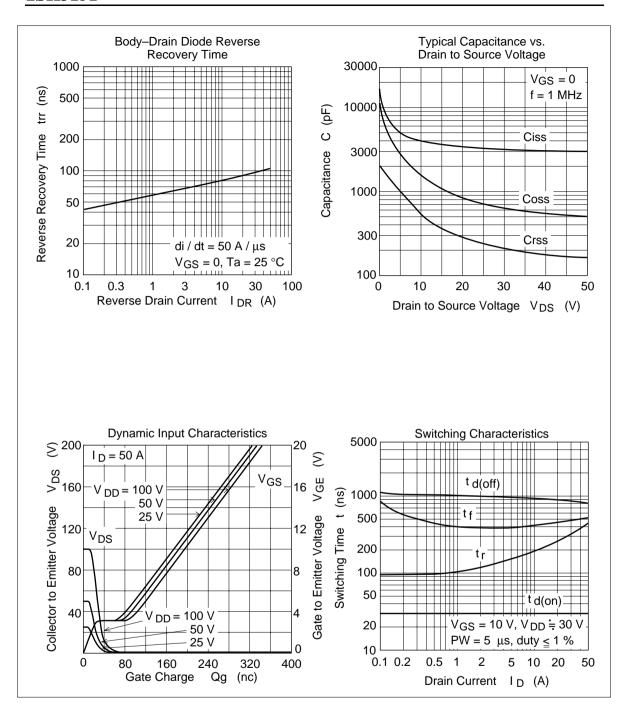


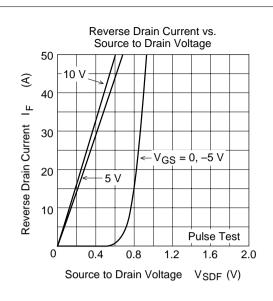


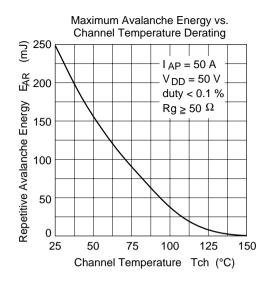










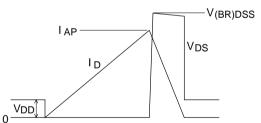


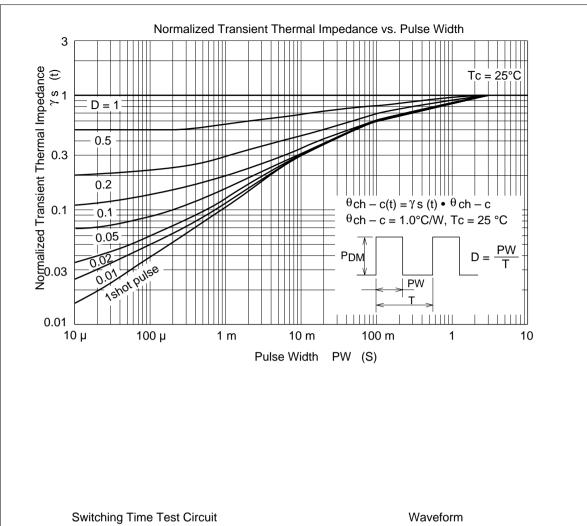
VDS Monitor C I AP Monitor D. U. T VDD

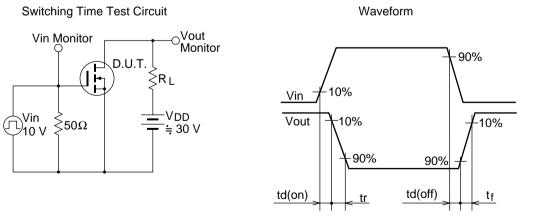
Avalanche Test Circuit

 $E_{AR} = \frac{1}{2} \cdot L \cdot I_{AP}^{2} \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$

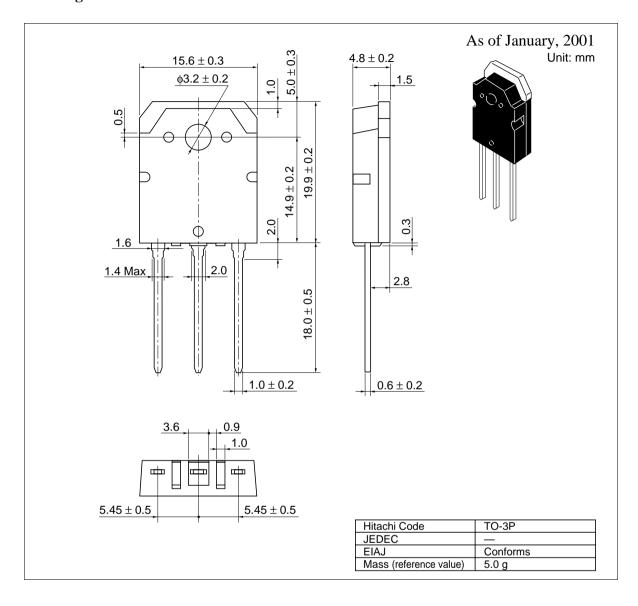
Avalanche Waveform







Package Dimensions



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