General purpose amplification (–30V, –1A) 2SB1733

Application

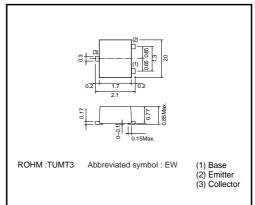
Low frequency amplifier Driver

● Features

- 1) A collector current is large.
- 2) Collector saturation voltage is low. VcE(sat): max. -350mV

at Ic = -500 mA / IB = -25 mA

●External dimensions (Unit : mm)



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	-30	V
Collector-emitter voltage	Vceo	-30	V
Emitter-base voltage	Vево	-6	V
Collector current	Ic	-1	Α
Collector current	Іср	-2	A *1
Power dissipation	Pc	0.4	W *2
Fower dissipation	'	0.8	W *3
Junction temperature	Tj	150	°C
Range of storage temperature	Tstg	-55 to +150	°C

Packaging specifications

	Package	Taping
Туре	Code	TL
	Basic ordering unit (pieces)	3000
2SB1733		0

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-30	_	_	V	Ic=-10μA
Collector-emitter breakdown voltage	BVceo	-30	_	_	V	Ic=-1mA
Emitter-base breakdown voltage	ВVево	-6	_	_	V	I _E =-10μA
Collector cutoff current	Ісво	_	_	-100	nA	Vcb=-30V
Emitter cutoff current	ІЕВО	_	_	-100	nA	V _{EB} =-6V
Collector-emitter saturation voltage	VCE(sat)	_	-150	-350	mV	Ic=-500mA, IB=-25mA
DC current gain	hfe	270	_	680	_	Vce=-2V, Ic=-100mA *
Transition frequency	f⊤	_	320	_	MHz	Vce=-2V, Ie=100mA, f=100MHz *
Corrector output capacitance	Cob	_	7	_	pF	Vcb=-10V, Ie=0A, f=1MHz

^{*} Pulsed

^{*1} Single pulse, Pw=1ms

^{*2} Each Terminal Mounted on a Recommended land pattern *3 Mounted on a 25mm×25mm×¹0.8mm ceramic substrate

^{*5} Wounted on a 25mm/25mm/ 0.0mm ceramic substi

Electrical characteristic curves

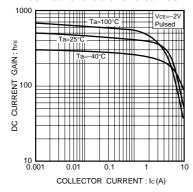


Fig.1 DC current gain vs. collector current

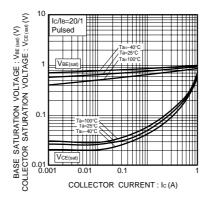


Fig.2 Collector-emitter saturation voltage base-emitter saturation voltage vs. collector current

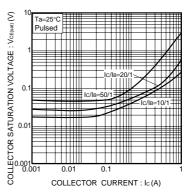


Fig.3 Collector-emitter saturation voltage vs. collector current

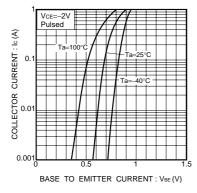


Fig.4 Grounded emitter propagation characteristics

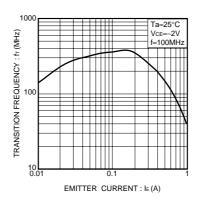


Fig.5 Gain bandwidth product vs. emitter current

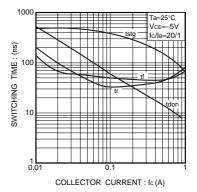


Fig.6 Switching time

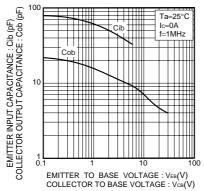


Fig.7 Collector output capacitance vs. collector-base voltage Emitter input capacitance vs. emitter-base voltage

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