

CMOS 4-BIT MICROCONTROLLER

**TMP47P241VN
TMP47P241VM**

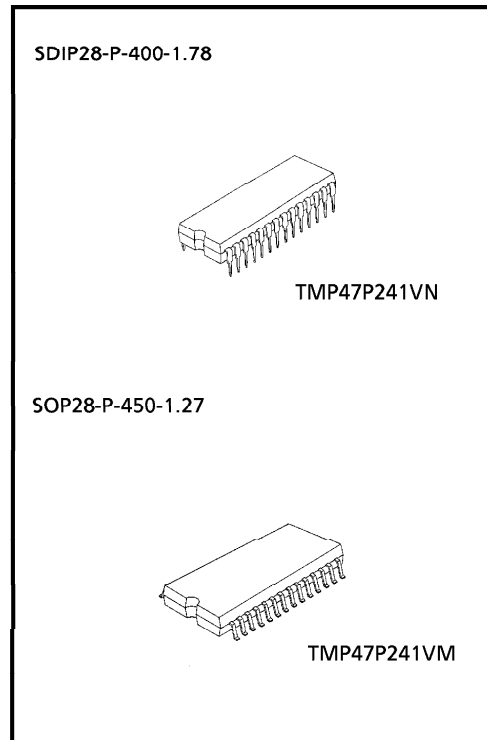
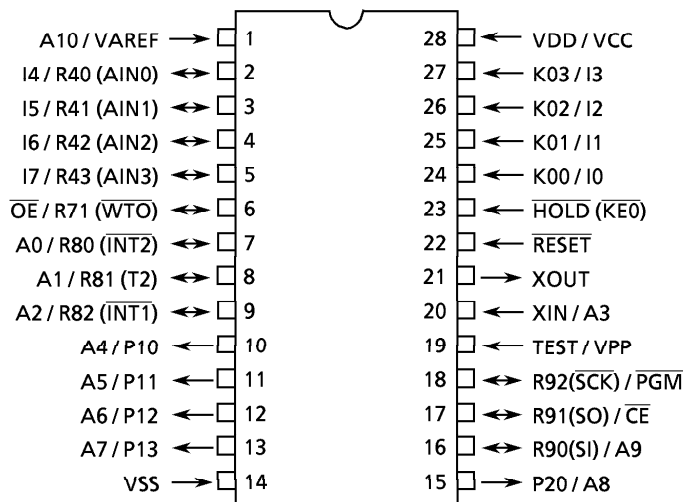
The 47P241V is the system evaluation LSI of 47C241 with 16K bits one-time PROM. The 47P241V programs / verifies using an adaptor socket to connect with PROM programmer, as it is in TMM2764AD.

In addition, the 47P241V and the 47C241 are pin compatible. The 47P241V operates as the same as the 47C241 by programming to the internal PROM.

PART No.	ROM	RAM	PACKAGE	ADAPTOR SOCKET
TMP47P241VN	OTP	128 x 4-bit	SDIP28-P-400-1.78	BM1156
TMP47P241VM	2048 x 8-bit		SOP28-P-450-1.27	BM1157

PIN ASSIGNMENT (TOP VIEW)

SDIP28-P-400-1.78 / SOP28-P-450-1.27



PIN FUNCTION

The 47P241V has MCU mode and PROM mode.

(1) MCU mode

The 47C241 and the 47P241V are pin compatible (TEST pin for out-going test. Be fixed to low level).

(2) PROM mode

PIN NAME	INPUT / OUTPUT	FUNCTIONS	PIN NAME(MCU mode)
A10	INPUT	Address inputs	VAREF
A9			R90
A8			P20
A7 to A4			P13 to P10
A3			XIN
A2 to A0			R82 to R80
I7 to I4	I/O	Data outputs (Inputs)	R43 to R40
I3 to I0			K03 to K00
$\overline{\text{PGM}}$	Input	Program control input	R92
$\overline{\text{CE}}$		Chip Enable input	R91
$\overline{\text{OE}}$		Output Enable input	R71
VPP	Power supply	+ 12.5 V / 5 V (Program supply voltage)	TEST
VCC		+ 5 V	VDD
VSS		0 V	VSS
RESET	Input	PROM mode setting pin. Be fixed to low level.	
HOLD	Input		
XOUT	Input	Input the clock from the external oscillator.	

OPERATIONAL DESCRIPTION

The following is an explanation of hardware configuration and operation in relation to the 47P241V. The 47P241V is the same as the 47C241 except that an OTP is used instead of a built-in mask ROM.

1. OPERATION mode

The 47P241V has an MCU mode and a PROM mode.

1.1 MCU mode

The MCU mode is set by fixing the TEST/VPP pin at the "L" level. Operation in the MCU mode is the same as for the 47C241, except that the TEST/VPP pin does not have built in pull-down resistor and cannot be used open. In the 47P241V, RC oscillation is impossible.

1.1.1 Program Memory

The program storage area is the same as for the 47C241.

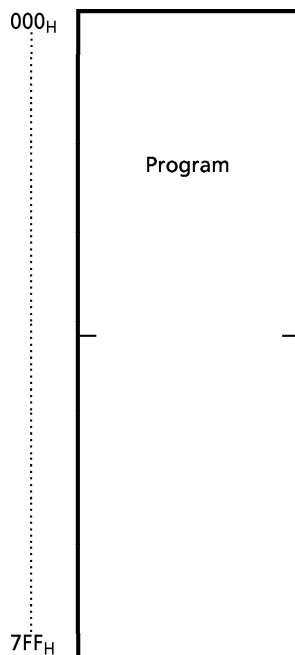


Figure 1-1. Program area (ROM)

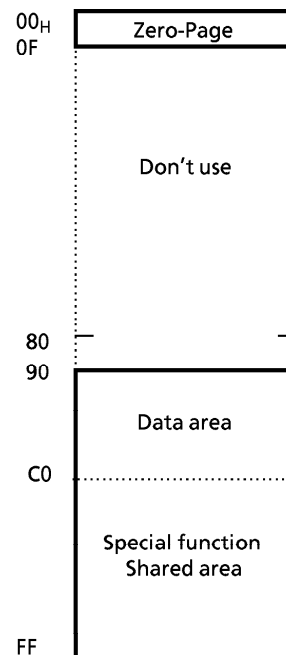


Figure 1-2. RAM addressing

1.1.2 Data Memory

The 47P241V has 128×4 -bit of data memory (RAM).

When the 47C940A is used as evaluator of the 47C241V, programming should be performed assuming that the RAM is assigned to addresses 00 to 7FH and 90 to FFH as show in Figure 1-2 by considering the application software evaluation. When the BM47214A (emulator) is used as the 47C241 evaluator, it is same.

1.1.3 Input / Output Circuitry

(1) Control pins

This is the same as for the 47C241 except that there is no built-in pull-down resistance for the TEST pin. In the 47P241V, RC oscillation is impossible. Connecting the resonator is required when using as evaluator of I/O code SG.

(2) I/O Ports

The input/output circuit of the 47P241V is the same as I/O code SA of the 47C241. External resistance, for example, is required when using as evaluator of other I/O codes (SB, SC).



Figure 1-3. I/O code and external circuitry

1.2 PROM mode

The PROM mode is set by setting the $\overline{\text{RESET}}$, $\overline{\text{HOLD}}$ pins to the "L" level. In PROM mode, programs can be written or verified using a general-purpose PROM writer with an adapter socket being attached. (A high-speed program mode is used is used set the ROM type the same as for the TMM2764AD.)

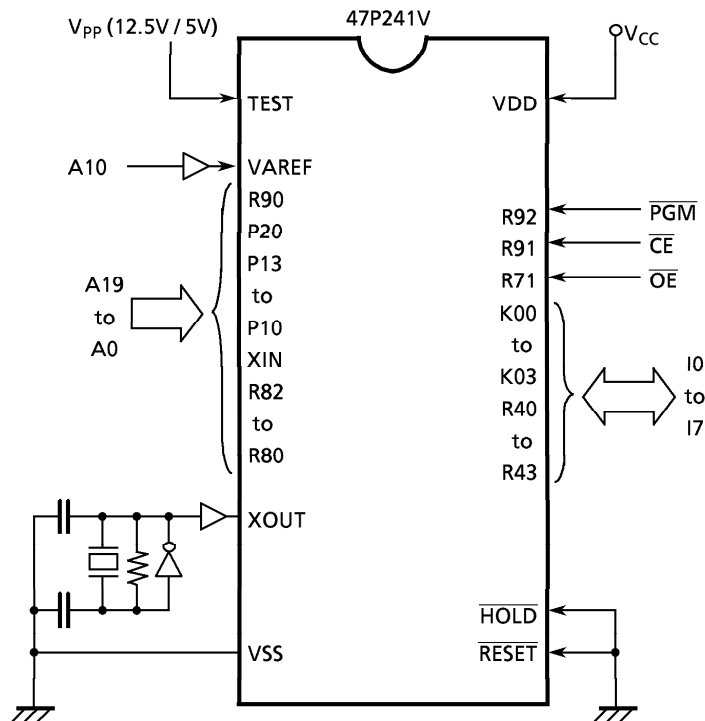


Figure 1-4. Setting for PROM mode

1.2.1 Program Writing

When writing a program, set a ROM type to "2764A" (programming voltage : 12.5V) . Since the 47P241V has a 2048 × 8-bit internal PROM (000 to 7FF_H) , set a stop address of a PROM writer to "7FF_H" . For a general-purpose PROM writer, use the writer which does not have or can release an electric signature mode.

1.2.2 High Speed Programming Mode

The program time can be greatly decreased by using this high speed programming mode. The device is set up in the high speed programming mode when the programming voltage (+ 12.5 V) is applied to the V_{pp} terminal with $V_{CC} = 6 V$ and $\overline{PGM} = V_{IH}$.

The programming is achieved by applying a single low level 1ms pulse the \overline{PGM} input after addresses and data are stable. Then the programmed data is verified by using Program Verify Mode.

If the programmed data is not correct, another program pulse of 1ms is applied and then programmed data is verified. This should be repeated until the program operates correctly (max. 25 times).

After correctly programming the selected address, one additional program pulse with pulse width 3 times that needed for programming is applied.

When programming has been completed, the data in all addresses should be verified with $V_{CC} = V_{pp} = 5 V$.

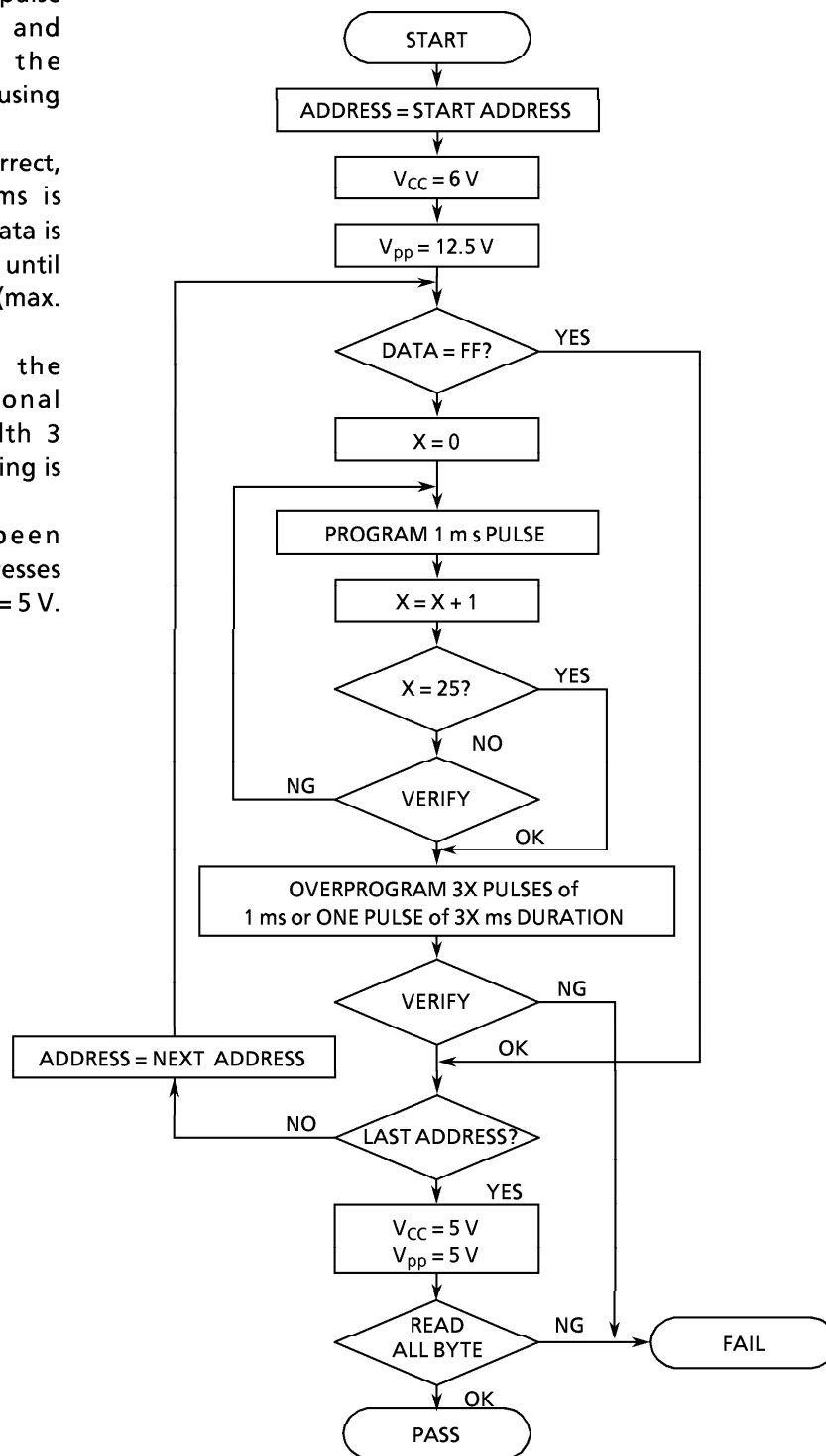


Figure 1-5. Flow Chart

ELECTRICAL CHARACTERISTICS

ABSOLUTE MAXIMUM RATINGS

 $(V_{SS} = 0 \text{ V})$

PARAMETER	SYMBOL	PINS	RATING	UNIT
Supply Voltage	V_{DD}		- 0.3 to 7	V
Program Voltage	V_{PP}	HOLD/VPP pin	- 0.3 to 14.0	V
Input Voltage	V_{IN}		- 0.3 to $V_{DD} + 0.3$	V
Output Voltage	V_{OUT1}	Except sink open drain pin	- 0.3 to $V_{DD} + 0.3$	V
	V_{OUT2}	Ports P1, P2, R7 to R9	- 0.3 to 10	
	V_{OUT3}	Port R4 (Analog inputs)	- 0.3 to $V_{DD} + 0.3$	
Output Current (Per 1 pin)	I_{OUT1}	Ports P1, P2	30	mA
	I_{OUT2}	Port R9	15	
	I_{OUT3}	Ports R4, R7, R8	3.2	
Output Current (Total)	ΣI_{OUT1}	Ports P1, P2, R9	120	mA
Power Dissipation [$T_{opr} = 70 \text{ }^\circ\text{C}$]	PD		600	mW
Soldering Temperature (time)	T_{sld}		260 (10 s)	$^\circ\text{C}$
Storage Temperature	T_{stg}		- 55 to 125	$^\circ\text{C}$
Operating Temperature	T_{opr}		- 30 to 70	$^\circ\text{C}$

RECOMMENDED OPERATING CONDITIONS

 $(V_{SS} = 0 \text{ V}, T_{opr} = - 30 \text{ to } 70 \text{ }^\circ\text{C})$

PARAMETER	SYMBOL	PINS	CONDITIONS	Min.	Max.	UNIT
Supply Voltage	V_{DD}		$f_c = 6.0 \text{ MHz}$	4.5	6.0	V
			$f_c = 4.2 \text{ MHz}$	2.7		
			In the HOLD mode	2.0		
Input High Voltage	V_{IH1}	Except Hysteresis Input	In the normal operating area	$V_{DD} \times 0.7$	V_{DD}	V
	V_{IH2}	Hysteresis Input		$V_{DD} \times 0.75$		
	V_{IH3}		In the HOLD mode	$V_{DD} \times 0.9$		
Input Low Voltage	V_{IL1}	Except Hysteresis Input	In the normal operating area	0	$V_{DD} \times 0.3$	V
	V_{IL2}	Hysteresis Input			$V_{DD} \times 0.25$	
	V_{IL3}		In the HOLD mode		$V_{DD} \times 0.1$	
Clock Frequency	f_c		$V_{DD} = 4.5 \text{ to } 6.0 \text{ V}$	0.4	6.0	MHz
			$V_{DD} = 2.7 \text{ to } 6.0 \text{ V}$		4.2	

D.C. CHARACTERISTICS

($V_{SS} = 0\text{ V}$, $T_{opr} = -30\text{ to }70\text{ }^{\circ}\text{C}$)

PARAMETER	SYMBOL	PINS	CONDITIONS	Min.	Typ.	Max.	UNIT
Hysteresis Voltage	V_{HS}	Hysteresis Input		—	0.7	—	V
Input Current	I_{IN1}	Port K0, TEST, $\overline{\text{RESET}}$, $\overline{\text{HOLD}}$	$V_{DD} = 5.5\text{ V}$, $V_{IN} = 5.5\text{ V} / 0\text{ V}$	—	—	± 2	μA
	I_{IN2}	Ports R (open drain)					
Input Resistance	R_{IN1}	Port K0 with pull-up/pull-down		30	70	150	$\text{k}\Omega$
	R_{IN2}	$\overline{\text{RESET}}$		100	220	450	
Output Leakage Current	I_{LO}	Ports R, P (open drain)	$V_{DD} = 5.5\text{ V}$, $V_{OUT} = 5.5\text{ V}$	—	—	2	μA
Output Low Voltage	V_{OL2}	Except XOUT, ports P	$V_{DD} = 4.5\text{ V}$, $I_{OL} = 1.6\text{ mA}$	—	—	0.4	V
Low Output Current	I_{OL1}	Ports P1, P2	$V_{DD} = 4.5\text{ V}$, $V_{OL} = 1.0\text{ V}$	—	20	—	mA
	I_{OL2}	Port R9		—	7	—	
Supply Current (in the Normal mode)	I_{DD}		$V_{DD} = 5.5\text{ V}$, $f_c = 4\text{ MHz}$	—	2	4	mA
			$V_{DD} = 3.0\text{ V}$, $f_c = 4\text{ MHz}$	—	1	2	
			$V_{DD} = 3.0\text{ V}$, $f_c = 400\text{ kHz}$		0.5	1	
Supply Current (in the HOLD mode)	I_{DDH}		$V_{DD} = 5.5\text{ V}$	—	0.5	10	μA

Note 1. Typ. values show those at $T_{opr} = 25\text{ }^{\circ}\text{C}$, $V_{DD} = 5\text{ V}$.

Note 2. Input Current I_{IN1} ; The current through resistor is not included, when the input resistor (pull-up / pull-down) is contained.

Note 3. Supply Current I_{DD} , I_{DDH} ; $V_{IN} = 5.3\text{ V} / 0.2\text{ V}$
The K0 port is open when the input resistor is contained. The voltage applied to the R port is within the valid range.

A / D CONVERSION CHARACTERISTICS

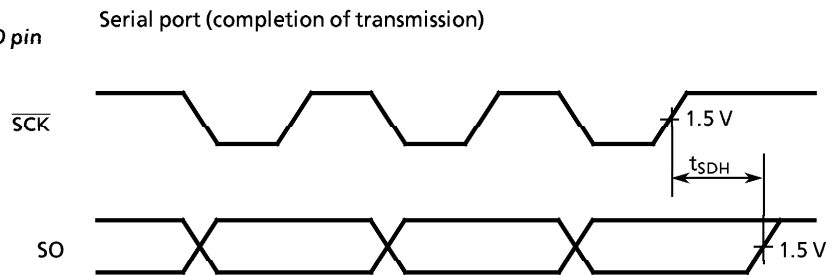
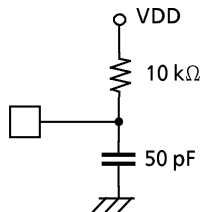
($T_{opr} = -30\text{ to }70\text{ }^{\circ}\text{C}$)

PARAMETER	SYMBOL	CONDITIONS	Min.	Typ.	Max.	UNIT
Analog Reference Voltage	V_{AREF}		$V_{DD} - 1.5$	—	V_{DD}	V
Analog Reference Voltage Range	ΔV_{AREF}	$V_{AREF} - V_{SS}$	2.7	—	—	V
Analog Input Voltage	V_{AIN}		V_{SS}	—	V_{AREF}	V
Analog Supply current	I_{REF}		—	0.5	1.0	mA
Nonlinearity Error		$V_{DD} = 5.0\text{ V}$, $V_{SS} = 0.0\text{ V}$ $V_{AREF} = 5.000\text{ V}$ $V_{ASS} = 0.000\text{ V}$	—	—	± 1	LSB
Zero Point Error			—	—	± 1	
Full Scale Error			—	—	± 1	
Total Error			—	—	± 2	

A. C. CHARACTERISTICS ($V_{SS} = 0\text{ V}$, $T_{opr} = -30\text{ to }70\text{ }^{\circ}\text{C}$)

PARAMETER	SYMBOL	CONDITIONS	Min.	Typ.	Max.	UNIT
Instruction Cycle Time	t_{cy}	$V_{DD} = 4.5\text{ to }6.0\text{ V}$	1.3	—	20	μs
		$V_{DD} = 2.7\text{ to }6.0\text{ V}$	1.9			
High level Clock pulse Width	t_{WCH}	External clock mode	80	—	—	ns
Low level Clock pulse Width	t_{WCL}					
A/D Sampling Time	t_{AIN}	$f_c = 4\text{ MHz}$	—	4	—	μs
Shift Data Hold Time	t_{SDH}		$0.5 t_{cy} - 300$	—	—	ns

Note. Shift Data Hold Time
External circuit for $\overline{\text{SCK}}$ pin and SO pin



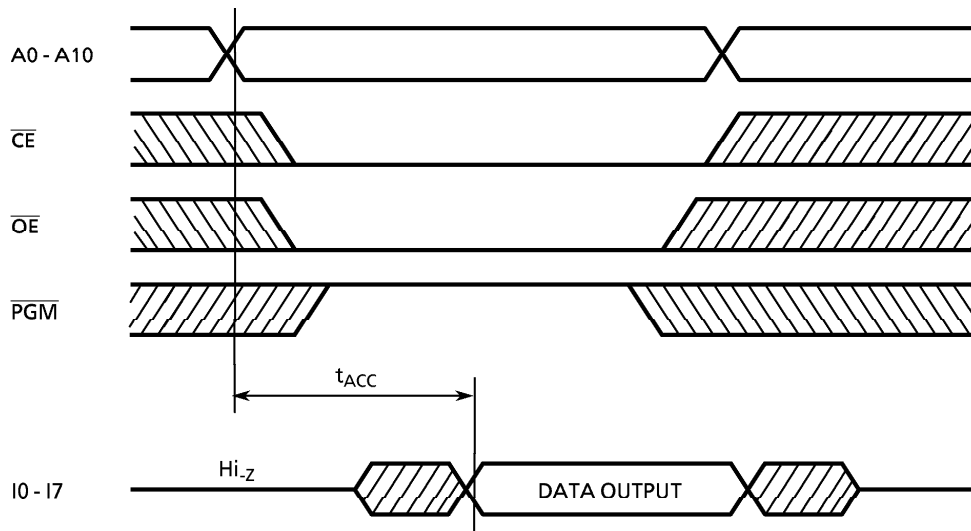
RECOMMENDED OSCILLATING CONDITIONS ($V_{SS} = 0\text{ V}$, $V_{DD} = 4.5\text{ to }6.0\text{ V}$, $T_{opr} = -40\text{ to }70\text{ }^{\circ}\text{C}$)

Recommended oscillating conditions of the 47P241V are equal to the 47C241's but RC oscillation is impossible.

DC/AC CHARACTERISTICS ($V_{SS} = 0\text{ V}$)

(1) Read Operation

PARAMETER	SYMBOL	CONDITION	Min.	Typ.	Max.	UNIT
Output Level High Voltage	V_{IH4}		$V_{CC} \times 0.7$	—	V_{CC}	V
Output Level Low Voltage	V_{IL4}		0	—	$V_{CC} \times 0.3$	V
Supply Voltage	V_{CC}		4.75	—	6.0	V
Programming Voltage	V_{PP}					
Address Access Time	t_{ACC}	$V_{CC} = 5.0 \pm 0.25\text{ V}$	0	—	350	ns



(2) High Speed Programming Operation

PARAMETER	SYMBOL	CONDITION	Min.	Typ.	Max.	UNIT
Input High Voltage	V_{IH4}		$V_{CC} \times 0.7$	-	V_{CC}	V
Input Low Voltage	V_{IL4}		0	-	$V_{CC} \times 0.3$	V
Supply Voltage	V_{CC}		4.75	-	6.0	V
V_{PP} Power Supply Voltage	V_{PP}		12.00	12.50	13.00	V
Programming Pulse Width	t_{PW}	$V_{CC} = 6.0 \pm 0.25$ V	0.95	1.0	1.05	ms

