



# STK402-050

## Two-Channel Class AB Audio Power Amplifier IC 30 W + 30 W

### Overview

The STK402-000 series products are audio power amplifier hybrid ICs that consist of optimally-designed discrete component power amplifier circuits that have been miniaturized using SANYO's unique insulated metal substrate technology (IMST). SANYO has adopted a new low thermal resistance substrate in these products to reduce the package size by about 60% as compared to the earlier SANYO STK407-000 series.

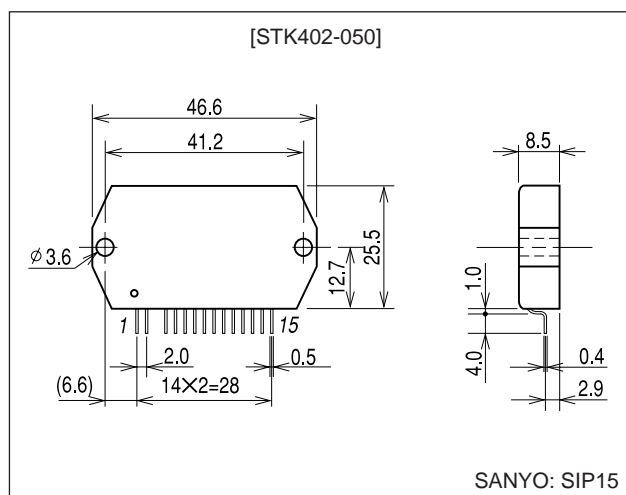
### Features

- Series of pin compatible power amplifiers ranging from 20 W × 2 channels to 120 W × 2 channels (10%/1 kHz) devices. The same printed circuit board can be used depending on the output power grade.
  - The pin arrangement is compatible with that of the 3-channel STK402-200 series. This means that 3-channel printed circuit boards can also be used for 2-channel products.
  - Miniature packages
    - 15 W/ch to 40 W/ch (THD = 0.4%,  $f = 20$  Hz to 20 kHz); 46.6 mm × 25.5 mm × 8.5 mm \*
    - 50 W/ch to 80 W/ch (THD = 0.4%,  $f = 20$  Hz to 20 kHz); 59.2 mm × 31.0 mm × 8.5 mm \*
- \*: Not including the pins.
- Output load impedance:  $R_L = 6 \Omega$
  - Allowable load shorted time: 0.3 seconds
  - Supports the use of standby, muting, and load shorting protection circuits.

### Package Dimensions

unit: mm

#### 4189-SIP15



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## Series Organization

These products are organized as a series based on their output capacity.

Item	Type No.							
	STK402-020	STK402-030	STK402-040	STK402-050	STK402-070	STK402-090	STK402-100	STK402-120
Output 1 (10%/1 kHz)	20 W + 20 W	30 W + 30 W	40 W + 40 W	45 W + 45 W	60 W + 60 W	80 W + 80 W	100 W + 100 W	120 W + 120 W
Output 2 (0.4%/20 Hz to 20 kHz)	15 W + 15 W	20 W + 20 W	25 W + 25 W	30 W + 30 W	40 W + 40 W	50 W + 50 W	60 W + 60 W	80 W + 80 W
Maximum supply voltage (No signal)	±30 V	±34 V	±38 V	±40 V	±50 V	±54 V	±57 V	±65 V
Maximum supply voltage (6 Ω)	±28 V	±32 V	±36 V	±38 V	±44 V	±47 V	±50 V	±57 V
Recommended supply voltage (6 Ω)	±19 V	±22 V	±25 V	±26.5 V	±30 V	±32 V	±35 V	±39 V
Package	46.6 mm × 25.5 mm × 8.5 mm					59.2 mm × 31.0 mm × 8.5 mm		

## Specifications

### Maximum Ratings at Ta = 25°C

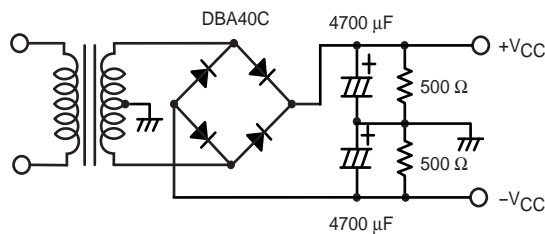
Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage (No signal)	V <sub>CC</sub> max(0)		±40	V
Maximum supply voltage	V <sub>CC</sub> max(1)	R <sub>L</sub> = 6 Ω	±38	V
Thermal resistance	θj-c	Per power transistor	3.6	°C/W
Junction temperature	Tj max	Both the Tj max and the Tc max conditions must be met.	150	°C
Operating IC substrate temperature	Tc max		125	°C
Storage temperature	Tstg		−30 to +125	°C
Allowable load shorted time *2	ts	V <sub>CC</sub> = ±26.5 V, R <sub>L</sub> = 6 Ω, f = 50 Hz, P <sub>O</sub> = 30 W	0.3	s

### Operating Characteristics at Tc = 25°C, R<sub>L</sub> = 6 Ω (noninductive load), Rg = 600 Ω, VG = 30 dB

Parameter	Symbol	Conditions*1					Ratings			Unit
		V <sub>CC</sub> (V)	f (Hz)	P <sub>O</sub> (W)	THD (%)		min	typ	max	
Output power (continuous output)	P <sub>O</sub> (1)	±26.5	20 to 20 k		0.4		27	30		W
	P <sub>O</sub> (2)	±26.5	1 k		10			45		
Total harmonic distortion	THD (1)	±26.5	20 to 20 k	1.0					0.4	%
	THD (2)	±26.5	1 k	5.0				0.01		
Frequency characteristics	f <sub>L</sub> , f <sub>H</sub>	±26.5		1.0		+0 −3 dB		20 to 50 k		Hz
Input impedance	ri	±26.5	1 k	1.0				55		kΩ
Output noise voltage *3	V <sub>NO</sub>	±32.0				Rg = 2.2 kΩ			1.2	mVrms
Quiescent current	I <sub>CCO</sub>	±32.0					20	50	80	mA
Neutral voltage	V <sub>N</sub>	±32.0					−70	0	+70	mV

Notes: 1. Unless otherwise noted, use a constant-voltage supply for the power supply used during inspection.

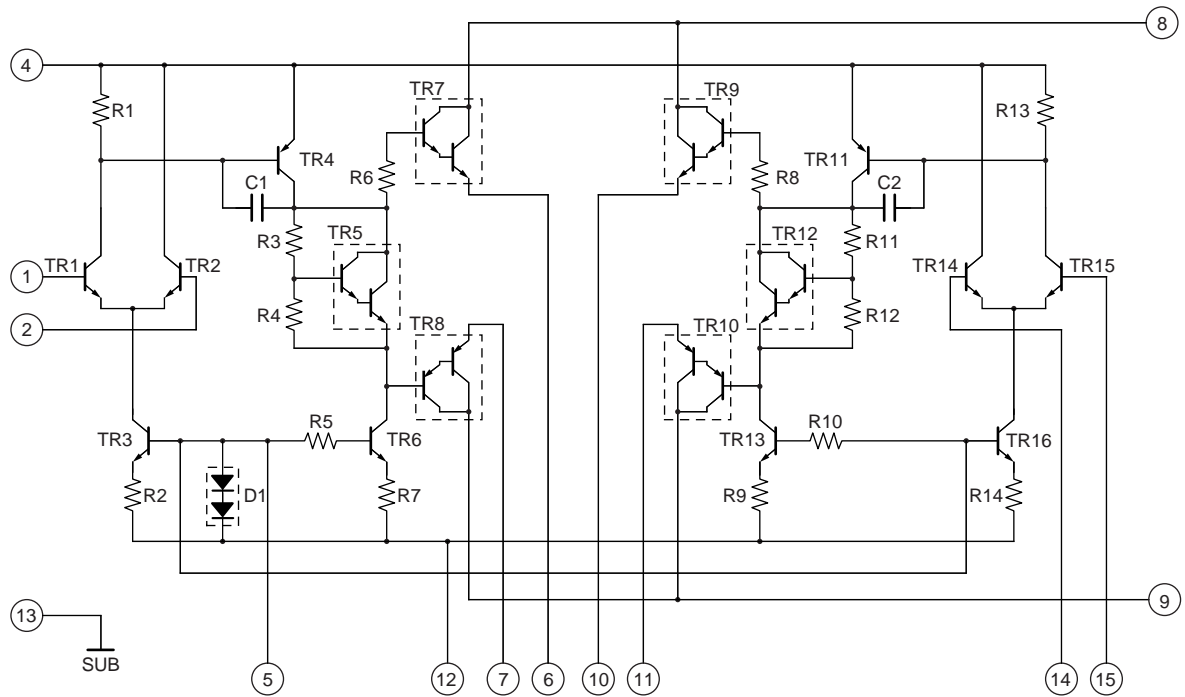
2. Use the transformer power supply circuit stipulated in the figure below for allowable load shorted time measurement and output noise voltage measurement..



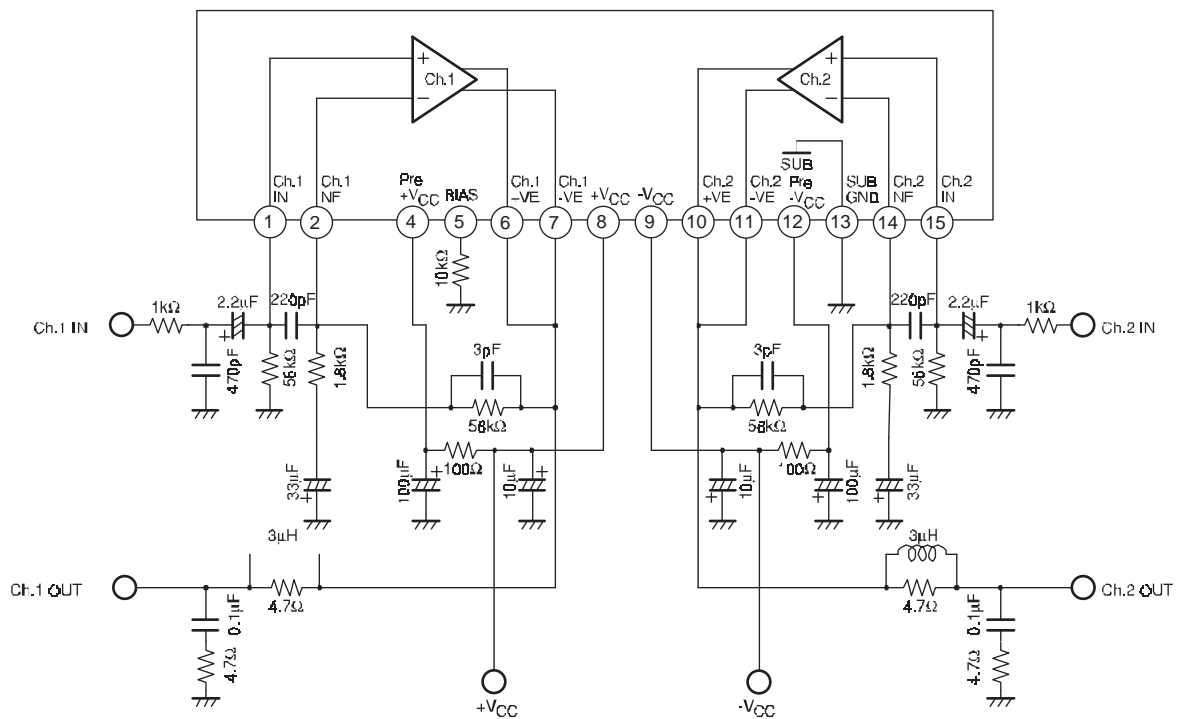
### Stipulated Transformer Power Supply (RP-25 equivalent)

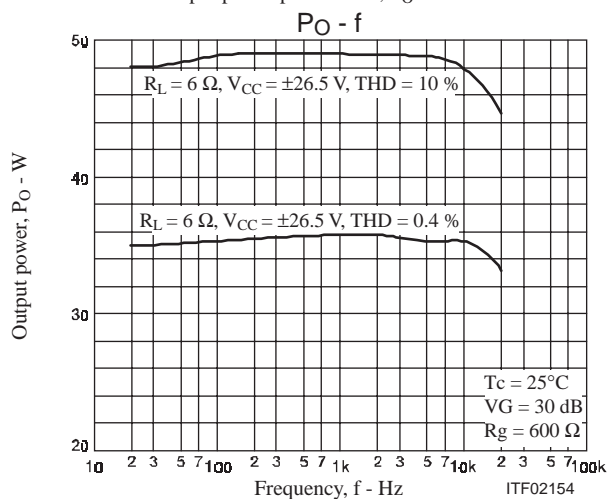
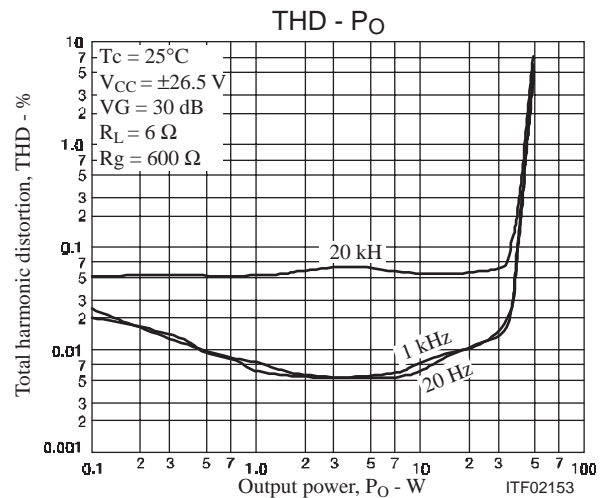
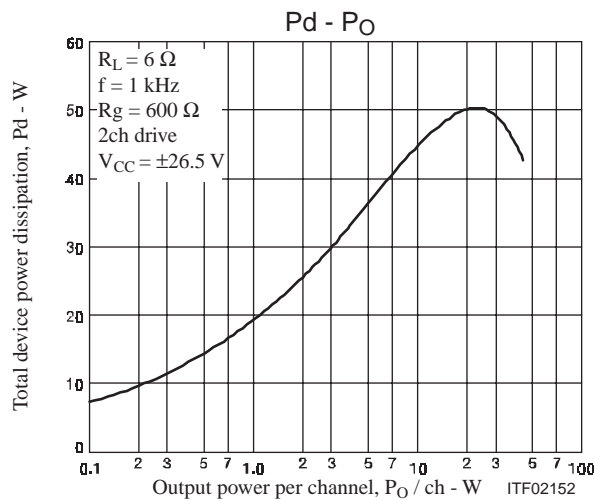
3. The output noise voltage values shown are peak values read with a VTVM. However, an AC stabilized (50 Hz) power supply should be used to minimize the influence of AC primary side flicker noise on the reading.

# Internal Equivalent Circuit



# Sample Application Circuit





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