

- ◆ CMOS
- ◆ Maximum Output Current: 100mA ($V_{OUT}=5.0V$)
- ◆ Highly Accurate Output Voltage: $\pm 2\%$
- ◆ Output Voltage Range: 2.0V~ 6.0V
- ◆ Highly Accurate Voltage Detection: $\pm 2\%$
- ◆ No Load Supply Current: 3.2 μ A(5.0V)
- ◆ SOT-25 Package

Applications

- Battery use
- Battery life & charge detection
- Memory battery back-up circuits
- Microprocessor reset circuitry
- Power failure detection
- Voltage Sources Reference
- Cameras, Video Cameras
- Various Portable Devices

General Description

The XC66D series consists of a voltage detector and voltage regulator built into the one chip and is, essentially, a voltage regulator with voltage detect capabilities.

Low power consumption and high accuracy is achieved through CMOS and laser trimming technologies.

The detector features an output driver, hysteresis circuit, comparator and extremely accurate standard voltage.

The regulator features an error correction circuit, output driver with current limiter functions, minimal input-output voltage differential and similarly accurate standard voltage.

SOT-25 (150mW) package is available.

Features

Max. output current: 100mA (Within max. continuous total power dissipation, $V_{OUT}=5.0V$)

Output voltage range: 2.0V ~ 6.0V programmable in 0.1V steps (Standard 5.0V)

Highly accurate output voltage: Set-up voltage accuracy $\pm 2\%$

Output voltage temp. characteristics: Typ. $\pm 100\text{ppm}/^\circ\text{C}$

Detect voltage range: 1.8V ~ 6.0V programmable in 0.1V steps

Highly accurate detect voltage: Set-up voltage accuracy $\pm 2\%$

Low power consumption: Typ. 3.2 μ A ($V_{OUT}= 5.0V$)

Detect voltage temp. characteristics: Typ. $\pm 100\text{ppm}/^\circ\text{C}$

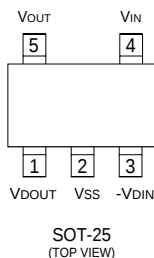
Detect voltage output configuration: N-ch open drain

CMOS (High level = V_{OUT})

Input stability: Typ. 0.1%/V

Ultra small package: SOT- 25 (150mW) mini-mold

Pin Configuration

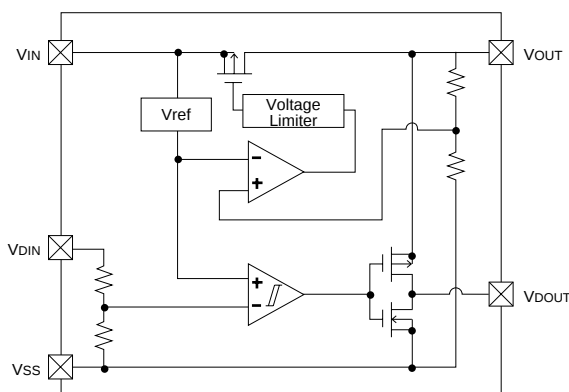


Pin Assignment

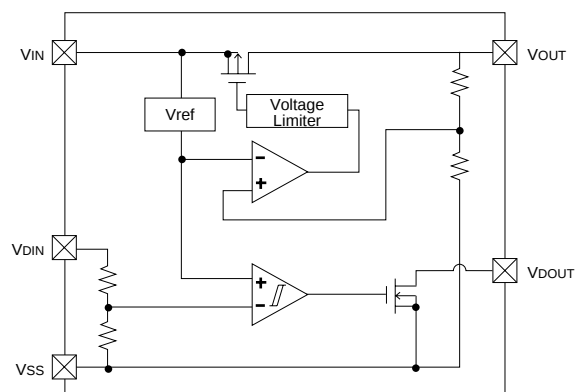
PIN NUMBER	PIN NAME	FUNCTION
1	V_{DOUT}	Voltage Detect Output
2	V_{SS}	Ground
3	V_{DIN}	Voltage Detect Input
4	V_{IN}	Power Supply
5	V_{OUT}	Voltage Regulator Output

Block Diagram

(1) XC66DC CMOS output (High level = V_{OUT})



(2) XC66DN N-ch open drain



Absolute Maximum Ratings

Ta=25°C

PARAMETER		SYMBOL	RATINGS	UNITS
Regulator Input Voltage		V _{IN}	V _{SS} -0.3 ~ 12	V
Regulator Output Current		I _{OUT}	150	mA
Regulator Output Voltage		V _{OUT}	V _{SS} -0.3 ~ V _{IN} +0.3	V
Detector Input Voltage		V _{DIN}	V _{SS} -0.3 ~ 12	V
Detector Output Current		I _{DOUT}	50	mA
Detector Output Voltage	N-ch open drain output	V _{DOUT}	V _{SS} -0.3 ~ 12	V
	CMOS output (High level =V _{OUT})		V _{SS} -0.3 ~ V _{IN} +0.3	
Continuous Total Power Dissipation		P _d	150	mW
Operating Ambient Temperature		T _{opr}	-30 ~ +80	°C
Storage Temperature		T _{stg}	-40 ~ +125	°C

Note: Please ensure that {(V_{IN} - V_{OUT}) × I_{OUT}} + {I_{DOUT} × V_{DOUT}} + {V_{IN} × I_{SS}} does not exceed the stated P_d values.

Electrical Characteristics

V_{DF} =1.8V ~ 6.0V, V_{OUT}(T) (Note1) =5V

Ta=25°C

	PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	CIRCUIT
DETECTOR	Detect Voltage	V _{DF}	V _{IN} = V _{DIN}	(V _{DF}) × 0.98	V _{DF}	(V _{DF}) × 1.02	V	1
	Hysteresis Range	V _{HYS}	V _{IN} = V _{DIN}	V _{DF} × 0.02	V _{DF} × 0.05	V _{DF} × 0.08	V	1
	Operating Voltage	V _{IN}		1.5	—	10.0	V	—
	Output Current	I _{DOUT}	N-ch V _{DS} =0.5V V _{SS} =1.5V =2.0V =3.0V =4.0V =5.0V	0.3 3.0 5.0 6.0 7.0	2.2 7.7 10.1 11.5 13.0	— — — — —	mA	2
			P-ch V _{DS} =0.9V V _{IN} =V _{DIN} =V _{OUT} =8V (CMOS)	—	-5	-2		6
	V _{DIN} Input Current	I _{DIN}	V _{DIN} =10V	—	0.4	1.0	μA	3
	Detect Voltage Temp. Characteristics	$\frac{\Delta V_{OUT}}{\Delta T_{opr} \cdot V_{DF}}$	-30°C ≤ T _{opr} ≤ 80°C	—	±100	—	ppm/°C	1
REGULATOR	Output Voltage	V _{OUT} (E) (Note2)	I _{OUT} =10mA V _{IN} = V _{OUT} (T) + 1V	4.9	5	5.1	V	4
	Maximum Output Current	I _{OUTmax}	V _{IN} = V _{OUT} (T) + 1V V _{OUT} (E) ≥ V _{OUT} (T) × 0.9	100	—	—	mA	4
	Load Stability	ΔV _{OUT}	V _{IN} = V _{OUT} (T) + 1V 1mA ≤ I _{OUT} ≤ 80mA	—	20	80	mV	4
	Input - Output Voltage Differential	V _{dif} (Note3)	I _{OUT} =60mA	—	260	520	mV	4
	Input Stability	$\frac{\Delta V_{OUT}}{\Delta V_{IN} \cdot V_{OUT}}$	I _{OUT} =10mA V _{OUT} (T) + 1V ≤ V _{IN} ≤ 10V	—	0.1	0.3	%/V	4
	Output Voltage Temp. Characteristics	$\frac{\Delta V_{OUT}}{\Delta T_{opr} \cdot V_{OUT}}$	I _{OUT} =10mA -30°C ≤ T _{opr} ≤ 80°C	—	±100	—	ppm/°C	4
	Input Voltage	V _{IN}		—	—	10.0	V	—
	Supply Current	I _{SS}	V _{IN} = V _{OUT} (T) +1V	—	3.2	8.6	mA	5

Note: 1. V_{OUT}(T) : User specified output voltage.

2. V_{OUT}(E) : Effective output voltage.

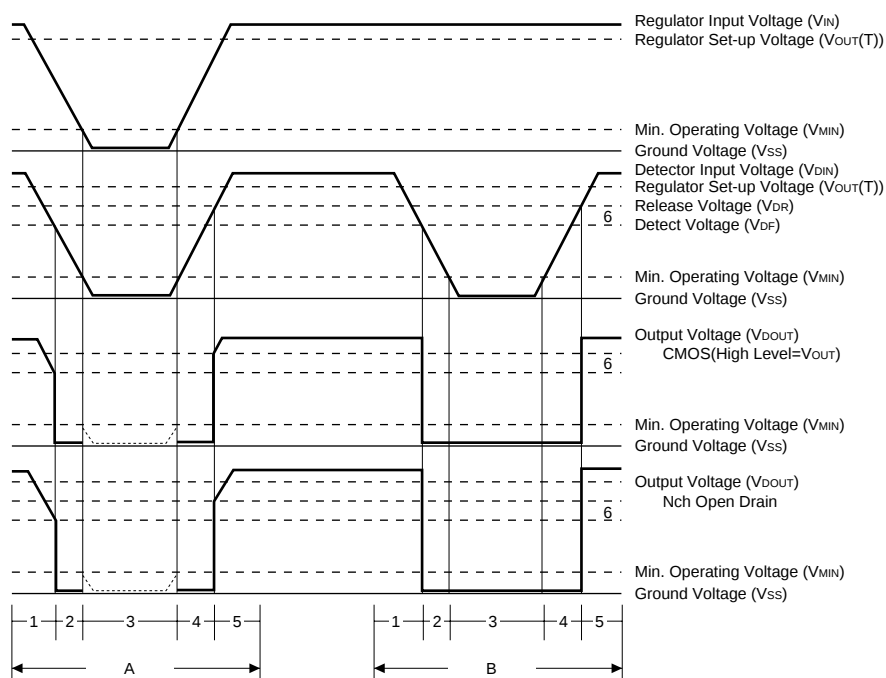
(i.e. the output voltage when a stable (V_{OUT}(T) + 1.0V) is provided, while maintaining a certain I_{OUT} value.)

3. V_{dif} : V_{dif} = {V_{IN1} - V_{OUT1}}

V_{OUT1} : The voltage equal to 98% of the output voltage whenever a stable (V_{OUT}(T) +1.0V) is provided at I_{OUT}.

V_{IN1} : The input voltage when the output is equal to V_{OUT}(E) × 98%.

■ Timing Chart (Nch open drain pull up voltage = Input voltage V_{IN})



■ Operational Notes [Detector : CMOS Output ('High' level = V_{OUT})]

Timing Chart A ($V_{IN}=V_{DIN}$)

- When a voltage greater than the release voltage (V_{DR}) is applied to the voltage input pin (V_{IN} , V_{DIN}), input voltage (V_{IN} , V_{DIN}) will gradually fall.
When a voltage greater than the detect voltage (V_{DF}) is applied to the voltage input pin (V_{IN} , V_{DIN}), the output pin (V_{DOUT}) voltage will be equal to the regulator's output voltage (V_{OUT}).
*With Nch open drain configurations a state of high impedance means that should the pin be pulled up, voltage will be equal to pull up voltage.
- When input voltage (V_{IN} , V_{DIN}) fall below detect voltage (V_{DF}), output voltage (V_{DOUT}) will be equal to ground level (V_{SS}).
- Should input voltage (V_{IN} , V_{DIN}) fall below the minimum operational voltage (V_{MIN}), output will become unstable.
Should V_{DIN} fall below V_{MIN} , voltage at the output pin (V_{DOUT}) will be equal to ground level (V_{SS}).
*With Nch open drain configurations output will equal pull up voltage as the output pin is generally pulled up.
- Should input voltage (V_{IN} , V_{DIN}) rise above ground voltage (V_{SS}), output voltage (V_{DOUT}) will equal ground level until the release voltage level (V_{DR}) is reached.
- The output pin voltage (V_{DOUT}) will be equal to the regulator output voltage (V_{OUT}) when input voltage (V_{IN} , V_{DIN}) rises above release voltage.
*With Nch open drain configurations it will be equal to the voltage dependent on pull up.

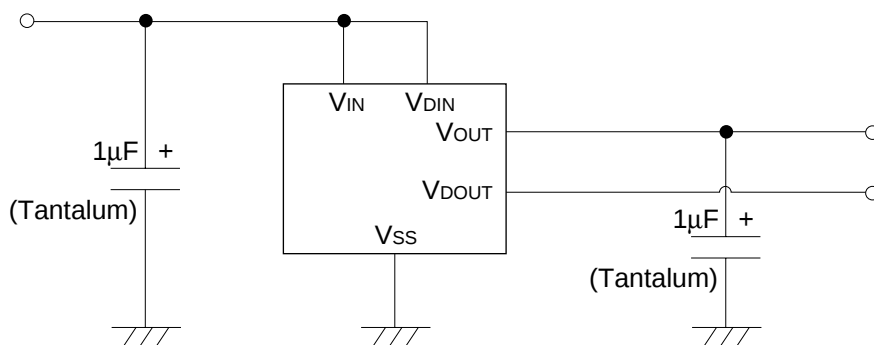
Timing Chart B (V_{IN} =voltages above set-up voltage + input/output voltage differential, V_{DIN} = sweep voltage)

Because a voltage higher than the minimum operating voltage is applied to the voltage input pin (V_{IN}), ground voltage will be output at the output pin (V_{DOUT}) during stage 3. (Stages 1, 2, 4, 5 are the same as in A above).

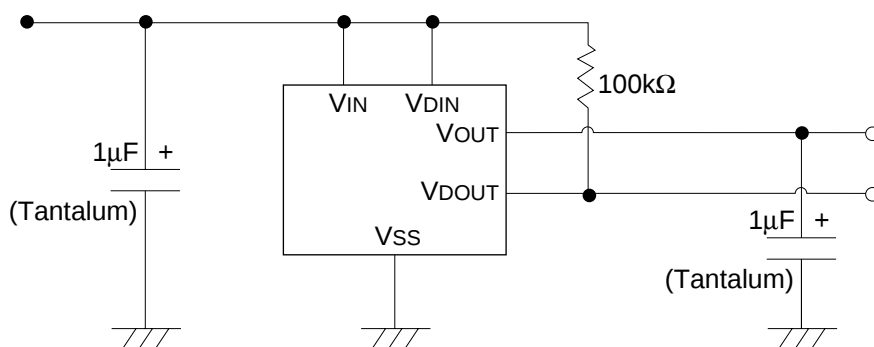
Note : The difference between release voltage (V_{DR}) and detect voltage (V_{DF}) is the Hysteresis Range (6).

Standard Circuit

CMOS Output (High Level = V_{OUT})



Nch Open Drain



Notes on Use

1. In cases where there is no capacitance (C_L), or the capacitance is small, or where a capacitor with an extremely low ESR value is used (e.g. ceramic), please use a capacitor ($C_L = 1.0\mu F$ [Tantalum]) in order to stop oscillation that may occur as the phase margin becomes smaller.
2. To reduce impedance between the power supply and the IC's input pin, which in turn will stop oscillation resulting from input voltage changes, connect a capacitor ($C_{IN} = \text{more than } 1.0\mu F$, ESR low) to the input side of the IC. Further, operation may become unstable and oscillation may occur should impedance up to the IC's input be high (a state which could be brought about by several factors including which devices are added to the input side, the surrounding wiring and/or the input power supply.) Stability can be improved by regulating increases in input capacitance and by reducing impedance.
3. The regulator's input pin (V_{IN}) and power supply pin are the same. Also, the voltage detector's power supply and the voltage regulator's power supply are the same. Therefore, to have the voltage detector operating normally, it is necessary to apply a voltage larger than the minimum operating voltage (1.5V) to the power supply input pin (V_{IN}).
4. With CMOS output, the detector's output voltage equals the regulator's output voltage following release. Possible changes in the regulator's output voltage (V_{ROUT}), following regulator load changes, will be output at the detector's output pin (V_{DOUT}).
5. As the operations of the detector will momentarily respond when steep rise and fall time voltages are input at the power supply pin (V_{IN}), please ensure that the $V_{IN}(V_{DIN})$ pin's input frequency's rise and fall time is more than $5\mu \text{ sec/V}$.
6. When using with the detector input pin (V_{DIN}) connected to the regulator output pin (V_{OUT}), the detector will momentarily respond as a result of transient output voltage changes brought about by the regulator's load changes. With large load currents and/or large load transitions from 1mA to 80mA for example, output voltage will momentarily drop, so please add a capacitor where $C_L = \text{more than } 4.7mF$.

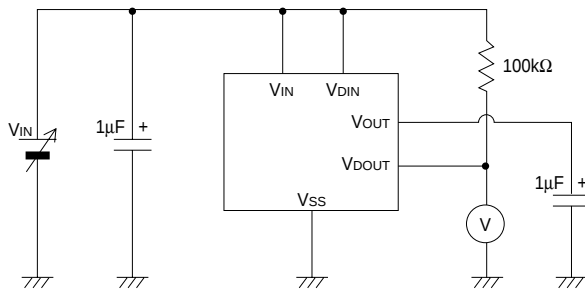
■ Ordering Information

XC66Dxxxxxx
↑ ↑ ↑ ↑
a b c d e

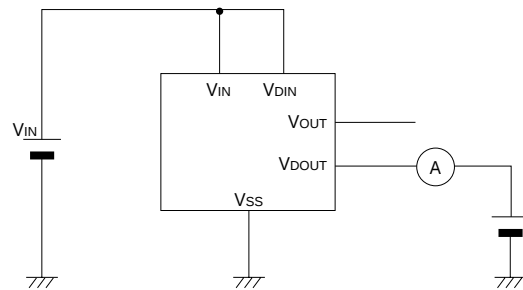
DESIGNATOR	DESCRIPTION	DESIGNATOR	DESCRIPTION
a	<u>Detector Output Configuration</u> C=CMOS(High level=V _{OUT}) N=N-ch open drain	d	<u>Package Type</u> M=SOT-25
b	<u>Detect Voltage (V_{DF})</u> 25=2.5V 35=3.8V	e	<u>Device Orientation</u> R=Embossed Tape (Orientation of Device:Right) L=Embossed Tape (Orientation of Device:Left)
c	<u>Regulator Output Voltage (V_{OUT})</u> 33=3.3V 50=5.0V		

Measuring Circuits

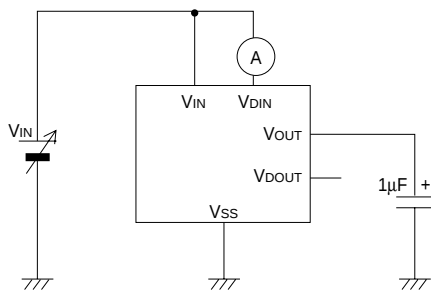
Circuit 1



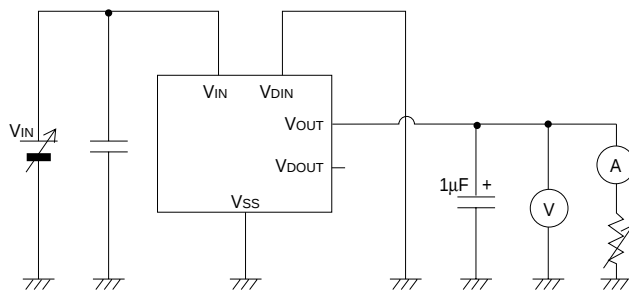
Circuit 2



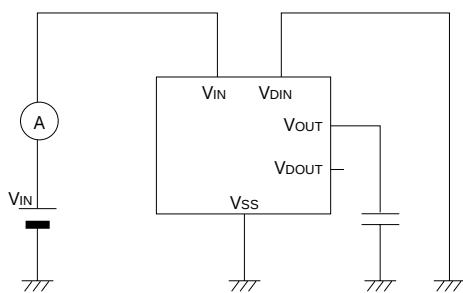
Circuit 3



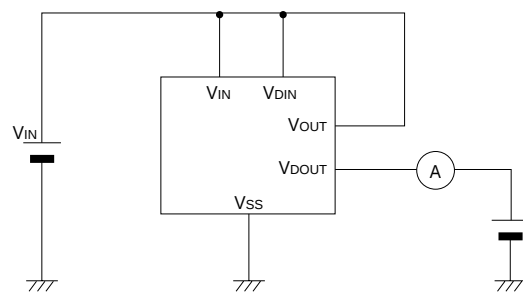
Circuit 4



Circuit 5

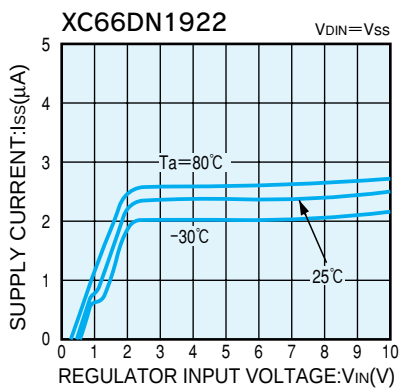


Circuit 6

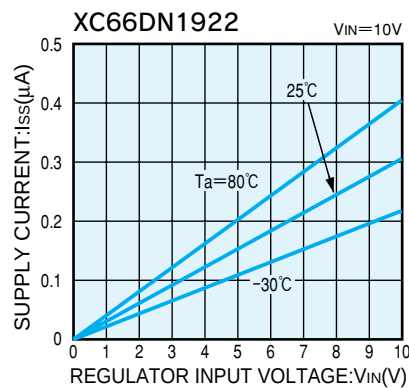


XC66D Electrical Characteristics

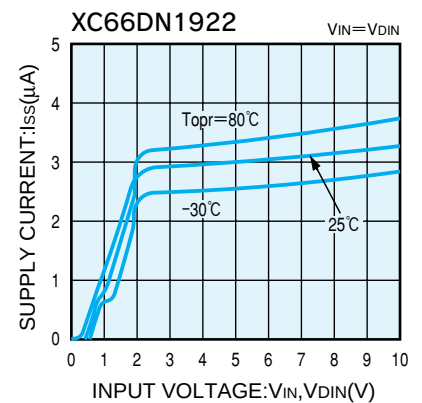
(1) SUPPLY CURRENT vs. REGULATOR INPUT VOLTAGE



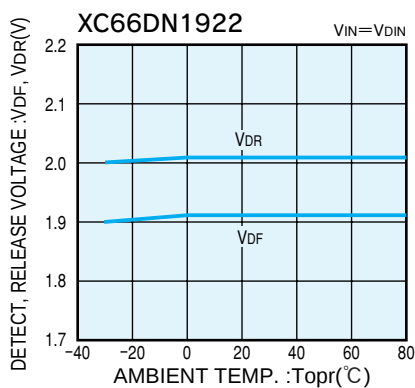
(2) SUPPLY CURRENT vs. DETECTOR INPUT VOLTAGE



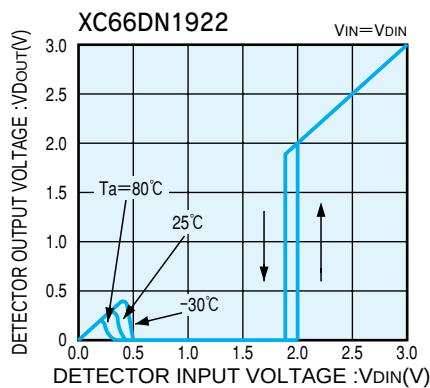
(3) SUPPLY CURRENT vs. REGULATOR, DETECTOR INPUT VOLTAGE



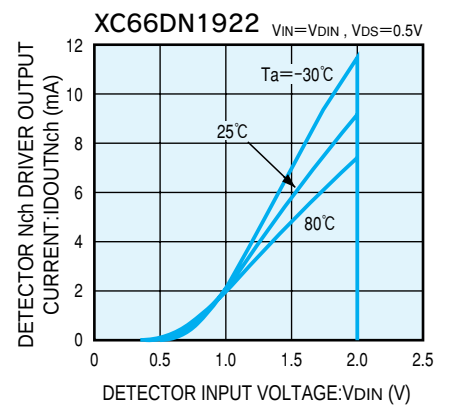
(4) DETECT VOLTAGE, RELEASE VOLTAGE vs. AMBIENT TEMP.



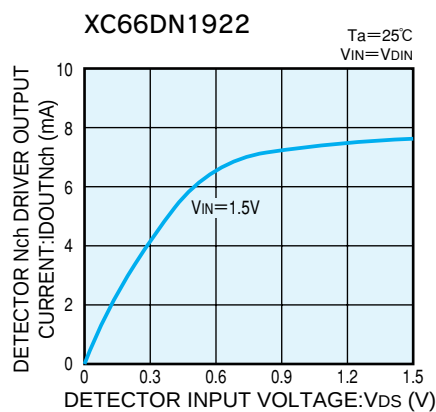
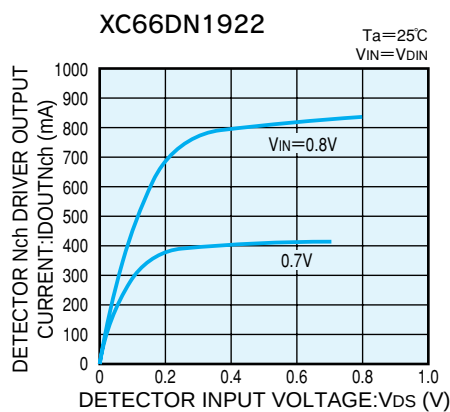
(5) DETECTOR OUTPUT VOLTAGE vs. DETECTOR INPUT VOLTAGE



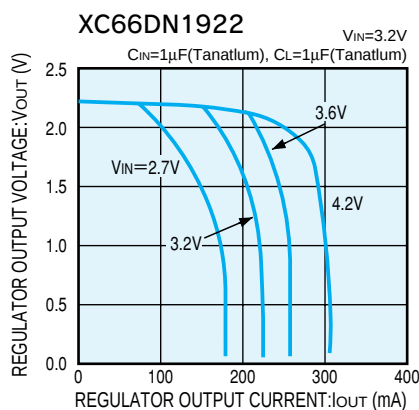
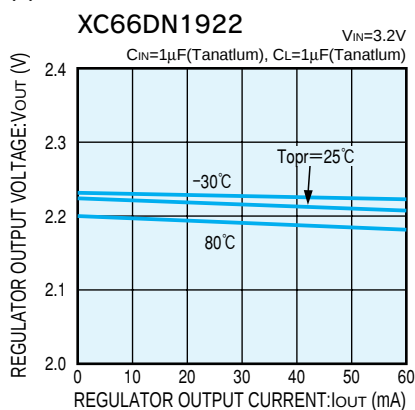
(6) DETECTOR Nch DRIVER OUTPUT CURRENT vs. V_{DS}



(7) DETECTOR Nch DRIVER OUTPUT CURRENT vs. DETECTOR INPUT VOLTAGE

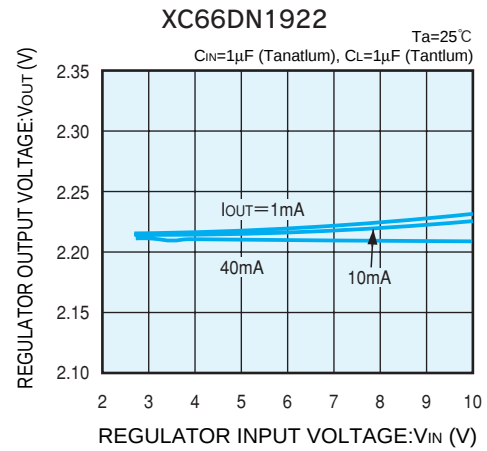
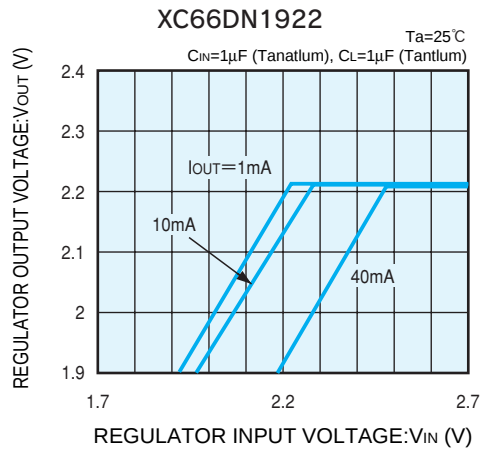


(8) REGULATOR OUTPUT VOLTAGE vs. REGULATOR OUTPUT CURRENT

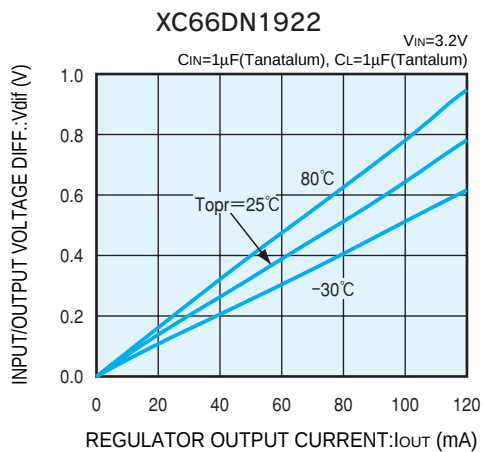


XC66D Electrical Characteristics

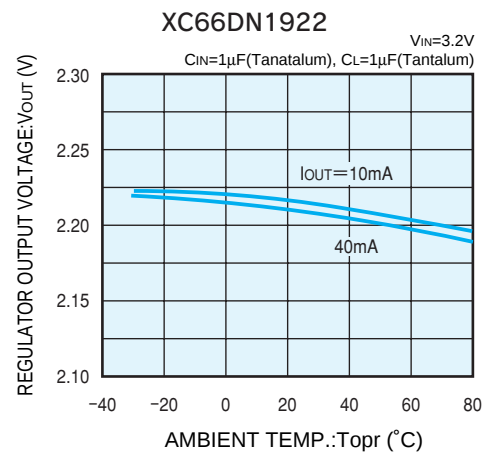
(9) REGULATOR OUTPUT VOLTAGE vs. REGULATOR INPUT VOLTAGE



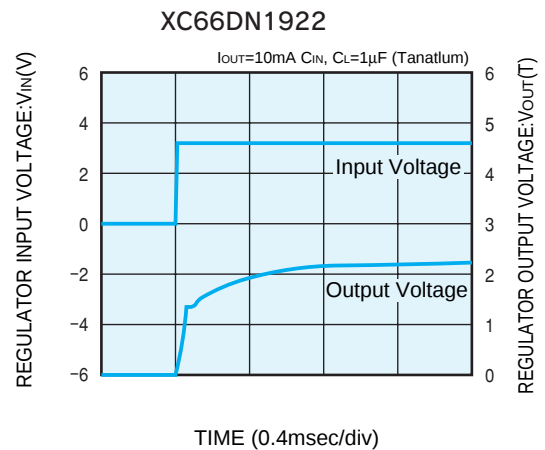
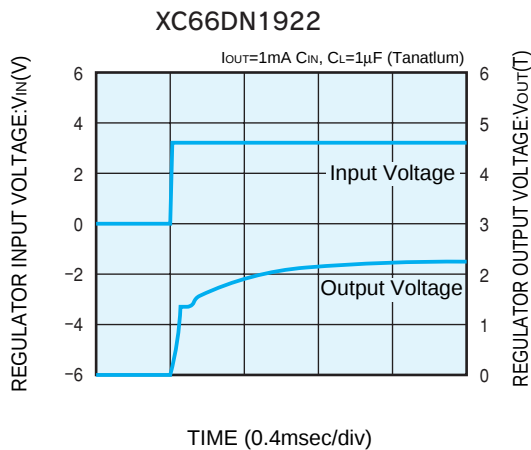
(10) INPUT/OUTPUT VOLTAGE DIFFERENTIAL vs. REGULATOR OUTPUT CURRENT



(11) REGULATOR OUTPUT VOLTAGE vs. AMBIENT TEMP.

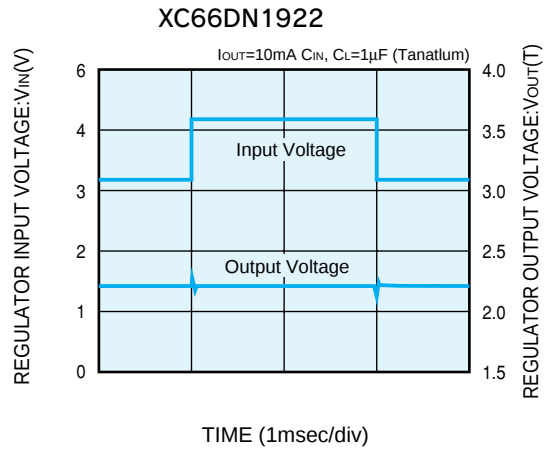
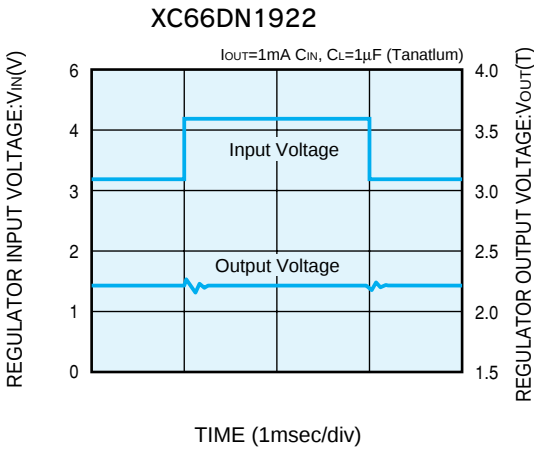


(12) INPUT TRANSIENT RESPONSE 1

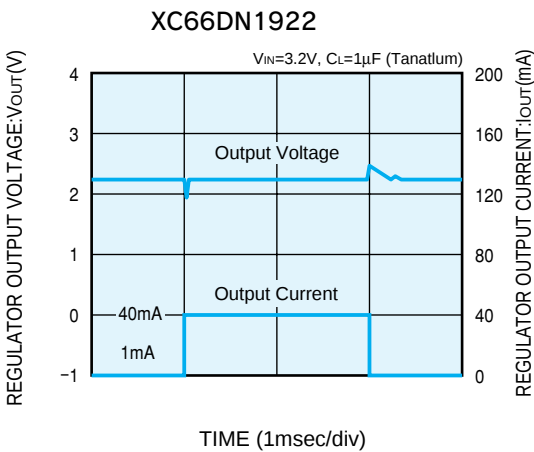


■ XC66D Electrical Characteristics

(13) INPUT TRANSIENT RESPONSE 2



(14) LOAD TRANSIENT RESPONSE



(15) RIPPLE REJECTION RATE

