

Structure : Silicone monolithic integrated circuit
 Product Name : Sound processor for car audio
 Model Name : **BD3443FS**
 Package : SSOP-A20

●Features

1. Reduce the switching noise of Primary / Fader Volume attenuation and Tone by using soft switching circuit.
2. Taking in a filter of bass and treble inside can reduce the external parts.
3. Bi-CMOS process
4. Reduce the noise of through mode by using tone-pass route.
5. Built-in ground isolation amplifier input, ideal for external stereo input.
6. The package of this IC is SSOP-A20. It gathers a sound input terminals, sound output terminals respectively and it arranges them, to be arranging facilitates the laying-out of PCB pattern and reduces PCB area to one-way in the flow of the signal.

●Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Power supply Voltage	VCC	10.0	V
Input Voltage	VIN	VCC+0.3~GND-0.3	V
Power Dissipation	Pd	940 *1	mW
Storage Temperature	Tastg	-55~+150	°C

*1 At Ta=25°C or higher, this value is decreased to 7.5mW/°C.

When Rohm standard board is mounted.

Rohm standard board: size: 70×70×1.6 (mm³)

material: FR4 glass-epoxy substrate (copper foil area: not more than 3%).

●Operating Range

Parameter	Symbol	Min.	Typ.	Max.	Unit
Power supply voltage	VCC	7.0	-	9.5	V
Temperature	Topr	-40	-	+85	°C

※ Design against radiation-proof isn't made.

Status of this document

The Japanese version of this document is the formal specification. A customer may use this translation only for a reference to help reading the formal version. If there are any differences in translation version of this document, formal version takes priority.

Application example

- ROHM cannot provide adequate confirmation of patents.
- The product described in this specification is designed to be used with ordinary electronic equipment or device (such as audio-visual equipment, office-automation equipment, communications devices, electrical appliances, and electronic toys.)
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●Function

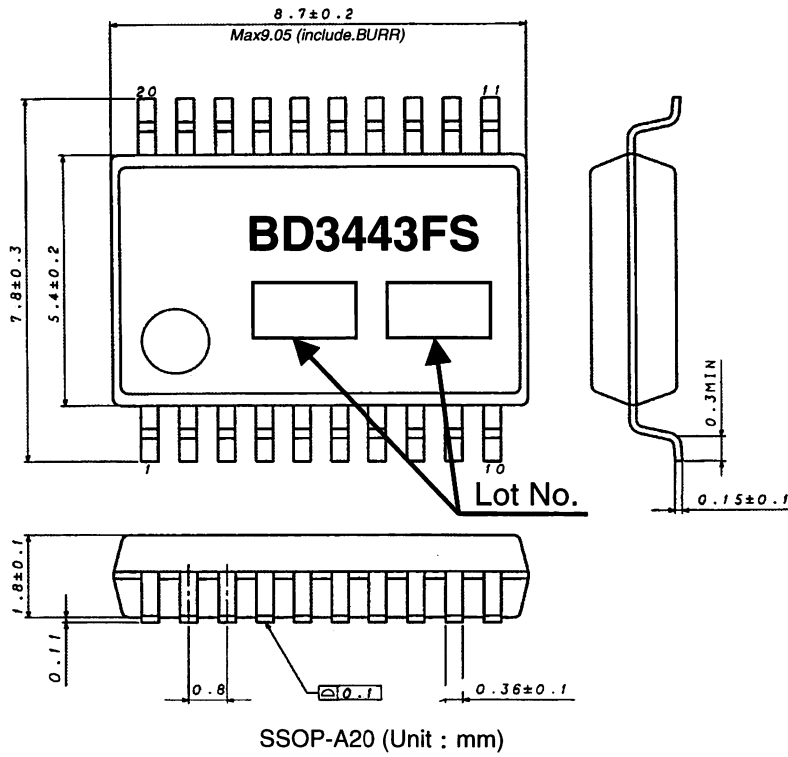
Function	Specifications
Input selector	Stereo 4 input, D input can be switched between single and differential input
Input gain	0~20dB, 1dB step
Primary volume	+12dB~-40dB(1dB step), Possible to use soft switching
Bass	-20~+20dB(1dB step), Q=1, f0=100Hz Possible to use soft switching
Treble	-20~+20dB(1dB step), Q=1, f0=10kHz Possible to use soft switching
Fader	0dB~-62dB(1dB step), -∞dB, Possible to control independently for each four output Possible to use soft switching

●Electrical characteristics

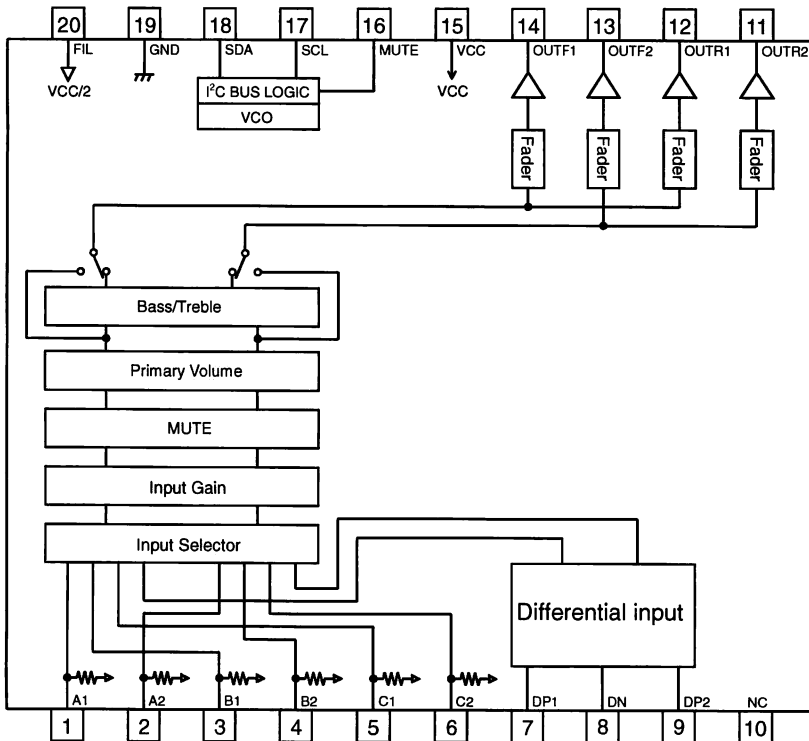
Unless otherwise specified, Ta=25°C, VCC=8.5V, f=1kHz, Vin=1Vrms, Rg=600Ω, RL=10kΩ, A input, Input gain 0dB, Volume 0dB, Bass 0dB, Treble 0dB, Fader 0dB

Item	Symbol	Limit			Unit	Condition
		Min.	Typ.	Max.		
Current upon no signal	I _Q	—	15	30	mA	No signal
Voltage gain	G _V	-1.5	0	1.5	dB	G _V =20log(V _{OUT} /V _{IN})
Channel balance	CB	-1.5	0	1.5	dB	CB=G _{V1} -G _{V2}
Total harmonic distortion	THD+N	—	0.005	0.05	%	V _{OUT} =1Vrms BW=400-30kHz
Output noise voltage	V _{NO}	—	6	25	μVrms	R _g =0Ω BW=IHF-A
Residual output noise voltage	V _{NOR}	—	2	10	μVrms	Fader=-∞dB R _g =0Ω BW=IHF-A
Cross-talk between channels	CTC	—	-100	-90	dB	R _g =0Ω CTC=20log(V _{OUT} /V _{IN}) BW=IHF-A
Ripple rejection	RR	—	-70	-40	dB	R _g =0Ω f=100Hz V _{RR} =100mVrms RR=20log(V _{OUT} /V _{CCIN})
Common mode rejection ratio	CMRR	50	65	—	dB	DP1 and DN input DP2 and DN input CMRR=20log(V _{IN} /V _{OUT}) BW=IHF-A
Maximum input voltage	V _{IM}	2.1	2.3	—	Vrms	V _{IM} at THD+N(V _{OUT})=1% BW=400-30kHz
Maximum gain	G _{V MAX}	+10	+12	+14	dB	Volume=+12dB V _{IN} =100mVrms G _V =20log(V _{OUT} /V _{IN})
Maximum attenuation	G _{F MIN}	—	-100	-90	dB	G _F =20log(V _{OUT} /V _{IN}) BW=IHF-A, Att=-∞dB
Maximum output voltage	V _{OM}	2.0	2.2	—	Vrms	THD+N=1% BW=400-30kHz

● Dimensional outline drawing



● Block diagram



● Terminal No. / Terminal name

Terminal No.	Terminal Name
1	A1
2	A2
3	B1
4	B2
5	C1
6	C2
7	DP1
8	DN
9	DP2
10	NC
11	OUTR2
12	OUTR1
13	OUTF2
14	OUTF1
15	VCC
16	MUTE
17	SCL
18	SDA
19	GND
20	FIL

●Cautions on use

- (1) Numbers and data in entries are representative design values and are not guaranteed values of the items.
- (2) Absolute maximum ratings
If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.
- (3) GND potential
Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.
- (4) Thermal design
Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.
- (5) Shorts between pins and misinstallation
When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is misinstalled and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.
- (6) Operation in strong magnetic fields
Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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