

STC08DE150HV

Hybrid emitter switched bipolar transistor ESBT® 1500V - 8A - 0.075 Ω

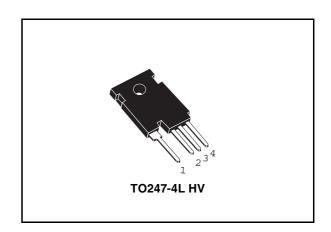
Preliminary Data

General features

Table 1. General features

V _{CS(ON)}	Ic	R _{CS(ON)}
0.6V	8A	0.075Ω

- Low equivalent on resistance
- Very fast-switch, up to 150 kHz
- Squared RBSOA, up to 1500 V
- Very low C_{ISS} driven by $R_G = 47 \Omega$
- In compliance with the 2002/93/EC European Directive



Description

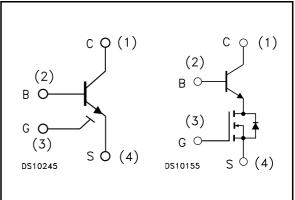
The STC08DE150HV is manufactured in a hybrid structure, using dedicated high voltage Bipolar and low voltage MOSFET technologies, aimed at providing the best performance in ESBT topology.

The STC08DE150HV is designed for use in aux flyback smps for any three phase application.

Applications

Single switch SMPS based on three phase mains

Internal schematic diagrams



Order codes

Part Number	Marking	Package	Packing
STC08DE150HV	C08DE150HV	TO247-4L HV	Tube

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STC08DE150HV Electrical ratings

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CS(SS)}	Collector-source voltage (V _{BS} =V _{GS} =0V)	1500	V
V _{BS(OS)}	Base-source voltage (I _C =0, V _{GS} =0V)	30	V
V _{SB(OS)}	Source-base voltage (I _C =0, V _{GS} =0V)	9	V
V _{GS}	Gate-source voltage	±20	V
I _C	Collector current	8	Α
I _{CM}	Collector peak current (t _P < 5ms)	15	Α
Ι _Β	Base current	4	Α
I _{BM}	Base peak current (t _P < 1ms)	8	Α
P _{tot}	Total dissipation at T _c ≤ 25°C	156	W
T _{stg}	Storage temperature	-40 to 150	°C
T _J	Max. operating junction temperature	125	°C

Table 3. Thermal data

Symbol	Parameter	Value Un	it
R _{thj-case}	Thermal resistance junction-case max	0.64 °C/	W

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Electrical characteristics STC08DE150HV

2 Electrical characteristics

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$

Table 4. Electrical characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{CS(SS)}	Collector-source current (V _{BS} =V _{GS} =0V)	V _{CS(SS)} =1500V			100	μΑ
I _{BS(OS)}	Base-source current (I _C =0, V _{GS} =0V)	V _{BS(OS)} =30V			10	μΑ
I _{SB(OS)}	Source-base current (I _C =0, V _{GS} =0V)	V _{SB(OS)} =9V			100	μΑ
I _{GS(OS)}	Gate-source leakage (V _{BS} =0V)	V _{GS} = ± 20V			500	nA
V _{CS(ON)}	Collector-source ON voltage	V _{GS} =10V I _C =8A I _B =1.6A V _{GS} =10V I _C =5A I _B =0.5A		0.6 0.6	1.4	V V
h _{FE}	DC current gain	I _C =8A V _{CS} =1V V _{GS} =10V I _C =5A V _{CS} =1V V _{GS} =10V	4.5 8	7.5 10		
V _{BS(ON)}	Base-source ON voltage	V _{GS} =10V I _C =8A I _B =1.6A V _{GS} =10V I _C =5A I _B =0.5A		1.5 1	2	V V
V _{GS(th)}	Gate threshold voltage	V _{BS} =V _{GS} I _B =250μA	1.5	2.2	3	٧
C _{iss}	Input capacitance	V_{CS} =25V f =1MHz V_{GS} = V_{CB} =0V		750		pF
Q _{GS(tot)}	Gate-source Charge	V _{GS} =10V I _C =8A V _{CS} =25V V _{CB} =0V		12.5		nC
t _s	INDUCTIVE LOAD Storage time Fall time	$V_{GS} = 10V$ $R_{G} = 47\Omega$ $V_{Clamp} = 1200V$ $t_{p} = 4\mu s$ $I_{C} = 5A$ $I_{B} = 0.5A$		526 8.5		ns ns
t _s	INDUCTIVE LOAD Storage time Fall time	$V_{GS} = 10V$ $R_{G} = 47\Omega$ $V_{Clamp} = 1200V$ $t_{p} = 4\mu s$ $I_{C} = 5A$ $I_{B} = 1A$		884 16		ns ns
V _{CSW}	Maximum collector- source voltage switched without snubber	$R_G = 47\Omega$ $h_{FE} = 5$ $I_C = 8A$	1500			V

Electrical characteristics Table 4.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{CS(dyn)}	Collector-source dynamic voltage (500ns)	$\begin{aligned} &V_{CC} = &V_{Clamp} = 300 V \\ &V_{GS} = 10 V &I_{C} = 4 A \\ &I_{B} = 0.8 A &t_{peak} = 500 ns \\ &R_{G} = &47 \Omega &I_{Bpeak} = 8 A \; (2 I_{C}) \end{aligned}$		6		V
V _{CS(dyn)}	Collector-source dynamic voltage (1µs)	$\begin{aligned} &V_{CC} = &V_{Clamp} = &300 V \\ &V_{GS} = &10 V &I_{C} = &4 A \\ &I_{B} = &0.8 A &t_{peak} = &500 ns \\ &R_{G} = &47 \Omega &I_{Bpeak} = &8 A \; (2 I_{C}) \end{aligned}$		2.2		>

Note (1) Pulsed duration = 300 μ s, duty cycle \leq 1.5%

Electrical characteristics (curves) 2.1

Output characteristics Figure 1.

Figure 2. Dynamic collector-emitter saturation voltage DG13990 $V_{CE(sat)dyn}$

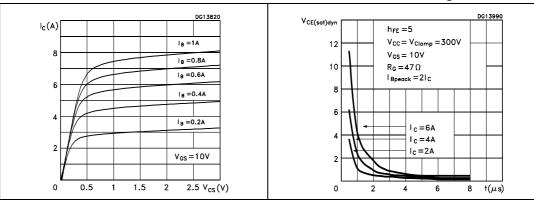
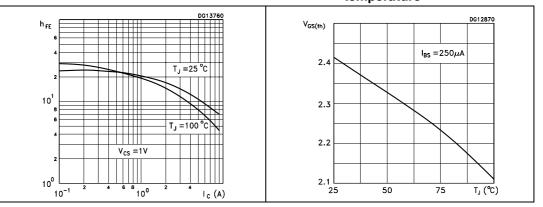


Figure 3. DC current gain

Figure 4. Gate threshold voltage vs temperature



Electrical characteristics STC08DE150HV

Figure 5. Collector-source On voltage Figure 6. Collector-source On voltage

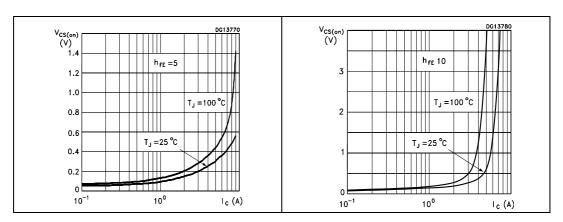


Figure 7. Base-source On voltage

Figure 8. Base-source On voltage

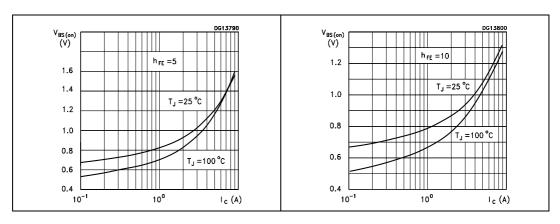
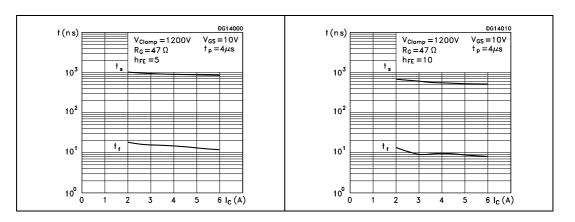
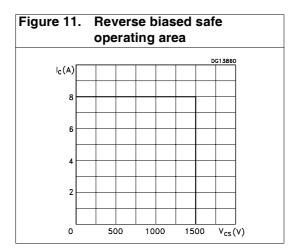


Figure 9. Inductive load switching time Figure 10. Inductive load switching time



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2.2 Test circuits

Figure 12. Enlargement FBSOA circuit

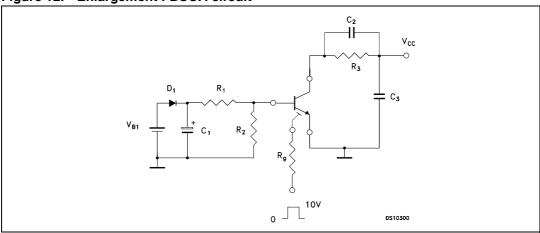


Table 5. Components, values

V _{B1} =4.16V	C ₁ =4700μF
D ₁ =BA157	C ₂ ≤1000pF
$R_1 = 1\Omega$	V _{CC} =1500V
$R_2 = 100\Omega$	V _g =10V
$R_3 = 180\Omega$	Pulse time = 5μs
$R_g = 47\Omega$	
_	

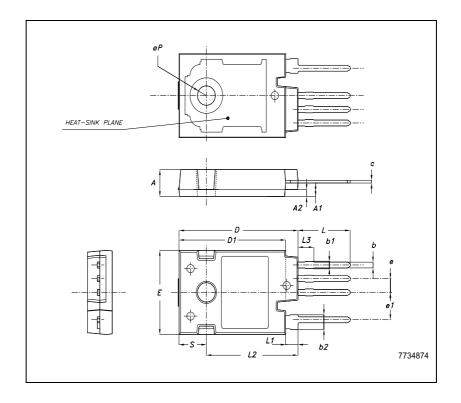
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3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

TO247-4L HV MECHANICAL DATA

DIM		mm.	
DIM.	MIN.	TYP	MAX.
Α	4.85		5.15
A1	2.20	2.50	2.60
A2		1.27	
b	0.95	1.10	1.30
b2	2.50		2.90
С	0.40		0.80
D	23.85	24	24.15
D1		21.50	
E	15.45	15.60	15.75
е	2.54		
e1	5.08		
L	10.20		10.80
L1	2.20	2.50	2.80
L2		18.50	
L3		3	
øΡ	3.55		3.65
S		5.50	



Revision history STC08DE150HV

4 Revision history

Table 6. Revision history

Date	Revision	Changes
25-Oct-2006	1	First release.

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