





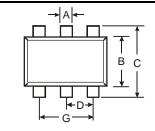
P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

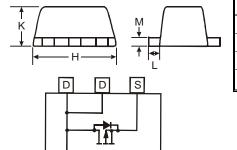
Features

- P-Channel MOSFET
- Very Low On-Resistance
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- Lead Free By Design/RoHS Compliant (Note 2)
- "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: SOT-563
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals Connections: See Diagram
- Terminals: Finish Matte Tin annealed over Copper lead frame. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4 Ordering Information: See Page 4
- Weight: 0.006 grams (approximate)





SOT-563									
Dim	Min	Max	Тур						
Α	0.15	0.30	0.25						
В	1.10	1.10 1.25							
С	1.55 1.70 1.60								
D	0.50								
G	0.90	1.10	1.00						
Н	1.50	1.70 1.60							
K	0.56	0.60	0.60						
L	0.10	0.30	0.20						
М	0.10	0.18	0.11						
All Dimensions in mm									

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V _{DSS}	-20	V		
Gate-Source Voltage	V _{GSS}	±12	V		
Continuous Drain Current (Note 1)	T _A = 25°C T _A = 70°C	I _D -860 -690		mA	
Power Dissipation (Note 1)		Steady State	P _D	170	mW
Continuous Drain Current (Note 1)	t ≤ 5s	T _A = 25°C T _A = 70°C	I _D	-950 -760	mA
Power Dissipation (Note 1)		t ≤ 5s	P _D	210	mW
Pulsed Drain Current	t	_p = 10μs	I _{DM}	-4.0	А
Operating and Storage Temperature Range	T _{i,} T _{STG}	-55 to +150	°C		

- Device mounted on FR-4 PCB with 1 inch square pads.
- No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.



Electrical Characteristics $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic			Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)							
Drain-Source Breakdown Voltage	BV _{DSS}	-20	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	ge Drain Current $T_J = 25^{\circ}C$ $T_J = 125^{\circ}C$		_	_	-1.0 -5.0	μА	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage		I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 4)		•		•	•	•	
Gate Threshold Voltage	V _{GS(th)}	-0.45	_	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
Static Drain-Source On-Resistance		R _{DS (ON)}	_	92 134 180	150 200 240	mΩ	V_{GS} = -4.5V, I_D = -950mA V_{GS} = -2.5V, I_D = -670mA V_{GS} = -1.8V, I_D = -200mA
Forward Transconductance		9FS	_	3.1	_	S	$V_{DS} = -10V, I_{D} = -810mA$
Diode Forward Voltage (Note 4)		V_{SD}	_	_	-0.9	V	$V_{GS} = 0V, I_{S} = -360 \text{mA}$
DYNAMIC CHARACTERISTICS						_	
Input Capacitance		C _{iss}	_	320	_	pF	
Output Capacitance		Coss		80	_	pF	V _{DS} = -16V, V _{GS} = 0V -f = 1.0MHz
Reverse Transfer Capacitance		C _{rss}	_	60	_	pF	1 - 1.0ivii 12

Notes: 4. Short duration test pulse used to minimize self-heating effect.

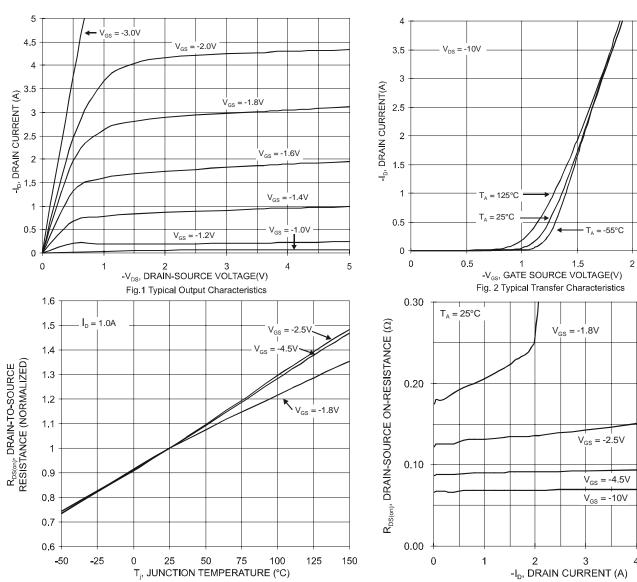


Fig. 3 On-Resistance Variation with Temperature

2.5

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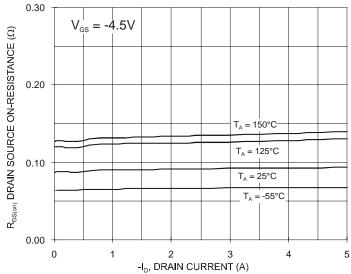


Fig. 5 Drain-Source On-Resistance Vs. Drain Current and Temperature

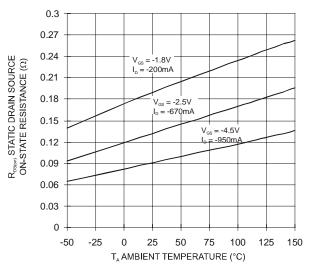


Fig. 7 Static Drain-Source On-State Resistance vs Ambient Temperature

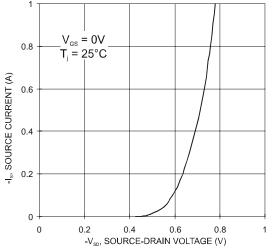
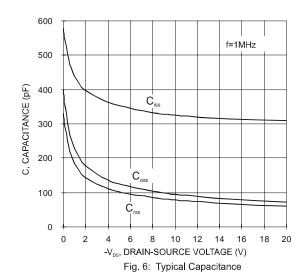
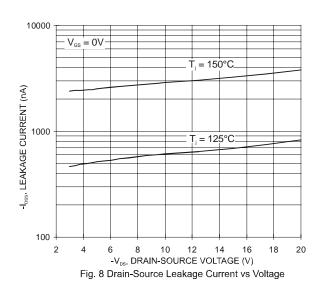


Fig. 9 Diode Forward Voltage vs. Current





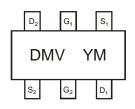


Ordering Information (Note 5)

Device	Packaging	Shipping			
DMP2104V-7	SOT-563	3000/Tape & Reel			

Notes: 5. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



DMV = Marking Code YM = Date Code Marking Y = Year ex: T = 2006M = Month ex: 9 = September

Date Code Key

Year	200	6	2007		2008		2009			2011	2	2012	
Code	Т		U		V		W			Υ		Z	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec	
Code	1	2	3	4	5	6	7	8	9	0	N	D	

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