# 200mA / 30V Low Vce (sat) Digital transistors (with built-in resistors)

# DTD743ZE / DTD743ZM

#### Applications

Inverter, Interface, Driver

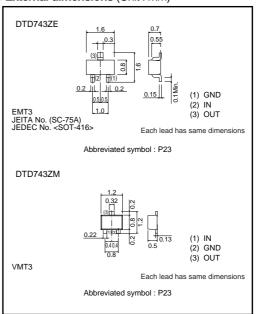
### ● Feature

- 1) VCE(sat) is lower than conventional products.
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 4) Only the on / off conditions need to be set for operation, making the device design easy.

#### ●Structure

NPN epitaxial plannar silicon transistor (Resistor built-in type)

#### ●External dimensions (Unit : mm)



# ● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	l lada
Parameter	Symbol	DTD743ZE DTD743ZM	Unit
Supply voltage	Vcc	30	V
Input voltage	Vin	−5 to +20	V
Collector current *1	IC (max)	200	mA
Power dissipation *2	Po	150	mW
Junction temperature	Tj	150	ొ
Storage temperature	Tstg	-55 to +150	ဗ

<sup>\*1</sup> Characteristics of built-in transistor. \*2 Each terminal mounted on a recommended land.

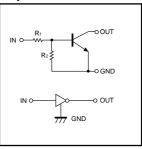
# Packaging specifications

T dollaging opcomoditions						
	Package	EMT3	VMT3			
	Packaging type	Taping	Taping			
	Code	TL	T2L			
Part No.	Basic ordering unit (pieces)	3000	8000			
DTD743ZE		0	-			
DTD743ZM		-	0			

# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	-	-	0.3	V	Vcc= 5V, Io= 100μA
	VI(on)	2.5	-	-		Vo=0.3V, Io=20mA
Output voltage	VO(on)	-	70	300	mV	Io/I:=50mA / 2.5mA
Input current	lı	-	-	1.4	mA	V= 5V
Output current	IO(off)	-	-	0.5	μΑ	Vcc=30V, Vi=0V
DC current gain	Gı	140	-	-	-	Vo=2V, Io=100mA
Transition frequency *	f⊤	-	260	-	MHz	Vc=10V, I=-5mA, f=100MHz
Input resistance	R <sub>1</sub>	3.29	4.7	6.11	kΩ	-
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	8.0	10	12	-	-
* Characteristics of built-in transis	tor.					

# ●Equivalent circuit



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