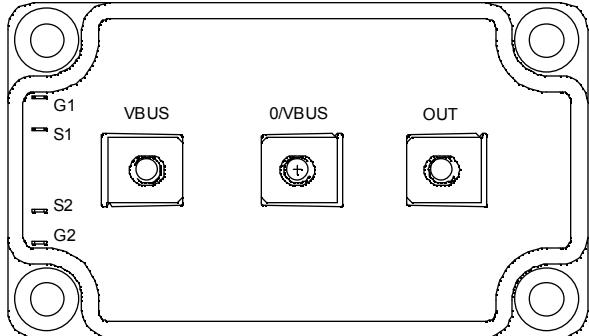
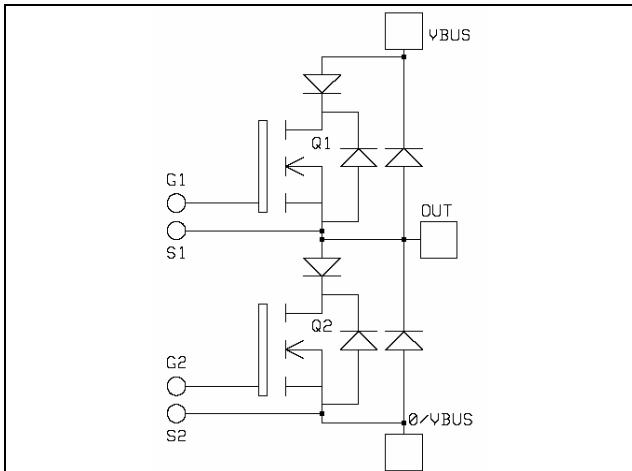


**Phase leg  
Series & SiC parallel diodes  
MOSFET Power Module**

**V<sub>DSS</sub> = 500V**  
**R<sub>DSon</sub> = 24mΩ max @ T<sub>j</sub> = 25°C**  
**I<sub>D</sub> = 150A @ T<sub>c</sub> = 25°C**



#### Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
V <sub>DSS</sub>	Drain - Source Breakdown Voltage	500	V
I <sub>D</sub>	Continuous Drain Current	T <sub>c</sub> = 25°C	A
		T <sub>c</sub> = 80°C	
I <sub>DM</sub>	Pulsed Drain current	600	
V <sub>GS</sub>	Gate - Source Voltage	±30	V
R <sub>DSon</sub>	Drain - Source ON Resistance	24	mΩ
P <sub>D</sub>	Maximum Power Dissipation	T <sub>c</sub> = 25°C	W
I <sub>AR</sub>	Avalanche current (repetitive and non repetitive)	24	A
E <sub>AR</sub>	Repetitive Avalanche Energy	30	
E <sub>AS</sub>	Single Pulse Avalanche Energy	1300	mJ

 CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handing Procedures Should Be Followed.

#### Application

- Motor control
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

#### Features

- **Power MOS 7® MOSFETs**
  - Low R<sub>DSon</sub>
  - Low input and Miller capacitance
  - Low gate charge
  - Avalanche energy rated
  - Very rugged
- **Parallel SiC Schottky Diode**
  - Zero reverse recovery
  - Zero forward recovery
  - Temperature Independent switching behavior
  - Positive temperature coefficient on VF
- Kelvin source for easy drive
- Very low stray inductance
  - Symmetrical design
  - M5 power connectors
- High level of integration

#### Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile

All ratings @  $T_j = 25^\circ\text{C}$  unless otherwise specified

### Electrical Characteristics

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$\text{BV}_{\text{DSS}}$	Drain - Source Breakdown Voltage	$V_{\text{GS}} = 0\text{V}, I_{\text{D}} = 1.5\text{mA}$		500			V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 500\text{V}$	$T_j = 25^\circ\text{C}$			500	$\mu\text{A}$
		$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 400\text{V}$	$T_j = 125^\circ\text{C}$			3	mA
$R_{\text{DS(on)}}$	Drain – Source on Resistance	$V_{\text{GS}} = 10\text{V}, I_{\text{D}} = 75\text{A}$				24	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}} = V_{\text{DS}}, I_{\text{D}} = 6\text{mA}$		3		5	V
$I_{\text{GSS}}$	Gate – Source Leakage Current	$V_{\text{GS}} = \pm 30\text{ V}, V_{\text{DS}} = 0\text{V}$				$\pm 500$	nA

### Dynamic Characteristics

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}} = 0\text{V}$ $V_{\text{DS}} = 25\text{V}$ $f = 1\text{MHz}$			19.6		nF
$C_{\text{oss}}$	Output Capacitance				4.2		
$C_{\text{rss}}$	Reverse Transfer Capacitance				0.3		
$Q_g$	Total gate Charge	$V_{\text{GS}} = 10\text{V}$ $V_{\text{Bus}} = 250\text{V}$ $I_{\text{D}} = 150\text{A}$			434		nC
$Q_{\text{gs}}$	Gate – Source Charge				120		
$Q_{\text{gd}}$	Gate – Drain Charge				216		
$T_{\text{d(on)}}$	Turn-on Delay Time	<b>Inductive switching @ <math>125^\circ\text{C}</math></b> $V_{\text{GS}} = 15\text{V}$ $V_{\text{Bus}} = 333\text{V}$ $I_{\text{D}} = 150\text{A}$ $R_G = 0.8\Omega$			10		ns
$T_r$	Rise Time				17		
$T_{\text{d(off)}}$	Turn-off Delay Time				50		
$T_f$	Fall Time				41		

### SiC Parallel diode ratings and characteristics

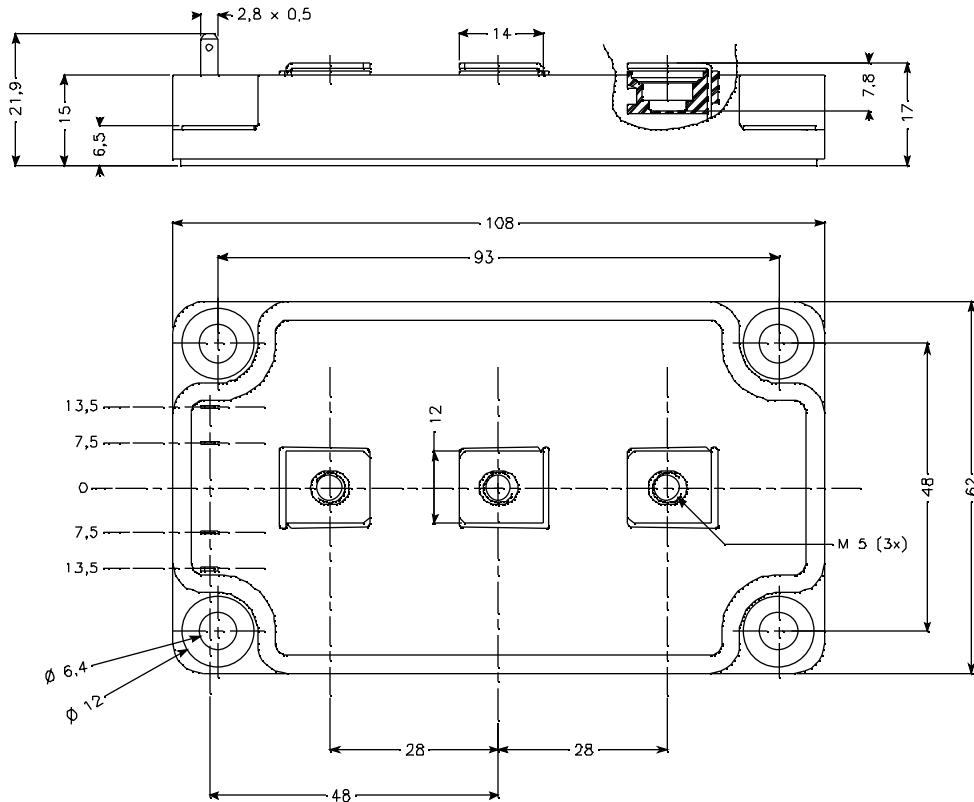
<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$V_{\text{RRM}}$	Maximum Peak Repetitive Reverse Voltage			600			V
$I_{\text{RRM}}$	Maximum Reverse Leakage Current	$V_R = 600\text{V}$	$T_j = 25^\circ\text{C}$		400	1600	$\mu\text{A}$
			$T_j = 125^\circ\text{C}$		800	8000	
$I_{\text{F(AV)}}$	Maximum Average Forward Current	50% duty cycle			80		A
$V_F$	Diode Forward Voltage	$I_F = 80\text{A}$	$T_j = 25^\circ\text{C}$		1.6	1.8	V
			$T_j = 175^\circ\text{C}$		2.0	2.4	
$Q_C$	Total Capacitive Charge	$I_F = 80\text{A}, V_R = 300\text{V}$ $di/dt = 2000\text{A}/\mu\text{s}$			112		nC
$Q$	Total Capacitance	$f = 1\text{MHz}, V_R = 200\text{V}$			520		pF
		$f = 1\text{MHz}, V_R = 400\text{V}$			400		

**Series diode ratings and characteristics**

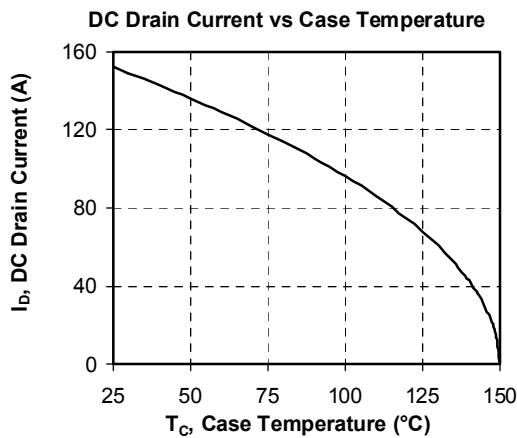
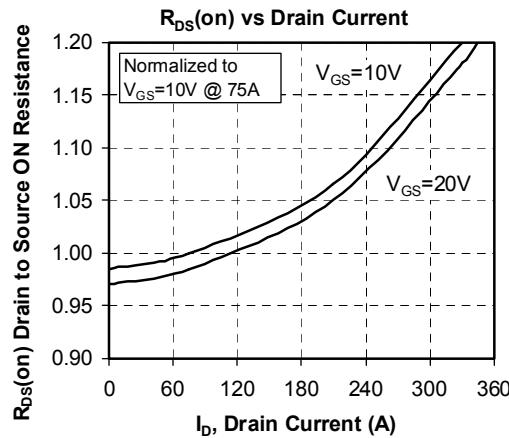
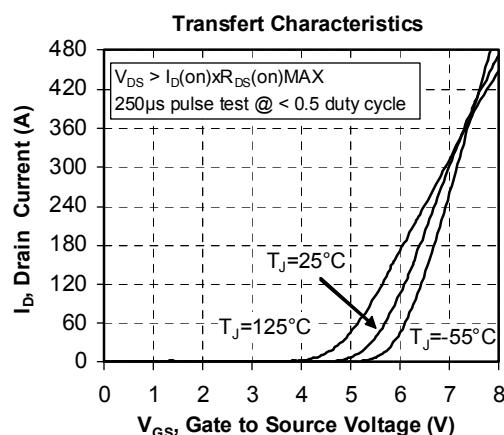
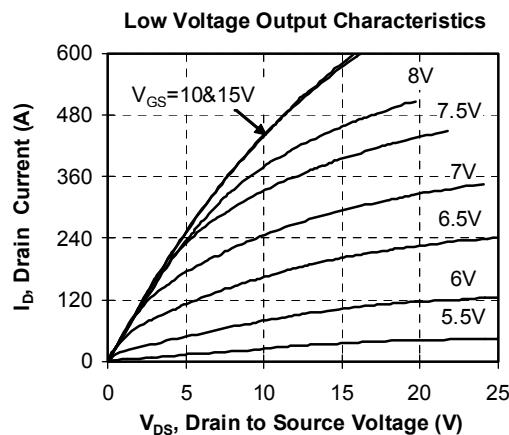
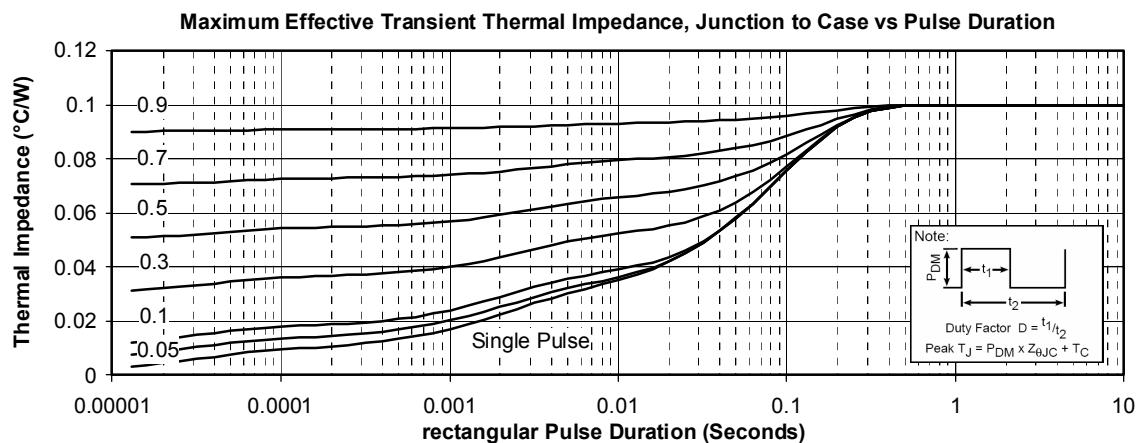
<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$I_{F(AV)}$	Maximum Average Forward Current	50% duty cycle	$T_c = 85^\circ\text{C}$		120		A
$V_F$	Diode Forward Voltage	$I_F = 120\text{A}$			1.1	1.15	V
		$I_F = 240\text{A}$			1.4		
		$I_F = 120\text{A}$	$T_j = 125^\circ\text{C}$		0.9		
$t_{rr}$	Reverse Recovery Time	$I_F = 120\text{A}$ $V_R = 133\text{V}$ $di/dt = 400\text{A}/\mu\text{s}$	$T_j = 25^\circ\text{C}$		31		ns
			$T_j = 125^\circ\text{C}$		60		
$Q_{rr}$	Reverse Recovery Charge	$I_F = 120\text{A}$ $V_R = 133\text{V}$ $di/dt = 400\text{A}/\mu\text{s}$	$T_j = 25^\circ\text{C}$		120		nC
			$T_j = 125^\circ\text{C}$		500		

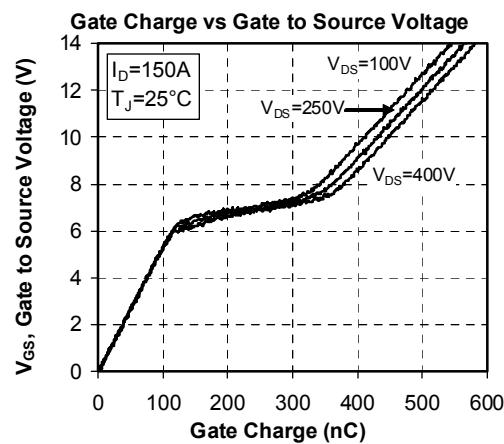
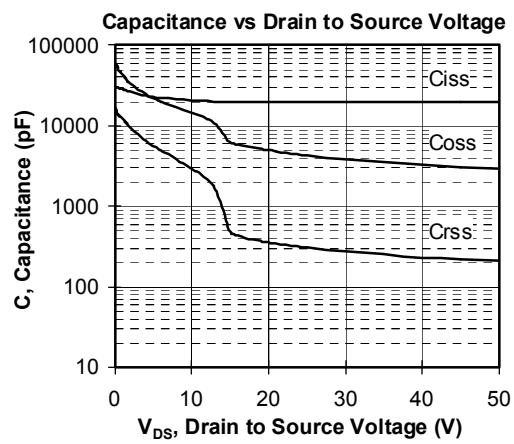
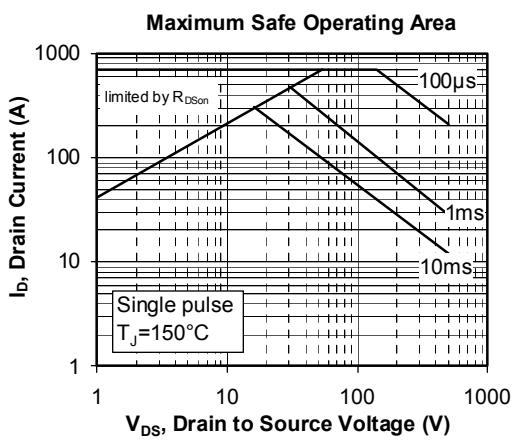
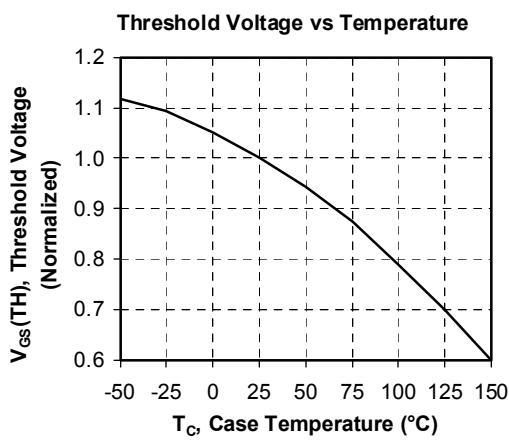
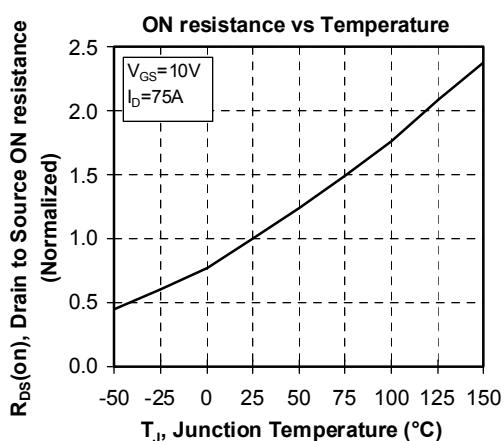
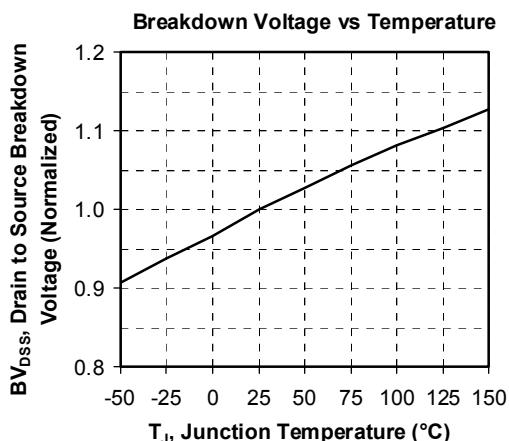
**Thermal and package characteristics**

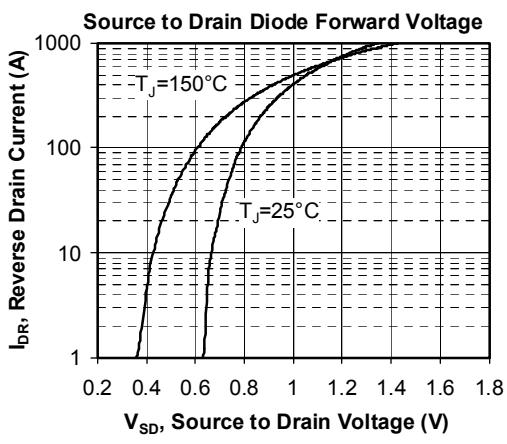
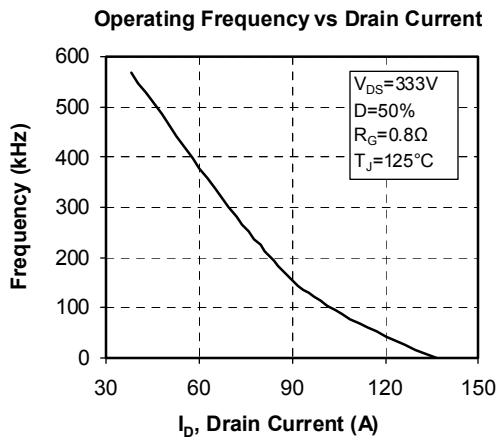
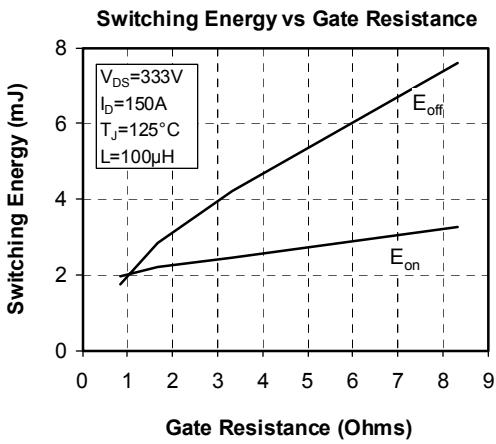
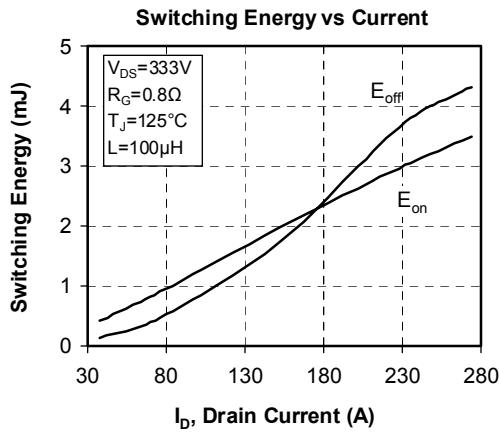
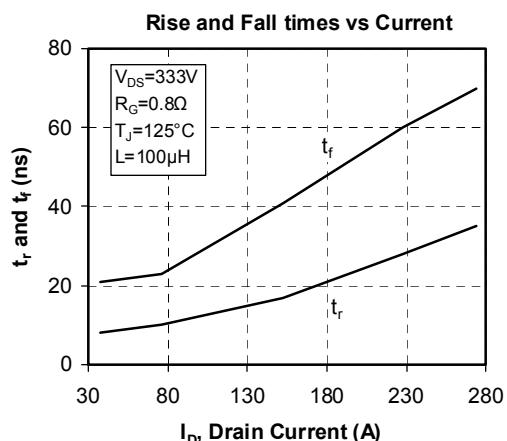
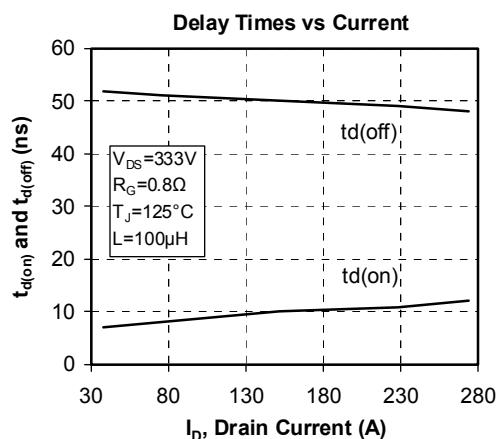
<i>Symbol</i>	<i>Characteristic</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$R_{thJC}$	Junction to Case	Transistor			0.10	°C/W
		Series diode			0.46	
		Parallel diode			0.35	
$V_{ISOL}$	RMS Isolation Voltage, any terminal to case t =1 min, $I_{isol}<1\text{mA}$ , 50/60Hz		2500			V
$T_J$	Operating junction temperature range		-40		150	°C
$T_{STG}$	Storage Temperature Range		-40		125	
$T_C$	Operating Case Temperature		-40		100	
Torque	Mounting torque	To heatsink	M6	3	5	N.m
		For terminals	M5	2	3.5	
Wt	Package Weight				280	g

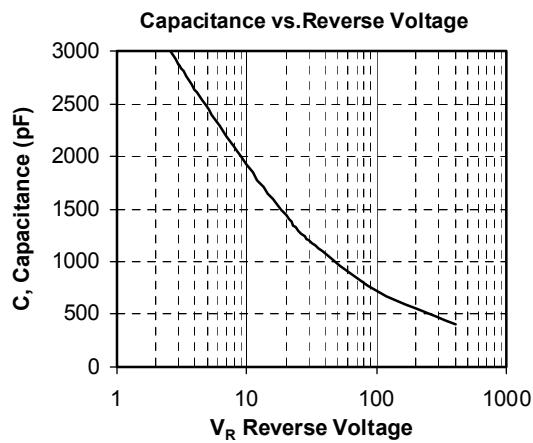
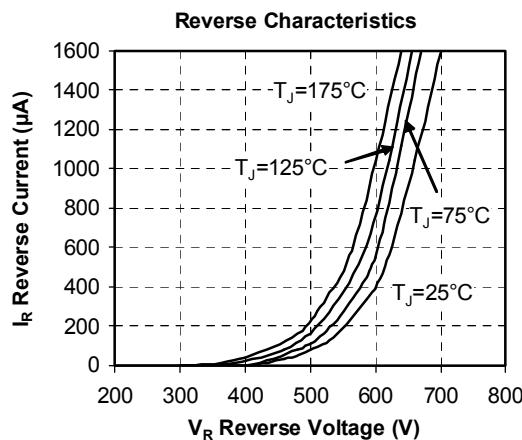
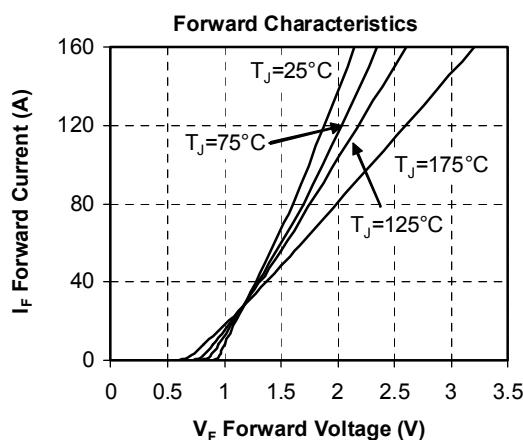
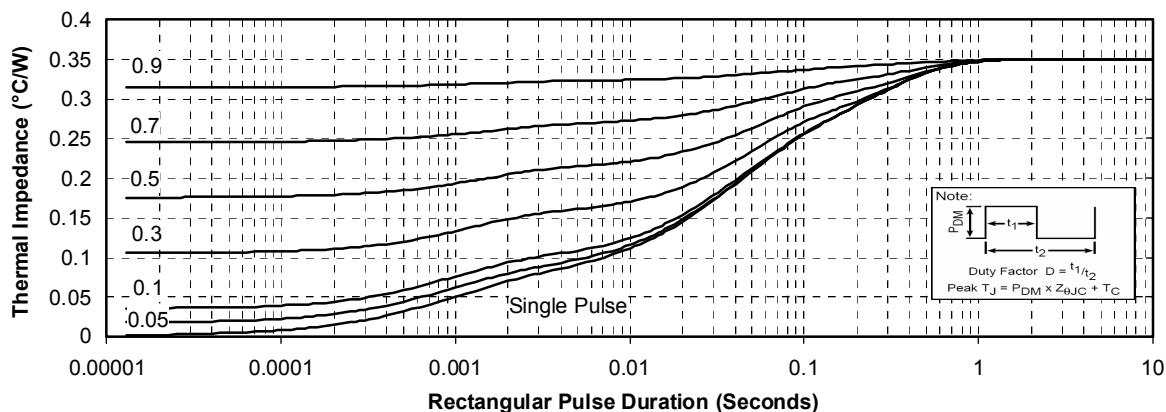
**Package outline**


### Typical MOSFET Performance Curve







**Typical SiC Diode Performance Curve**
**Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration**


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APT's products are covered by one or more of U.S patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S and Foreign patents pending. All Rights Reserved.