

HIGH CURRENT, LOW DROPOUT VOLTAGE REGULATORS

5100 SERIES

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(315) 701-6751

FEATURES:

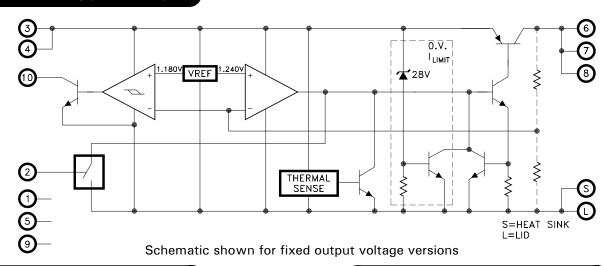
- · Extremely Compact 10 Pin SOIC With Heat Sink Tab
- Extremely Low Dropout Voltage: 350mV @ 1.5 Amps
- Available in 3.3V, 5.0V, 12.0V and Adjustable Versions
- · Open Collector Error Flag Output
- · TTL Level Enable Pin: Zero Current Shutdown Mode
- Reverse Battery and Load Dump Protection
- · Low Ground Current: 22mA Typical at Full Load
- 1% Guaranteed Accuracy
- Output Current to 1.5 Amps
- · Contact MSK for MIL-PRF-38534 Qualification Status



DESCRIPTION:

The MSK 5100 series voltage regulators are available in +3.3V, +5.0V, +12.0V or adjustable output configurations. All boast ultra low dropout specifications due to the utilization of a super PNP output pass transistor with monolithic technology. Dropout voltages of 350mV at 1.5 amps are typical in this configuration, which drives efficiency up and power dissipation down. Accuracy is guaranteed with a 1% output voltage tolerance. The series also offers a TTL/CMOS compatible on/off enable function as well as an output flag pin. The MSK 5100 series is packaged in a space efficient 10 pin power SOIC with a built in copper-moly sink tab.

EQUIVALENT SCHEMATIC



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TYPICAL APPLICATIONS

- · High Efficiency, High Current Linear Regulators
- Constant Voltage/Current Regulators
- System Power Supplies
- · Switching Power Supply Post Regulators
- · Battery Powered Equipment

PIN-OUT INFORMATION

MSK5100-00		MSK510	MSK5100-3.3,-5.0,-12		
1	NC	1	NC		
2	Enable	2	Enable		
3	Vin A	3	Vin A		
4	Vin B	4	Vin B		
5	NC	5	NC		
6	Vout A	6	Vout A		
7	Vout B	7	Vout B		
8	NC	8	Vout C		
9	Adj	9	NC		
10	NC	10	Flag		
		HEAT SINK			

The heat sink of the package and the lid are electrically connected to ground.

ABSOLUTE MAXIMUM RATINGS

V_{INP}	Input Voltage (100mS 1%D.C.) $-20V$ to $+60$)V Tst	Storage Temperature Range -65°C to +150°C
V_{IN}	Input Voltage	V TLD	Lead Temperature
V_{EN}	Enable Voltage0.3V to 26	SV	(10 Seconds Soldering)
Іоит	Output Current	5A T J	Operating Temperature
			MSK5100 Series40°C to +85°C
			MSK5100B/E Series55°C to +125°C

ELECTRICAL SPECIFICATIONS

Davamatav	Toot Conditio	Test Conditions ①③		MSK 5100B/E SERIES		MSK 5100 SERIES				
Parameter	rest Condition			Min.	Тур.	Max.	Min.	Тур.	Max.	Units
Output Voltage Tolerance	IOUT = 1A; VIN = VOUT + 1V		1	-	±0.5	± 1.0	-	±0.5	±1.0	%
Output voltage rolerance			2,3	-	± 1.0	±2.0	-	-	-	%
Dropout Voltage (2)	ΔV out = -1%; lout=100m A		1	-	80	200	-	80	225	mV
Diopout Voitage (2)	ΔVout = -1%; lout = 1.5A		1	-	350	600	-	350	625	mV
Load Regulation	VIN = VOUT + 5V		1	-	±0.2	±1.0	-	±0.2	±1.2	%
Load Negulation	10mA ≤ Ιουτ ≤ 1.5A		2,3	-	±0.3	±2.0	-	±0.3	-	%
Line Regulation	(Vout +1V) ≤ Vin ≤ 26V		1	ı	±0.05	±0.5	ı	±0.05	±0.6	%
Line negulation	Ioυτ = 10mA		2,3	-	±0.5	±1.0	1	±0.5	-	%
Output Current Limit ②	Vout = 0V; Vin = Vout + 1V		-	1	2.1	3.5	ı	2.1	3.5	Α
Ground Current ②	VIN = VOUT + 1V; IOUT = 0.75A		-	-	8	20	-	8	20	mA
Ground Current (2)	VIN = VOUT + 1V; IOUT = 1.5A		-	-	22	-	1	22	-	mA
Output Noise ②	Output Noise ② $CL = 10\mu\text{F}; 10\text{Hz} \le f \le 100\text{KHz}$		-	-	400	-	-	400	-	μV
Enable Input Voltage (2)	Enable Input Voltage ②		1	2.4	-	-	2.4	-	-	V
Litable input voltage (2)			1	-	-	0.8	-	-	0.8	V
Enable Input Current (2)	Enable Input Current ② HI		1	-	100	600	-	100	600	μΑ
Litable input Current &			1	-	-	2	-	-	2	μΑ
Shutdown Output Current ②	VENABLE ≤ 0.8V	•	-	-	10	500	-	10	500	μΑ
Flag Output Leakage ② ⑨	Flag Output Leakage ② ⑨ Voн=26V		-	-	0.01	2	-	0.01	2	μΑ
Flag Output On Voltage 9 IoL $\leq 250\mu$ A; VIN = Vol		ит-2V	1	-	0.2	0.4	-	0.2	0.4	V
Flag Threshold ② ⑨	Flag Threshold ② ⑨ VIN=Vout-7%		-	1	75	-	ı	75	-	mV
Reference Voltage (8)	Reference Voltage (8) Normal Operation		1	1.22	1.24	1.26	1.22	1.24	1.26	V
Reference Voltage Temp Drift ② ⑧ Normal Operation			-	-	20	-	1	20	-	ppm/°C
Adjust Pin Bias Current ② ⑧ Full Temp; VIN = VOUT + 1V			-	-	40	120	-	40	150	nA
Thermal Resistance ②	Thermal Resistance ② Junction to Case		-	-	1.8	2	-	1.8	2	°C/W
Thermal Shutdown TJ			-	-	130	-	-	130	-	°C

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NOTES:

- ① Output decoupled to ground using 33µF minimum capacitor unless otherwise specified. ② Guaranteed by design but not tested. Typical parameters are representative of actual device performance but are for reference only.
- 3 All output parameters are tested using a low duty cycle pulse to maintain T_J = Tc.
 4 Industrial grade and "E" suffix devices shall be tested to subgroups 1 and 4 unless otherwise specified.
 5 Military grade devices ('B' suffix) shall be 100% tested to subgroups 1,2,3 and 4..
 6 Subgroup 1,4 Tc=+25°C
- Subgroup 2 $T_J = +125$ ° C Subgroup 3 $T_A = -55 \,^{\circ}C$
- (7) Applies to MSK5100-00 adjustable version only.
- 8 Applies to fixed output devices only.
- Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.

PART NUMBER	OUTPUT VOLTAGE
MSK5100-00	Adjustable
MSK5100-3.3	+3.3V
MSK5100-5.0	+5.0V
MSK5100-12	+ 12.0V

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APPLICATION NOTES

REGULATOR PROTECTION:

The MSK 5100 series is fully protected against reversed input polarity, overcurrent faults, overtemperature conditions (Pd) and transient voltage spikes of up to 60V. If the regulator is used in dual supply systems where the load is returned to a negative supply, the output voltage must be diode clamped to ground.

OUTPUT CAPACITOR:

The output voltage ripple of the MSK 5100 series voltage regulators can be minimized by placing a filter capacitor from the output to ground. The optimum value for this capacitor may vary from one application to the next, but a minimum of 33μ F is recommended for optimum performance. Transient load response can also be improved by placing a capacitor directly across the load.

LOAD CONNECTIONS:

In voltage regulator applications where very large load currents are present, the load connection is very important. The path connecting the output of the regulator to the load must be extremely low impedance to avoid affecting the load regulation specifications. Any impedance in this path will form a voltage divider with the load. The MSK 5100 series requires a minimum of 10mA of load current to stay in regulation.

ENABLE PIN:

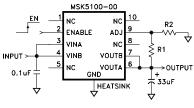
The MSK 5100 series of voltage regulators are equipped with a TTL compatible ENABLE pin. A TTL high level on this pin activates the internal bias circuit and powers up the device. A TTL low level on this pin places the controller in shutdown mode and the device draws approximately $10\mu\text{A}$ of quiescent current. If the enable function is not used, simply connect the enable pin to the input.

FLAG OUTPUT PIN:

All of the fixed output voltage versions of the MSK 5100 series are equipped with a flag output pin. Since the flag pin is an open collector configuration it can be pulled up to any voltage between 3V and 26V. This feature allows direct interfacing to practically any logic. This active low output has a typical level of 0.22V when the flag comparator detects an "out of regulation" condition. Flag states include low input voltage, overtemperature shutdown and output current limit. Extremely high level input voltage transients will also cause the flag output pin to activate.

MSK5100-00 OUTPUT ADJUSTMENT:

The MSK 5100-00 is an adjustable version in the series of high performance regulators. The diagram below illustrates proper adjustment technique for the output voltage. The series resistance of R1 + R2 should be selected to pass the minimum regulator output current requirement of 10mA.



 $VOUT=1.240V \times [1+(R1/R2)]$

HEAT SINK SELECTION:

To select a heat sink for the MSK 5100, the following formula for convective heat flow may be used.

Governing Equation:

$$T_i = Pd x (R_{\theta}ic + R_{\theta}cs + R_{\theta}sa) + Ta$$

WHERE:

Ti = Junction Temperature

Pd = Total Power Dissipation

 $R\theta jc = Junction to Case Thermal Resistance$

 $R\theta cs = Case \ to \ Heat \ Sink \ Thermal \ Resistance$

Resa = Heat Sink to Ambient Thermal Resistance

Ta = Ambient Temperature

First, the power dissipation must be calculated as follows:

Next, the user must select a maximum junction temperature. The maximum allowable junction temperature is 125°C. The equation may now be rearranged to solve for the required heat sink to ambient thermal resistance (Resa).

EXAMPLE:

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An MSK 5100-3.3 is configured for Vin = +5V and Vout = +3.3V. lout is a continuous 1A DC level. The ambient temperature is $+25\,^{\circ}$ C. The maximum desired junction temperature is $125\,^{\circ}$ C.

Rejc =
$$2^{\circ}$$
C/W and Recs = 0.5° C/W typically.
Power Dissipation = $(5V - 3.3V) \times (1A)$
= 1.7 Watts

Folive for Resa:
Resa =
$$\left[\frac{125^{\circ}\text{C} - 25^{\circ}\text{C}}{1.7\text{W}}\right] - 2^{\circ}\text{C/W} - 0.5^{\circ}\text{C/W}$$

= 56.32°C/W

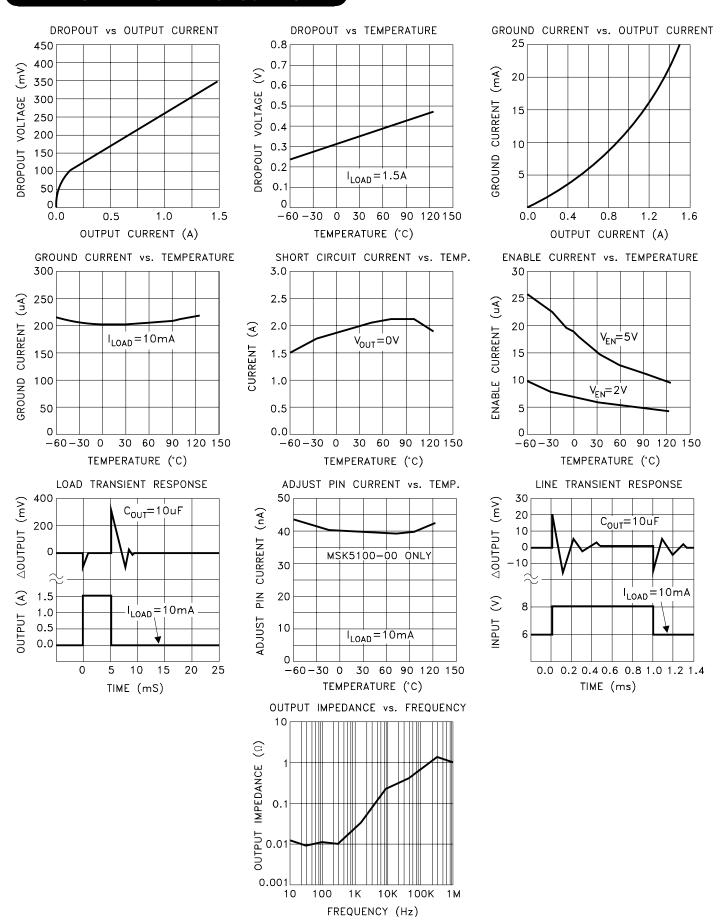
In this example, a heat sink with a thermal resistance of no more than 56°C/W must be used to maintain a junction temperature of no more than 125°C.

DEVICE SOLDERING/CASE CONNECTION:

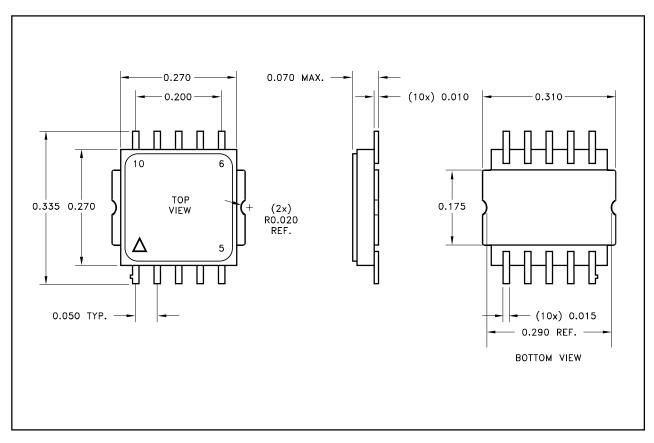
The MSK 5100 series are highly thermally conductive devices and the thermal path from the package heat sink to the internal junctions is very short. Standard surface mount techniques should be used when soldering the device into a circuit board. A hole can be cut in the printed circuit board to allow the heat sink of the package to be thermally bonded to an external heat sink for very high power applications. The external heat sink needs to be connected to ground because the heat sink and lid of the MSK 5100 are also electrically connected to ground. The user is urged to keep this in mind when designing the printed circuit board for the MSK 5100. There should be no printed circuit traces making contact with the case or lid of the device except for ground. The ground plane can be used to pull heat away from the device.

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TYPICAL PERFORMANCE CURVES



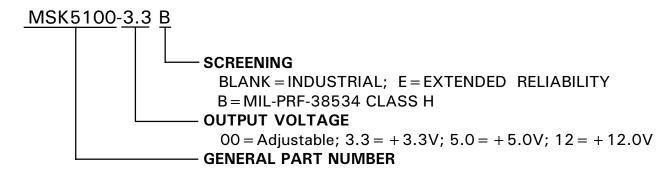
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ESD Triangle indicates Pin 1.

NOTE: ALL DIMENSIONS ARE ±0.010 INCHES UNLESS OTHERWISE LABELED.

ORDERING INFORMATION



The above example is a +3.3V, Military regulator.

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