

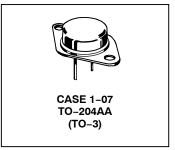
# **NPN Silicon Transistors**

... fast switching speeds and high current capacity ideally suit these parts for use in switching regulators, inverters, wide-band amplifiers and power oscillators in industrial and commercial applications.

- High Speed  $t_f = 0.5 \mu s \text{ (Max)}$
- High Current  $I_{C(max)} = 30 \text{ Amps}$
- Low Saturation  $V_{CE(sat)} = 2.5 \text{ V (Max)}$  @  $I_C = 20 \text{ Amps}$
- This device is available in Pb-free package(s). Specifications herein
  apply to both standard and Pb-free devices. Please see our website at
  www.onsemi.com for specific Pb-free orderable part numbers, or
  contact your local ON Semiconductor sales office or representative.

## 2N5038

20 AMPERE
NPN SILICON
POWER TRANSISTOR
90 VOLTS
140 WATTS



#### \*MAXIMUM RATINGS

| Rating   | Symbol                            | Value       | Unit          |
|--|-----------------------------------|-------------|---------------|
| Collector-Base Voltage   | V <sub>CBO</sub>                  | 150         | Vdc           |
| Collector-Emitter Voltage  | V <sub>CEV</sub>                  | 150         | Vdc           |
| Emitter-Base Voltage   | V <sub>EBO</sub>                  | 7           | Vdc           |
| Collector Current - Continuous<br>Peak (1)                         | I <sub>C</sub><br>I <sub>CM</sub> | 20<br>30    | Adc           |
| Base Current - Continuous  | I <sub>B</sub>                    | 5           | Adc           |
| Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C | P <sub>D</sub>                    | 140<br>0.8  | Watts<br>W/°C |
| Operating and Storage Junction Temperature Range                   | T <sub>J</sub> , T <sub>stg</sub> | -65 to +200 | °C            |

#### THERMAL CHARACTERISTICS

| Characteristic                       | Symbol         | Max  | Unit |
|--------------------------------------|----------------|------|------|
| Thermal Resistance, Junction to Case | $R_{	heta JC}$ | 1.25 | °C/W |

<sup>\*</sup>Indicates JEDEC Registered Data.

<sup>(1)</sup> Pulse Test: Pulse Width ≤ 10 ms, Duty Cycle ≤ 50%.

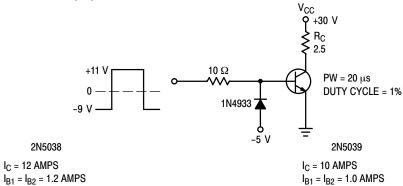


Figure 1. Switching Time Test Circuit

\*ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

| Characteristic  |                           | Symbol                | Min    | Max      | Unit |
|---|---------------------------|-----------------------|--------|----------|------|
| OFF CHARACTERISTICS   | 3                         |                       |        | •        |      |
| Collector-Emitter Sustai<br>(I <sub>C</sub> = 200 mAdc, I <sub>B</sub> = 0  | 3                         | V <sub>CEO(sus)</sub> | 90     | -        | Vdc  |
| Collector Cutoff Current $(V_{CE} = 140 \text{ Vdc}, V_{BE(off)} = 1.5 \text{ V})$ $(V_{CE} = 100 \text{ Vdc}, V_{BE(off)} = 1.5 \text{ Vdc}, T_{C} = 150^{\circ}\text{C})$ |                           | I <sub>CEX</sub>      | -      | 50<br>10 | mAdc |
| Emitter Cutoff Current $(V_{EB} = 5 \text{ Vdc}, I_C = 0)$ $(V_{EB} = 7 \text{ Vdc}, I_C = 0)$  |                           | I <sub>EBO</sub>      | -<br>- | 5<br>50  | mAdc |
| ON CHARACTERISTICS  | (2)                       | <u>.</u>              |        |          |      |
| DC Current Gain<br>(I <sub>C</sub> = 12 Adc, V <sub>CE</sub> = 5  | Vdc)                      | h <sub>FE</sub>       | 20     | 100      | -    |
| Collector-Emitter Satura<br>(I <sub>C</sub> = 20 Adc, I <sub>B</sub> = 5 Ad   | S .                       | V <sub>CE(sat)</sub>  | -      | 2.5      | Vdc  |
| Base-Emitter Saturation Voltage (I <sub>C</sub> = 20 Adc, I <sub>B</sub> = 5 Adc)   |                           | V <sub>BE(sat)</sub>  | _      | 3.3      | Vdc  |
| DYNAMIC CHARACTER   | ISTICS                    |                       |        |          | 1    |
| Magnitude of Common-<br>Forward Current Trans<br>(I <sub>C</sub> = 2 Adc, V <sub>CE</sub> = 10  |                           | h <sub>fe</sub>       | 12     | -        | -    |
| SWITCHING CHARACTE  | RISTICS                   |                       |        | •        | •    |
| RESISTIVE LOAD  |                           |                       |        |          |      |
| Rise Time (\  | / <sub>CC</sub> = 30 Vdc) | t <sub>r</sub>        | -      | 0.5      | μS   |
| Storage Time $(I_C = 12 \text{ Adc}, I_{B1} = I_{B2} = 1.2 \text{ Adc})$  |                           | t <sub>s</sub>        | _      | 1.5      | μs   |

<sup>\*</sup>Indicates JEDEC Registered Data.

<sup>(2)</sup> Pulse Test: Pulse Width  $\leq$  300,  $\mu$ s, Duty Cycle  $\leq$  2%.

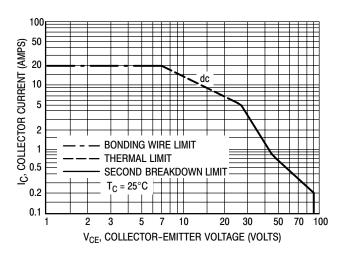


Figure 2. Forward Bias Safe Operating Area

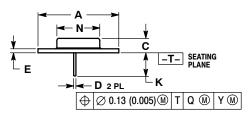
There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C$  –  $V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

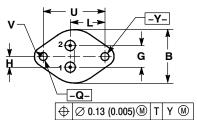
Second breakdown pulse limits are valid for duty cycles to 10%. At high case temperatures, thermal limitations may reduce the power that can be handled to values less than the limitations imposed by second breakdown.

#### 2N5038

#### PACKAGE DIMENSIONS

### **CASE 1-07** TO-204AA (TO-3) **ISSUE Z**





#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- 3. ALL RULES AND NOTES ASSOCIATED WITH REFERENCED TO-204AA OUTLINE SHALL APPLY.

|     | INCHES    |           | MILLIMETERS |           |  |
|-----|-----------|-----------|-------------|-----------|--|
| DIM | MIN       | MAX       | MIN         | MAX       |  |
| Α   | 1.550 REF |           | 39.37 REF   |           |  |
| В   |           | 1.050     |             | 26.67     |  |
| C   | 0.250     | 0.335     | 6.35        | 8.51      |  |
| D   | 0.038     | 0.043     | 0.97        | 1.09      |  |
| Е   | 0.055     | 0.070     | 1.40        | 1.77      |  |
| G   | 0.430 BSC |           | 10.92 BSC   |           |  |
| Н   | 0.215 BSC |           | 5.46 BSC    |           |  |
| K   | 0.440     | 0.480     | 11.18       | 12.19     |  |
| L   | 0.665     | 0.665 BSC |             | 16.89 BSC |  |
| N   |           | 0.830     |             | 21.08     |  |
| Q   | 0.151     | 0.165     | 3.84        | 4.19      |  |
| 5   | 1.187 BSC |           | 30.15 BSC   |           |  |
| ٧   | 0.131     | 0.188     | 3.33        | 4.77      |  |

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082-1312 USA Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free

Japan: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051 Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.