PT78NR100 Series

1 Amp Plus to Minus Voltage Integrated Switching Regulator
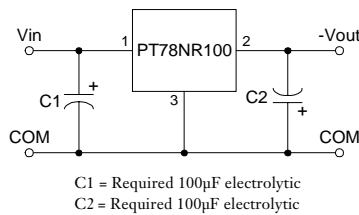
- Negative output from positive input
- Wide Input Range
- Self-Contained Inductor
- Short Circuit Protection
- Over-Temperature Protection
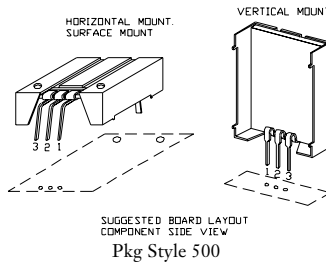- Fast Transient Response

The PT78NR100 Series creates a negative output voltage from a positive input voltage greater than 7V. These easy-to-use, 3-terminal, Integrated Switching Regulators (ISRs) have maximum output power of 5 watts and a negative output voltage that is laser trimmed. They also have excellent line and load regulation.

Standard Application

C1 = Required 100µF electrolytic
C2 = Required 100µF electrolytic

Pin-Out Information

Pin	Function
1	+V_{in}
2	-V_{out}
3	GND

HORIZONTAL MOUNT. SURFACE MOUNT.

VERTICAL MOUNT

SUGGESTED BOARD LAYOUT
COMPONENT SIDE VIEW
Pkg Style 500

Ordering Information

PT78NR1 XX Y

Output Voltage
- **03** = -3.0 Volts
- **05** = -5.0 Volts
- **52** = -5.2 Volts
- **07** = -7.0 Volts
- **08** = -8.0 Volts
- **09** = -9.0 Volts
- **12** = -12.0 Volts
- **15** = -15.0 Volts

Package Suffix
- **V** = Vertical Mount
- **S** = Surface Mount
- **H** = Horizontal Mount

Specifications

Characteristics (T$_a$= 25°C unless noted)	Symbols	Conditions	PT78NR100 SERIES Min	Typ	Max	Units
Output Current	I_o	Over V_{in} range V_o=-5V	0.05*	—	1.00	A
		V_o=-7, -8, -9V	0.05*	—	0.55	A
		V_o=-12V	0.05*	—	0.40	A
		V_o=-15V	0.05*	—	0.30	A
Short Circuit Current	I_{sc}	V_{in}=10V	—	4×I_{max}	—	Apk
Inrush Current	I_{ir}	V_{in}=10V	—	4	—	A
	t_{ir}	On start-up	—	0.5	—	mSec
Input Voltage Range	V_{in}	$0.1 \le I_o \le I_{max}$ V_o=-5V	7	—	25	V
		V_o=-7, -8, -9V	7	—	21	V
		V_o=-12V	7	—	18	V
		V_o=-15V	7	—	15	V
Output Voltage Tolerance	ΔV_o	Over V_{in} range T$_a$=-20°C to +70°C	—	±1.0	±3.0	%V_o
Line Regulation	Reg$_{line}$	Over V_{in} range	—	±0.5	±1.0	%V_o
Load Regulation	Reg$_{load}$	$0.1 \le I_o \le I_{max}$	—	±0.5	±1.0	%V_o
V_o Ripple/Noise	V_n	V_{in}=10V, I_o=I_{max}	—	±2	—	%V_o
Transient Response (with 100µF output cap)	t_{tr}	50% load change V_o over/undershoot	—	100 / 5.0	250 / —	µSec / %V_o
Efficiency	η	V_{in}=10V, I_o=0.5×I_{max}, V_o = -5V	—	75	—	%
Switching Frequency	f_o	Over V_{in} and I_o ranges	600	650	700	kHz
Absolute Maximum Operating Temperaturte Range	T$_a$	Free Air Convection, (40-60LFM) Over V_{in} and I_o Ranges	-40	—	+85	°C
Recommended Operating Temperature Range	T$_a$	Free Air Convection, (40-60LFM) Over V_{in} and I_o Ranges	-40	—	+60**	°C
Thermal Resistance	θ$_{ja}$	Free Air Convection, (40-60LFM)	—	45	—	°C/W
Storage Temperature	T$_s$	—	-40	—	+125	°C
Mechanical Shock	—	Per Mil-STD-883D, Method 2002.3	—	500	—	G's
Mechanical Vibration	—	Per Mil-STD-883D, Method 2007.2, 20-2000 Hz, soldered in a PC board	—	5	—	G's
Weight	—	—	—	6.5	—	Grams

*ISR will operate down to no load with reduced specifications. **See Thermal Derating chart.

Note: The PT78NR100 Series requires a 100µF electrolytic or tantalum output capacitor for proper operation in all applications.
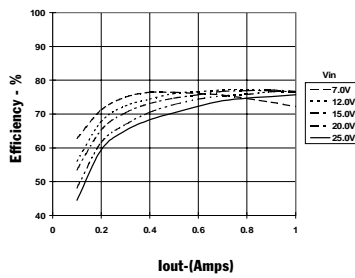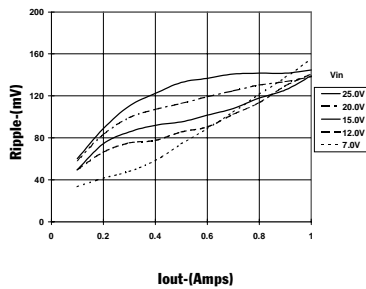
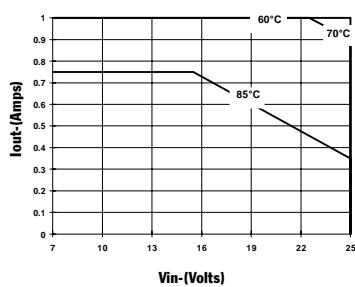### PT78NR105 -5.0 VDC (See Note 1)
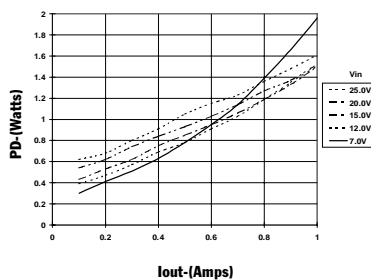
Efficiency vs Output Current

Ripple vs Output Current
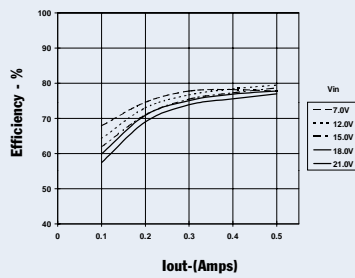

Thermal Derating (T$_a$) (See Note 2)
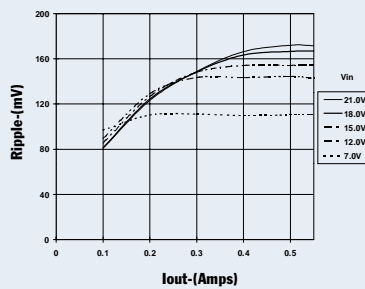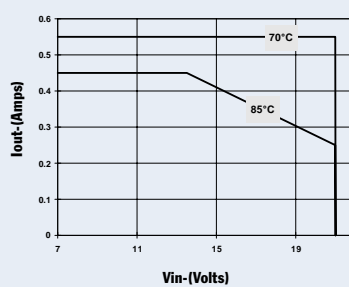

Power Dissipation vs Output Current

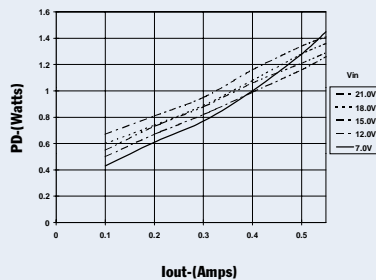### PT78NR109 -9.0 VDC (See Note 1)

Efficiency vs Output Current

Ripple vs Output Current
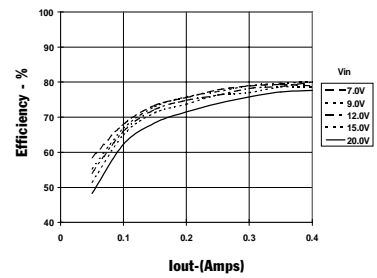

Thermal Derating (T$_a$) (See Note 2)

Power Dissipation vs Output Current
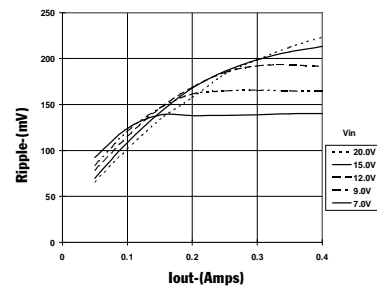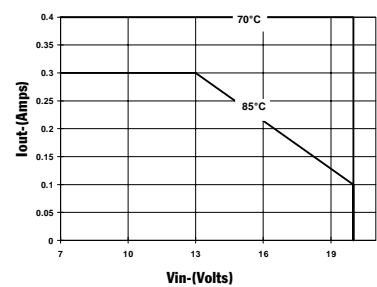

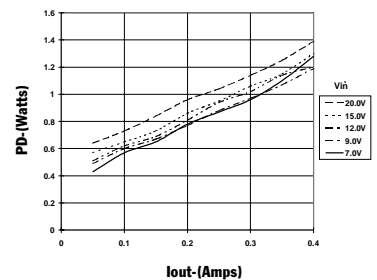### PT78NR112 -12.0 VDC (See Note 1)

Efficiency vs Output Current

Ripple vs Output Current

Thermal Derating (T$_a$) (See Note 2)

Power Dissipation vs Output Current

Note 1: *All data listed in the above graphs, except for derating data, has been developed from actual products tested at 25°C. This data is considered typical data for the ISR.*
Note 2: *Thermal derating graphs are developed in free air convection cooling of 40-60 LFM. (See Thermal Application Notes.)*

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