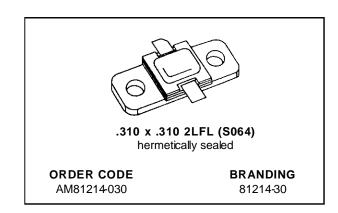


AM81214-030

RF & MICROWAVE TRANSISTORS L-BAND RADAR APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- RUGGEDIZED VSWR ∞:1
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- Pout = 26 W MIN. WITH 7.2 dB GAIN

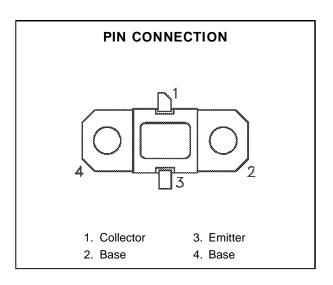


DESCRIPTION

The AM81214-030 device is a high power transistor specifically designed for L-Band Radar pulsed driver applications.

The device is capable of operation over a wide range of pulse widths, duty cycles and temperatures and is capable of withstanding ∞:1 output VSWR at rated RF conditions. Low RF thermal resistance and computerized automatic wire bonding techniques ensure high reliability and product consistency.

The AM81214-030 is supplied in the IMPAC™ Hermetic Metal/Ceramic package with internal Input/Output matching structures.



ABSOLUTE MAXIMUM RATINGS (Tcase = 25°C)

Symbol	Parameter	Value	Unit
P _{DISS}	Power Dissipation* $(T_C \le 100^{\circ}C)$	63	W
Ic	Device Current*	2.75	А
Vcc	Collector-Supply Voltage*	32	V
TJ	Junction Temperature (Pulsed RF Operation)	250	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

R _{TH(j-c)} Junction-Case Thermal Resistance*	2.4	°C/W
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^{*}Applies only to rated RF amplifier operation

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ELECTRICAL SPECIFICATIONS (Tcase = 25°C)

STATIC

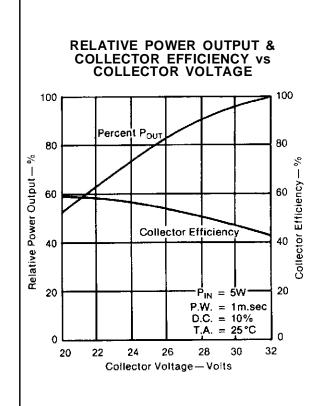
			Value			
Symbol		Test Conditions	Min.	Тур.	Max.	Unit
ВУсво	I _C = 10mA	$I_E = 0mA$	55	_	_	V
BV _{EBO}	I _E = 1mA	$I_C = 0mA$	3.5	_	_	V
BV _{CER}	IC = 20mA	$R_{BE} = 10\Omega$	55	_	_	V
ICES	V _{BE} = 0V	$V_{CE} = 28V$	_	_	5	mA
h _{FE}	V _{CE} = 5V	$I_C = 1A$	15	_	150	_

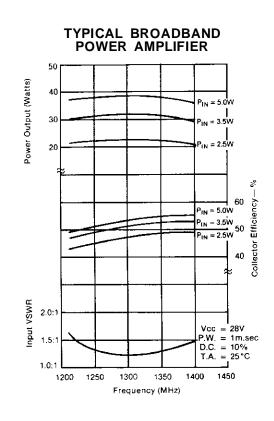
DYNAMIC

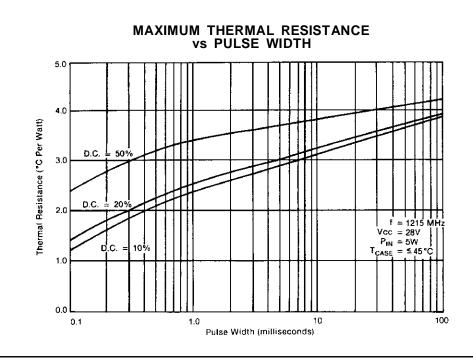
				Value			
Symbol	Test Conditions			Min.	Тур.	Max.	Unit
PIN	f = 1215 — 1400MHz	$P_{\text{IN}} = 5W \text{ Peak}$	$V_{CC} = 28V$	26	36	_	W
ης	f = 1215 — 1400MHz	$P_{\text{IN}} = 5W \text{ Peak}$	$V_{CC}=28V$	45	49	_	%
GP	f = 1215 — 1400MHz	P _{IN} = 5W Peak	$V_{CC} = 28V$	7.2	8.5	_	dB

Note: Pulse Width = 1000μ S Duty Cycle = 10%

TYPICAL PERFORMANCE

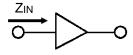




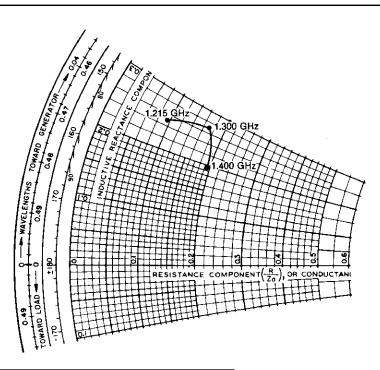


IMPEDANCE DATA



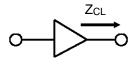


 $\begin{aligned} P_{IN} &= 5.0 \text{ W} \\ V_{CC} &= 28 \text{ V} \\ Z_{O} &= 50 \text{ Ohms} \end{aligned}$

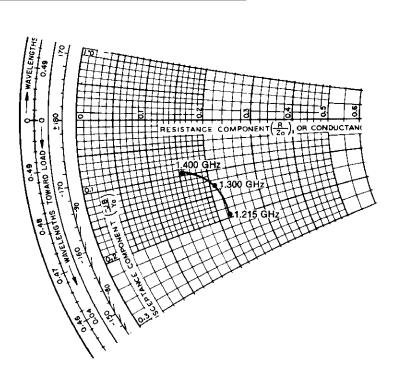


FREQ.	$Z_{IN}\left(\Omega\right)$	Z _{CL} (Ω)
L = 1.215 GHz	4.5 + j 12.5	11.0 – j 10.0
M = 1.300 GHz	8.5 + j 13.5	10.5 – j 6.5
H = 1.400 GHz	9.5 + j 10.0	8.0 – j 5.0

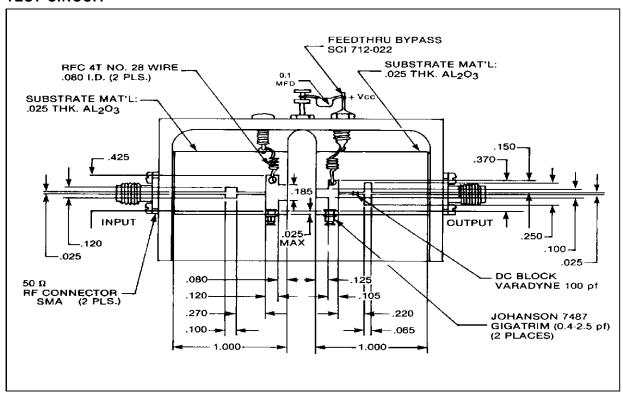
TYPICAL COLLECTOR LOAD IMPEDANCE



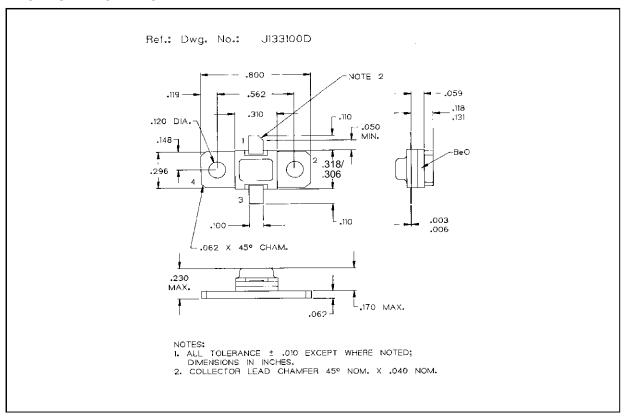
 $\begin{aligned} P_{IN} &= 5.0 \text{ W} \\ V_{CC} &= 28 \text{ V} \\ Z_{O} &= 50 \text{ Ohms} \end{aligned}$



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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