150mA Hi-PSRR, Low-Quiescent LDO with In-Rush Current Control For USB Application

■ General Description

The AME8831 family of positive, linear regulators feature low quiescent current (17µA typ.) with low dropout voltage, making them ideal for battery applications. The space-saving SOT-25 package is attractive for "Pocket" and "Hand Held" applications.

These rugged devices have both Thermal Shutdown, and Current limitation to prevent device failure under the "Worst" of operating conditions. In applications requires a low noise regulated supply. The AME8831 family uses the SR pin to program the output voltage's slew rate to control the in-rush current. This is specifically used in the USB application where large load capacitance is present at start-up.

The AME8831 also features a logic-enabled sleep mode to shutdown the regulator, reducing quiescent current to 1µA typical at T_A= 25°C.

The AME8831 is stable with an output capacitance of 4.7µF or larger.

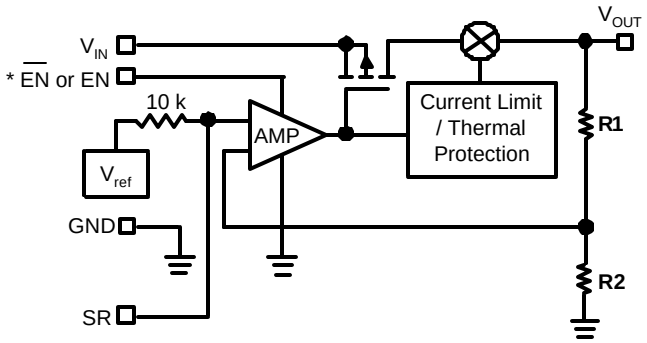
■ Features

- Guaranteed 150mA Output
- Dropout Voltage Typically 150 mV at 150 mA
- 17µA Quiescent Current
- Over-Temperature Shutdown
- Over-Current Limitation
- Noise Reduction Bypass Capacitor
- Power-Saving Shutdown Mode
- Space-Saving SOT-25 Package
- Factory Pre-set Output Voltages
- Enable pin option
  - $\overline{EN}$ active low enable
  - EN active high enable
- All AME's Lead Free Products Meet RoHS Standards

■ Applications

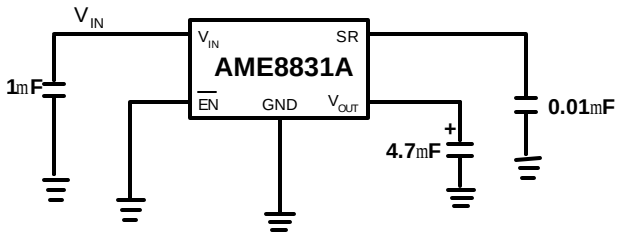
- Instrumentation
- Portable Electronics
- Wireless Devices
- Cordless Phones
- PC Peripherals
- Battery Powered Widgets
- Electronic Scales

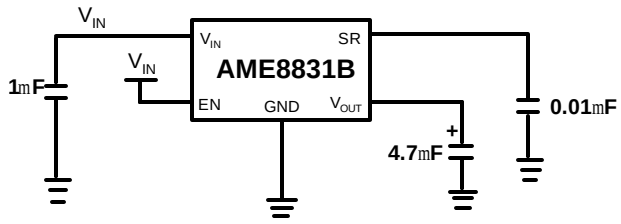
■ Function Block Diagram



* AME8831A: $\overline{EN}$, AME8831B: EN

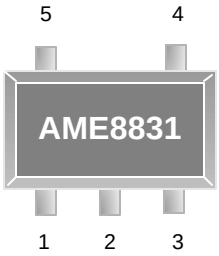
■ Typical Application





■ Pin Configuration

SOT-25
Top View

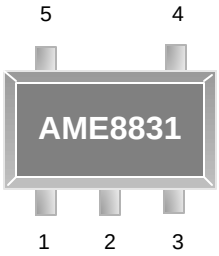


AME8831A

1. V_{IN}
2. GND
3. $\overline{EN}$
4. SR
5. V_{OUT}

*** Die Attach:**
Conductive Epoxy

SOT-25
Top View



AME8831B

1. V_{IN}
2. GND
3. EN
4. SR
5. V_{OUT}

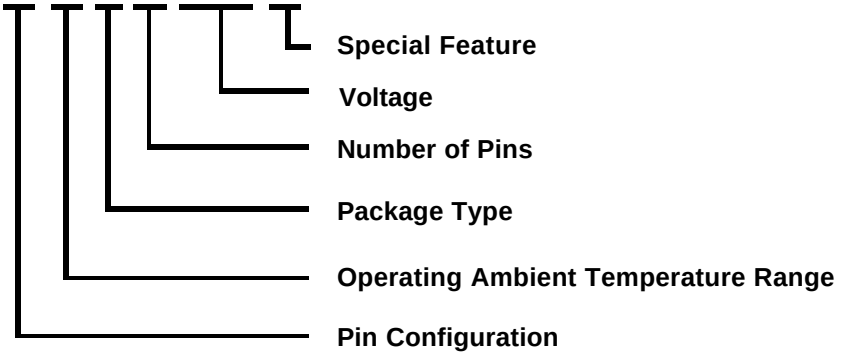
*** Die Attach:**
Conductive Epoxy

■ Pin Description

Pin Number	Pin Name	Pin Description
1	V_{IN}	The V_{IN} terminal is the input to the device.
2	GND	Regulator ground.
3	$\overline{EN}$, EN	$\overline{EN}$ active low enable, EN active high enable.
4	SR	The SR terminal is used to control the in-rush current.
5	V_{OUT}	The V_{OUT} terminal is the regulated output of the device.

■ Ordering Information

AME8831 x x x x xxx x



Pin Configuration	Operating Ambient Temperature Range	Package Type	Number of Pins	Voltage	Special Feature
A: 1. V_{IN} 2. GND 3. EN 4. SR 5. V_{OUT} B: 1. V_{IN} 2. GND 3. EN 4. SR 5. V_{OUT}	E: -40°C to +85°C	E: SOT-2X	V: 5	330: V=3.3V	Y: Lead free & Low profile Z: Lead free

■ Ordering Information

Part Number	Marking*	Output Voltage	Package	Operating Ambient Temperature Range
AME8831AEEV330Z	BDVww	3.3V	SOT-25	- 40°C to + 85°C
AME8831AEEV330Y	BDVww	3.3V	TSOT-25	- 40°C to + 85°C

Note: ww represents the date code and pls refer to the Date Code Rule befor Package Dimension.
* A line on top of the first character represents lead free plating such as BDVww.
Please consult AME sales office or authorized Rep./Distributor for output voltage and package type availability.

■ Absolute Maximum Ratings

Parameter	Maximum	Unit
Input Voltage	6	V
Output Current	$P_D / (V_{IN} - V_{OUT})$	mA
Output Voltage	GND-0.3 to V_{IN}+0.3	V
ESD Classification	C*	

Caution: Stress above the listed absolute maximum rating may cause permanent damage to the device
HBM C: 4000V+

■ Recommended Operating Conditions

Parameter	Symbol	Rating	Unit
Ambient Temperature Range	T_A	- 40 to +85	^oC
Junction Temperature Range	T_J	- 40 to +125	
Storage Temperature Range	T_{STG}	-65 to +150	

■ Thermal Information

Parameter	Package	Die Attach	Symbol	Maximum	Unit
Thermal Resistance* (Junction to Case)	SOT-25	Conductive Epoxy	θ_{JC}	81	^oC / W
Thermal Resistance (Junction to Ambient)			θ_{JA}	260	
Internl Power Dissipation			P_D	400	mW
Maximum Junction Temperature				150	^oC
Solder Iron (10 Sec)**				350	^oC

* Measure θ_{JC} on center of molding compound if IC has no tab.
** MIL-STD-202G 210F

■ Electrical Specifications (contd.)

$\overline{EN}$=0, T_J = -40 to +125°C, V_{IN} = V_{OUT}(typ) +1V, I_{OUT} = 1mA, C_{OUT} = 4.7µF, $C_{(SR)}$ = 0.01µF (unless otherwise noted)

Parameter	Symbol	Test Condition		Min	Typ	Max	Units
Input Voltage	V_{IN}	(See Note1)		3.5		5.5	V
Output Voltage	V_{OUT}	T_J = 25°C			3.3		V
		10µA < I_{OUT} < 150mA 3.8V < V_{IN} < 5.5V	T_J = -40°C to +125°C	3.201		3.399	
Output Current	I_{OUT}	(See Note2)		0		150	mA
Output Current Limit	I_{LIM}	V_{OUT} = 0V		200	350	750	mA
Quiescent Current	I_Q	10µA < I_{OUT} < 150mA	T_J = 25°C		17		µA
		10µA < I_{OUT} < 150mA	T_J = -40°C to +125°C			30	
Line Regulation (See Note 3)	REG_{LINE}	4.3V<V_{IN}<5.5V	T_J = 25°C	-0.2	0.1	0.2	%/V
		4.3V<V_{IN}<5.5V	T_J= -40°C to +125°C	-0.3		0.3	
Load Regulation (See Note 4)	REG_{LOAD}	1mA <= I_{OUT} <= 150mA	T_J = 25°C	-0.02	0.0025	0.02	%/mA
Dropout Voltage	V_{DO}	V_{OUT} = V_{OUT}(NOM) -2.0%	T_J = 25°C		150		mV
			T_J = -40°C to +125°C			300	
Over Temerature Shutdown	OTS				150		°C
Over Temerature Hysterisis	OTH				20		
V_{OUT} Temperature Coefficient	TC				30		ppm
Power Supply Ripple Rejection	PSRR	f = 1 kHz, I_{OUT} = 150mA C_{OUT} =10µF, $C_{(SR)}$ = 0.01µF	T_J = 25°C		65		dB

Note1 : $V_{IN(min)} = V_{OUT(min)} + V_{DO\ (max load)}$

Note2 : Continuous output current and operating junction temperature are limited by internal protection circuitry, but it is not recommended that the device operate under conditions beyond those specified in this table for extended periods of time.

Note3: Line Reg: $$\frac{\frac{\Delta Vout}{\Delta Vin}\times 100\%}{Vout}$$

Note4: Load Reg: $$\frac{\frac{\Delta Vout}{Vout}\times 100\%}{\Delta I}$$

■ Electrical Specifications

Parameter	Symbol	Test Condition			Min	Typ	Max	Units
Output Voltage Noise	e_N	BW=200Hz to 100kHz I_{OUT} = 150mA C_{OUT} = 10μF $C_{(SR)}$ =0 .47μF		T_J = 25°C		100		μVRMS
EN, $\overline{EN}$ Input Threshold	V_{EH}	V_{IN} = 3.5V to 5.5V			2.0		V_{IN}	V
	V_{EL}				0		0.4	
EN, $\overline{EN}$ Input Bias Current	I_{EN}, $I_{\overline{EN}}$	EN = V_{IN}, $\overline{EN}$ = 0, V_{IN} = 3.5V to 5.5V					1	μA
Shutdown Current	I_{SD}	EN = 0, $\overline{EN}$ = V_{IN}				1	2	μA
Turn_on Time	t_{ON}	R_{LOAD} = 22Ω C_{OUT}= 10μF	$C_{(SR)}$= 0.01μF	T_J = 25°C		20		ms
			$C_{(SR)}$ = 0.1μF			200		
			$C_{(SR)}$ = 0.22μF			450		

■ Detail Description

The AME8831 family of CMOS regulators contain a PMOS pass transistor, voltage reference, error amplifier, over-current protection, and thermal shutdown function.

The P-channel pass transistor receives data from the error amplifier, over-current limit, and thermal protection circuits. During normal operation, the error amplifier compares the output voltage to a precision reference. Over-current and Thermal shutdown circuits become active when the junction temperature exceeds 150°C, or the current exceeds about 350mA. During thermal shutdown, the output voltage remains low. Normal operation is restored when the junction temperature drops below 120°C.

The AME8831 switches from voltage mode to current mode when the load exceeds the rated output current. This prevents over-stress.

■ External Capacitors

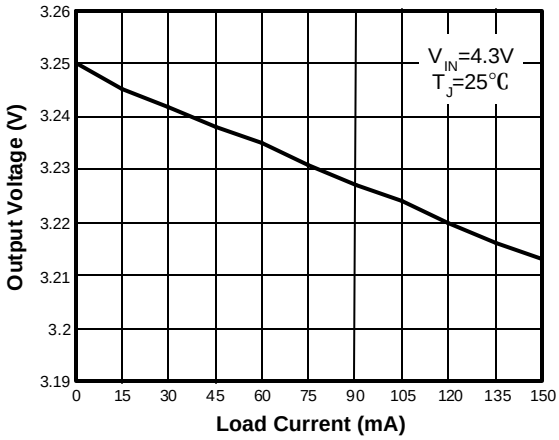
The AME8831 is stable with an output capacitor to ground of 2.2µF or greater. Ceramic capacitors have the lowest ESR, and will offer the best AC performance. Conversely, Aluminum Electrolytic capacitors exhibit the highest ESR, resulting in the poorest AC response. Unfortunately, large value ceramic capacitors are comparatively expensive. One option is to parallel a 0.1µF ceramic capacitor with a 10µF Aluminum Electrolytic. The benefit is low ESR, high capacitance, and low overall cost.

A second capacitor is recommended between the input and ground to stabilize V_{IN}. The input capacitor should be at least 0.1µF to have a beneficial effect. All capacitors should be placed in close proximity to the pins. A "Quiet" ground termination is desirable. This can be achieved with a "Star" connection

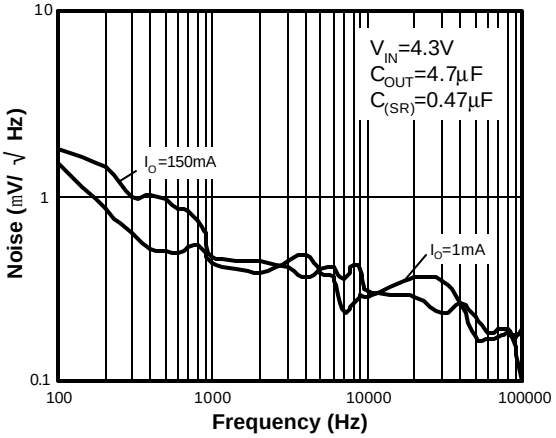
■ Enable

The Enable pin is optional. EN for active high enable, $\overline{EN}$ for active low enable. When disable the Enable Pin EN=0, $\overline{EN}=V_{IN}$, the PMOS pass transistor shuts off, and all internal circuits are powered down. In this state, the standby current is less than 1µA.

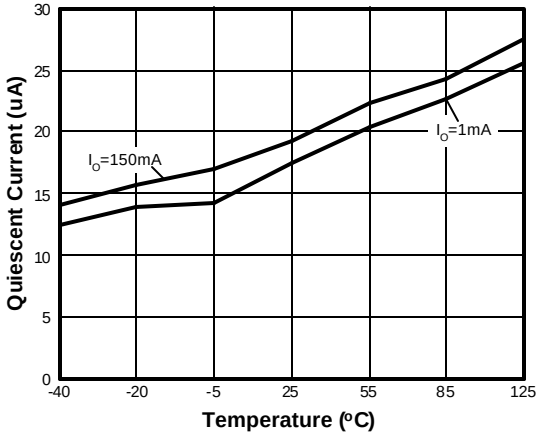
Output Voltage vs Load Current



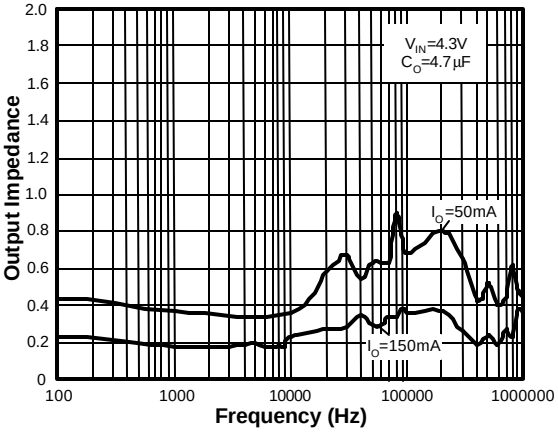
Output Noise Spectral Density



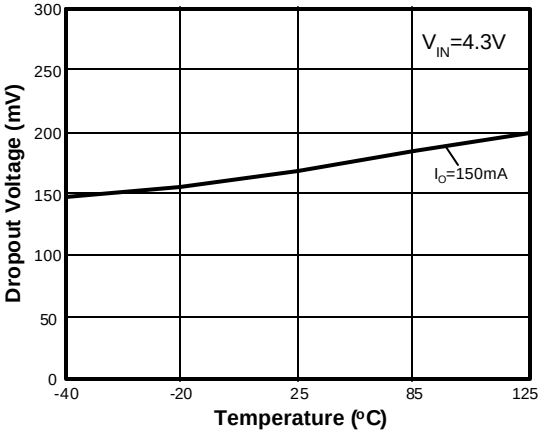
Quiescent Current vs Free Air Temp.



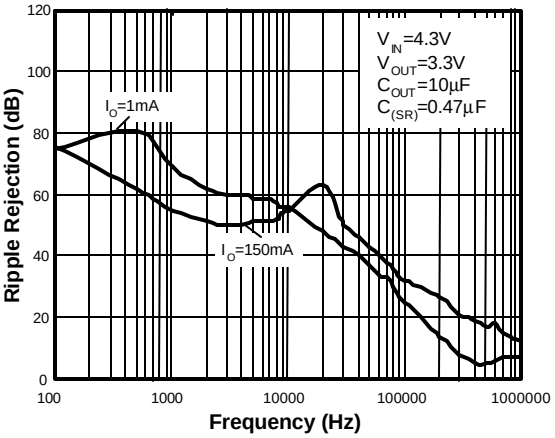
Output Impedance vs Frequency



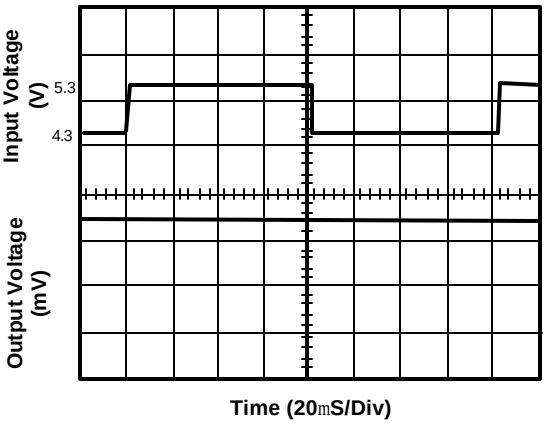
Dropout Voltage vs. Temperature



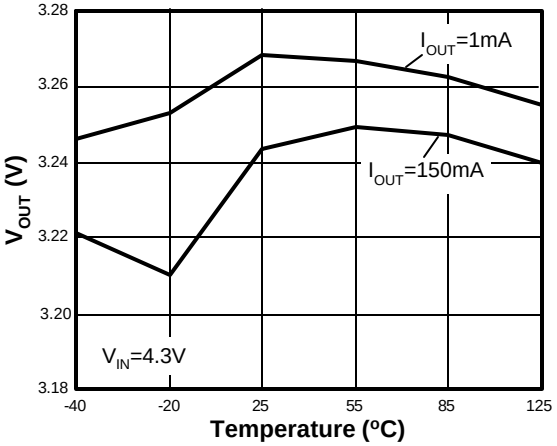
Ripple Rejection vs Frequency



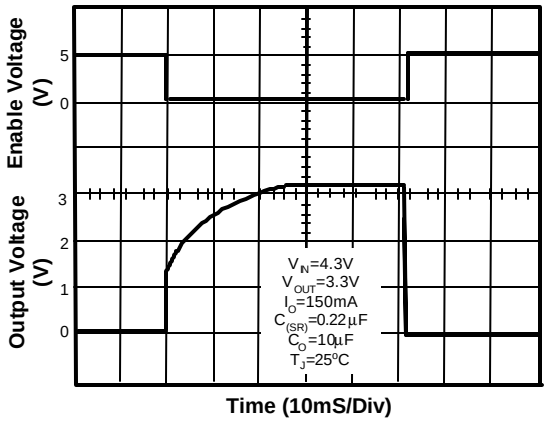
Line Transient Response



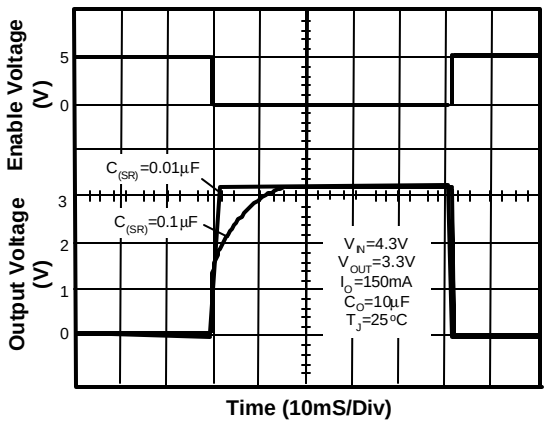
V_{OUT} vs Temperature



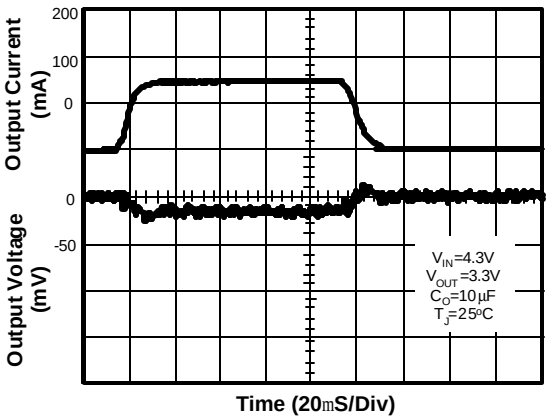
Output Voltage, Enable Voltage vs Time (Start-Up)



Output Voltage, Enable Voltage vs Time (Start-Up)



Load Transient Response

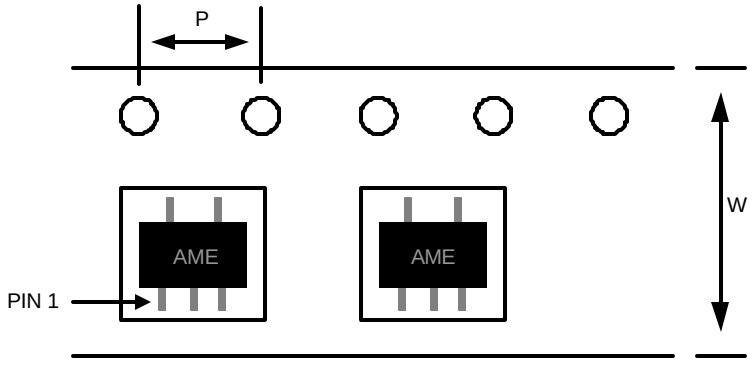


■ Date Code Rule

Marking			Date Code		Year
A	A	A	W	W	xxx0
A	A	A	W	W	xxx1
A	A	A	W	W	xxx2
A	A	A	W	W	xxx3
A	A	A	W	W	xxx4
A	A	A	W	W	xxx5
A	A	A	W	W	xxx6
A	A	A	W	W	xxx7
A	A	A	W	W	xxx8
A	A	A	W	W	xxx9

■ Tape and Reel Dimension

SOT-25

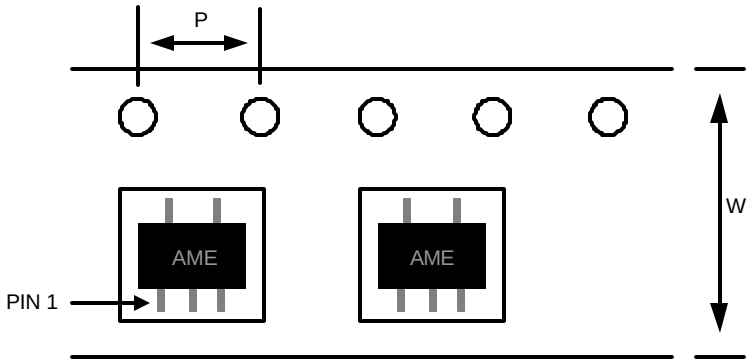


Carrier Tape, Number of Components Per Reel and Reel Size

Package	Carrier Width (W)	Pitch (P)	Part Per Full Reel	Reel Size
SOT-25	8.0±0.1 mm	4.0±0.1 mm	3000pcs	180±1 mm

■ Tape and Reel Dimension

TSOT-25



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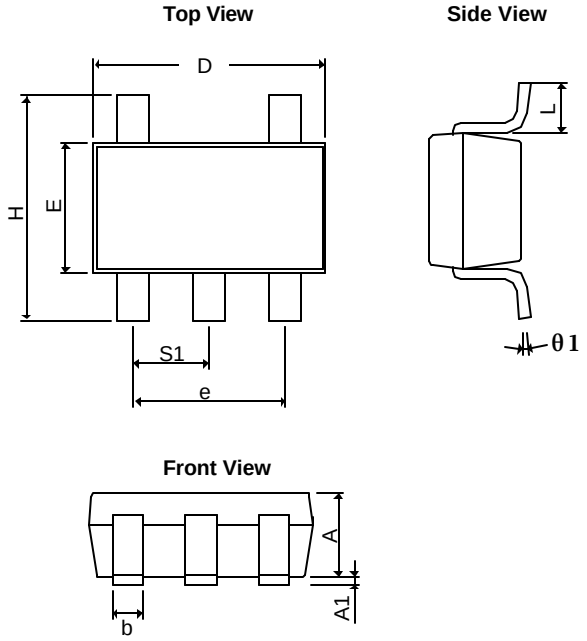
TSOT-25	8.0±0.1 mm	4.0±0.1 mm	3000pcs	180±1 mm

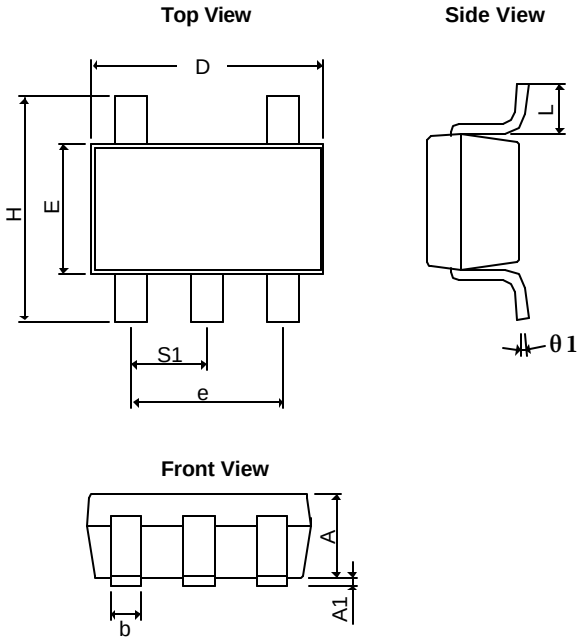
■ Package Dimension

SOT-25



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.20REF		0.0472REF	
A_1	0.00	0.15	0.0000	0.0059
b	0.30	0.55	0.0118	0.0217
D	2.70	3.10	0.1063	0.1220
E	1.40	1.80	0.0551	0.0709
e	1.90 BSC		0.07480 BSC	
H	2.60	3.00	0.10236	0.11811
L	0.37BSC		0.0146BSC	
q1	0^o	10^o	0^o	10^o
S_1	0.95BSC		0.0374BSC	

TSOT-25



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
$A+A_1$	0.90	1.25	0.0354	0.0492
b	0.30	0.50	0.0118	0.0197
c	0.09	0.25	0.0035	0.0098
D	2.70	3.10	0.1063	0.1220
E	1.40	1.80	0.0551	0.0709
e	1.90 BSC		0.07480 BSC	
H	2.40	3.00	0.09449	0.11811
L	0.35BSC		0.0138BSC	
q1	0^o	10^o	0^o	10^o
S_1	0.95BSC		0.0374BSC	

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