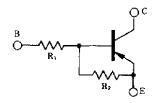


AP1 SERIES

on-chip resistor NPN silicon epitaxial transistor For mid-speed switching

FEATURES

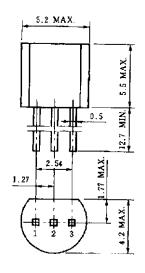
- · Current drive available up to 0.7 A
- · On-chip bias resistor
- Low power consumption during drive



AP1 SERIES LISTS

Products	R ₁ (KΩ)	R ₂ (KΩ)
AP1A4A	-	10
AP1L2Q	0.47	4.7
AP1A3M	1.0	1.0
AP1F3P	2.2	10
AP1J3P	3.3	10
AP1L3N	4.7	10
AP1A4M	10	10

PACKAGE DRAWING (UNIT: mm)



Electrode Connection

Emitter EIAJ : SC-43B
 Collector JEDEC: TO-92
 Base IEC : PA33

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Parameter	Symbol	Ratings	Unit
Collector to base voltage	Vсво	-25	V
Collector to emitter voltage	Vceo	-25	V
Emitter to base voltage	V _{EBO}	-10	V
Collector current (DC)	Ic(DC)	-0.7	А
Collector current (Pulse)	Ic(pulse) *	-1.0	А
Base current (DC)	I _{B(DC)}	-0.02	Α
Total power dissipation	Рт	750	mW
Junction temperature	Tj	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

^{*} PW \leq 10 ms, duty cycle \leq 50 %

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version. Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.



AP1A4A
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	Vcb = -22 V, IE = 0			100	nA
DC current gain	h _{FE1} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.1 \text{ A}$	200			-
DC current gain	h _{FE2} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.5 \text{ A}$	100			-
DC current gain	h _{FE3} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.7 \text{ A}$	50			-
Collector saturation voltage	V _{CE(sat)} **	Ic = -0.3 A, $Ic = -6 A$		-0.28	-0.4	V
Low level input voltage	VIL **	$V_{CE} = -5.0 \text{ V}, \text{ Ic} = -100 \ \mu\text{A}$			-0.3	V
Input resistance	R ₁		-	-	-	Ω
E-to-B resistance	R ₂		7	10	13	kΩ

^{**} PW \leq 350 μ s, duty cycle \leq 2 %

AP1L2Q ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	Vcb = -22 V, IE = 0			-100	nA
DC current gain	h _{FE1} **	Vce = -2.0 V, Ic = -0.1 A	150	350		-
DC current gain	h _{FE2} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.5 \text{ A}$	100	300		-
DC current gain	h _{FE3} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.7 \text{ A}$	50	200		_
Low level output voltage	V OL **	$V_{IN} = -5.0 \text{ V}, \text{ Ic} = -0.3 \text{ A}$		-0.3	-0.4	V
Low level input voltage	VIL **	$V_{CE} = -5.0 \text{ V}, \text{ Ic} = -100 \ \mu\text{A}$		-0.65	-0.3	V
Input resistance	R ₁		329	470	611	Ω
E-to-B resistance	R ₂		3.39	4.7	6.11	kΩ

^{**} PW \leq 350 μ s, duty cycle \leq 2 %

AP1A3M ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	Vcb = -22 V, IE = 0			100	nA
DC current gain	h _{FE1} **	$V_{CE} = -2.0 \text{ V}, I_{C} = -0.1 \text{ A}$	80			-
DC current gain	hFE2 **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.5 \text{ A}$	100			-
DC current gain	h _{FE3} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.7 \text{ A}$	50			-
Low level output voltage	V ol **	$V_{IN} = -5.0 \text{ V}, \text{ Ic} = -0.2 \text{ A}$		-0.3	-0.4	V
Low level input voltage	VIL **	$V_{CE} = -5.0 \text{ V}, \text{ Ic} = -100 \ \mu\text{A}$			-0.3	V
Input resistance	R ₁		0.7	1.0	1.3	kΩ
E-to-B resistance	R ₂		0.7	1.0	1.3	kΩ

^{**} PW \leq 350 μ s, duty cycle \leq 2 %



AP1F3P ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	Vcb = -22 V, IE = 0			-100	nA
DC current gain	h _{FE1} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.1 \text{ A}$	200	470		_
DC current gain	h _{FE2} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.5 \text{ A}$	100	300		_
DC current gain	h _{FE3} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.7 \text{ A}$	50	200		-
Low level output voltage	V ol **	$V_{IN} = -5.0 \text{ V}, \text{ Ic} = -0.2 \text{ A}$		-0.2	-0.4	V
Low level input voltage	VIL **	$V_{CE} = -5.0 \text{ V}, \text{ Ic} = -100 \ \mu\text{A}$		-0.65	-0.3	V
Input resistance	R ₁		2.3	3.3	4.3	kΩ
E-to-B resistance	R ₂		7	10	13	kΩ

^{**} PW \leq 350 μ s, duty cycle \leq 2 %

AP1J3P ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	Vcb = -22 V, IE = 0			100	nA
DC current gain	h _{FE1} **	Vce = -2.0 V, Ic = -0.1 A	300	600		-
DC current gain	h _{FE2} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.5 \text{ A}$	300	700		-
DC current gain	h _{FE3} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.7 \text{ A}$	135	600		-
Low level output voltage	V ol **	$V_{IN} = -5.0 \text{ V}, \text{ Ic} = -0.15 \text{ A}$		0.14	0.3	V
Low level input voltage	VIL **	$V_{CE} = -5.0 \text{ V}, \text{ Ic} = -100 \ \mu\text{A}$			0.3	V
Input resistance	R ₁		2.31	3.3	4.29	kΩ
E-to-B resistance	R ₂		7	10	13	kΩ

^{**} PW \leq 350 μ s, duty cycle \leq 2 %

AP1L3N ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	Vcb = -22 V, IE = 0			-100	nA
DC current gain	h _{FE1} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.1 \text{ A}$	200			-
DC current gain	h _{FE2} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.5 \text{ A}$	100			-
DC current gain	h _{FE3} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.7 \text{ A}$	50			-
Low level output voltage	Vol **	$V_{IN} = -5.0 \text{ V}, \text{ Ic} = -0.15 \text{ A}$			-0.45	V
Low level input voltage	VIL **	$V_{CE} = -5.0 \text{ V}, \text{ Ic} = -100 \ \mu\text{A}$			-0.3	V
Input resistance	R ₁		3.29	4.7	6.11	kΩ
E-to-B resistance	R ₂		7	10	13	kΩ

^{**} PW \leq 350 μ s, duty cycle \leq 2 %

Data Sheet D16171EJ1V0DS

3



AP1A4M ELECTRICAL CHARACTERISTICS (Ta = 25°C)

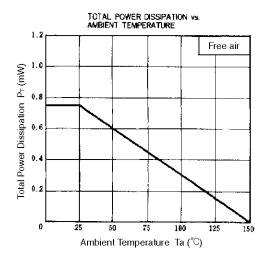
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector cutoff current	Ісво	Vcb = -22 V, IE = 0			-100	nA
DC current gain	h _{FE1} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.1 \text{ A}$	200			-
DC current gain	h _{FE2} **	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.5 \text{ A}$	100			-
DC current gain	h _{FE3} **	$V_{CE} = -2.0 \text{ V}, I_{C} = -0.7 \text{ A}$	50			-
Low level output voltage	V OL **	$V_{IN} = -5.0 \text{ V}, \text{ Ic} = -0.1 \text{ A}$			-0.4	V
Low level input voltage	VIL **	$V_{CE} = -5.0 \text{ V}, \text{ Ic} = -100 \ \mu\text{A}$			-0.3	V
Input resistance	R ₁		7	10	13	kΩ
E-to-B resistance	R ₂		7	10	13	kΩ

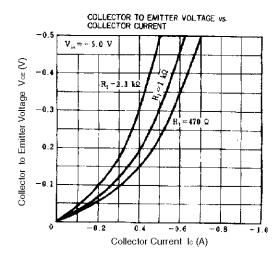
^{**} PW \leq 350 μ s, duty cycle \leq 2 %

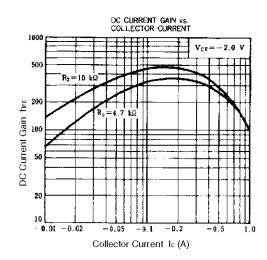
4

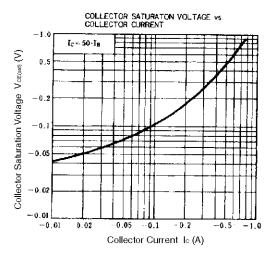


TYPICAL CHARACTERISTICS (Ta = 25°C)









Data Sheet D16171EJ1V0DS 5

- The information in this document is current as of July, 2001. The information is subject to change
 without notice. For actual design-in, refer to the latest publications of NEC's data sheets or data
 books, etc., for the most up-to-date specifications of NEC semiconductor products. Not all products
 and/or types are available in every country. Please check with an NEC sales representative for
 availability and additional information.
- No part of this document may be copied or reproduced in any form or by any means without prior written consent of NEC. NEC assumes no responsibility for any errors that may appear in this document.
- NEC does not assume any liability for infringement of patents, copyrights or other intellectual property rights of
 third parties by or arising from the use of NEC semiconductor products listed in this document or any other
 liability arising from the use of such products. No license, express, implied or otherwise, is granted under any
 patents, copyrights or other intellectual property rights of NEC or others.
- Descriptions of circuits, software and other related information in this document are provided for illustrative
 purposes in semiconductor product operation and application examples. The incorporation of these
 circuits, software and information in the design of customer's equipment shall be done under the full
 responsibility of customer. NEC assumes no responsibility for any losses incurred by customers or third
 parties arising from the use of these circuits, software and information.
- While NEC endeavours to enhance the quality, reliability and safety of NEC semiconductor products, customers
 agree and acknowledge that the possibility of defects thereof cannot be eliminated entirely. To minimize
 risks of damage to property or injury (including death) to persons arising from defects in NEC
 semiconductor products, customers must incorporate sufficient safety measures in their design, such as
 redundancy, fire-containment, and anti-failure features.
- NEC semiconductor products are classified into the following three quality grades:
 - "Standard", "Special" and "Specific". The "Specific" quality grade applies only to semiconductor products developed based on a customer-designated "quality assurance program" for a specific application. The recommended applications of a semiconductor product depend on its quality grade, as indicated below. Customers must check the quality grade of each semiconductor product before using it in a particular application.
 - "Standard": Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots
 - "Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)
 - "Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

The quality grade of NEC semiconductor products is "Standard" unless otherwise expressly specified in NEC's data sheets or data books, etc. If customers wish to use NEC semiconductor products in applications not intended by NEC, they must contact an NEC sales representative in advance to determine NEC's willingness to support a given application.

(Note)

- (1) "NEC" as used in this statement means NEC Corporation and also includes its majority-owned subsidiaries.
- (2) "NEC semiconductor products" means any semiconductor product developed or manufactured by or for NEC (as defined above).

M8E 00.4