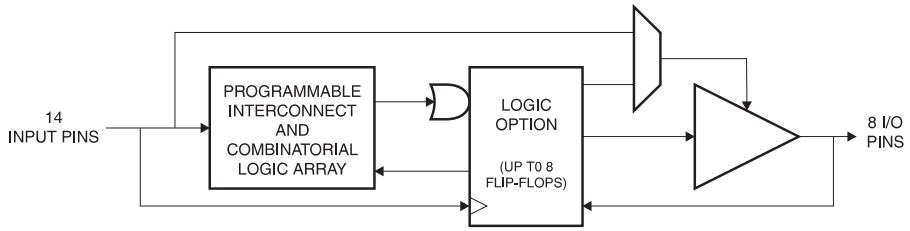
Features

- **User-Controlled Power Down Pin**
- **High-Speed Equivalent of ATF20V8B**
- **Pin-Controlled Zero Standby Power (10 µA Typical) Option**
- **Industry Standard Architecture**
  - **Emulates Many 24-Pin PALs®**
  - **Low-Cost Easy-to-Use Software Tools**
- **High-Speed Electrically-Erasable Programmable Logic Devices**
  - **5 ns Maximum Pin-to-Pin Delay**
- **CMOS and TTL Compatible Inputs and Outputs**
  - **Latch Feature Hold Outputs to Previous Logic States**
- **Advanced Flash Technology**
  - **Reprogrammable**
  - **100% Tested**
- **High-Reliability CMOS Process**
  - **20 Year Data Retention**
  - **100 Erase/Write Cycles**
  - **2,000V ESD Protection**
  - **200 mA Latchup Immunity**
- **Commercial, and Industrial Temperature Ranges**
- **Dual-in-Line and Surface Mount Packages in Standard Pinouts**
- **PCI Compliant**

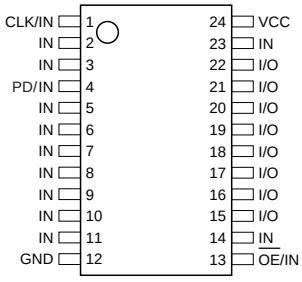
Block Diagram



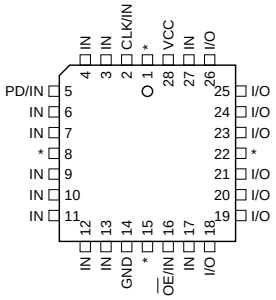
Pin Configurations

Pin Name	Function
CLK	Clock
IN	Logic Inputs
I/O	Bidirectional Buffers
$\overline{OE}$	Output Enable
*	No Internal Connection
VCC	+5V Supply
PD	Power Down

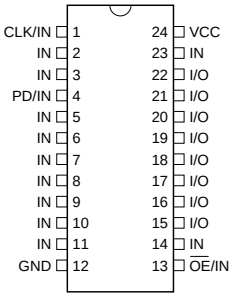
TSSOP Top View



PLCC Top View



DIP



High-Performance EE PLD

ATF20V8C

Advance Information

Rev. 0408D–01/99

Description

The ATF20V8C is a high performance CMOS (Electrically Erasable) Programmable Logic Devices (PLDs) which utilize Atmel's proven electrically erasable Flash memory technology. Speeds down to 5 ns and power dissipation as low as 10 µA are offered. All speed ranges are specified over the full 5V ± 10% range for industrial temperature ranges, and 5V ± 5% for commercial ranges.

The ATF20V8C provides a high-speed CMOS PLD solution with maximum pin to pin delay of 5 ns. The ATF20V8C also has a user-controlled power down feature, offering “zero” standby power (10 µA typical). The user-controlled power down feature allows the user to manage total system power to meet specific application requirements, enhance reliability all without sacrificing speed. Pin “keeper” circuits on input and output pin reduce static power consumed by pull-up resistors.

The ATF20V8C incorporates a superset of the generic architectures, which allows direct replacement of the 20R8 family and most 24-pin combinatorial PLDs. Eight outputs are each allocated eight product terms. Three different modes of operation, configured automatically with software, allowing highly complex logic functions to be realized.

DC and AC Operating Conditions

	Commercial	Industrial
Operating Temperature (Ambient)	0°C - 70°C	-40°C - 85°C
V_{CC} Power Supply	5V ± 5%	5V ± 10%

Functional Description

The ATF20V8C macrocell can be configured in one of three different modes. Each mode makes the ATF20V8C look like a different device. The ATF20V8C can be a registered output, combinatorial I/O, combinatorial output, or dedicated input. Most PLD compilers can choose the right mode automatically. The user can also force the selection by supplying the compiler with a mode selection. The determining factors would be the usage of register versus combinatorial outputs and dedicated outputs versus output with output enable control.

The ATF20V8C has a user controlled power down pin which, when active, allows the user to place the device into a “zero” standby power down mode. The device can also operate at high speed. Maximum pin-to-pin delays of 5 ns are offered. Static power loss due to pull-up resistors is eliminated by using input and output pin “keeper” circuits which holds pins to their previous logic levels when idle.

The universal architecture of the ATF20V8C can be programmed to emulate many 24-pin PAL devices. The user can download the subset device JEDEC programming file to the PLD programmer, and the ATF20V8C can be configured to act like the chosen device.

Unused product terms are automatically disabled by the compiler to decrease power consumption. A Security Fuse, when programmed, protects the contents the ATF20V8C. Eight bytes (64 fuses) of User Signature are accessible to the user for purposes such as storing project name, part number, revision or date. The User Signature is accessible regardless of the state of the Security Fuse.

Atmel Headquarters

Corporate Headquarters
2325 Orchard Parkway
San Jose, CA 95131
TEL (408) 441-0311
FAX (408) 487-2600

Europe
Atmel U.K., Ltd.
Coliseum Business Centre
Riverside Way
Camberley, Surrey GU15 3YL
England
TEL (44) 1276-686677
FAX (44) 1276-686697

Asia
Atmel Asia, Ltd.
Room 1219
Chinachem Golden Plaza
77 Mody Road
Tsimshatsui East
Kowloon, Hong Kong
TEL (852) 27219778
FAX (852) 27221369

Japan
Atmel Japan K.K.
Tonetsu Shinkawa Bldg., 9F
1-24-8 Shinkawa
Chuo-ku, Tokyo 104-0033
Japan
TEL (81) 3-3523-3551
FAX (81) 3-3523-7581

Atmel Operations

Atmel Colorado Springs
1150 E. Cheyenne Mtn. Blvd.
Colorado Springs, CO 80906
TEL (719) 576-3300
FAX (719) 540-1759

Atmel Rousset
Zone Industrielle
13106 Rousset Cedex, France
TEL (33) 4 42 53 60 00
FAX (33) 4 42 53 60 01

Fax-on-Demand
North America:
1-(800) 292-8635

International:
1-(408) 441-0732

e-mail
literature@atmel.com

Web Site
http://www.atmel.com

BBS
1-(408) 436-4309

0408D–01/99/xM