GENERAL DESCRIPTION

The CM1117F series of adjustable and fixed voltage regulators are designed to provide 1A for applications requiring high efficiency. All internal circuitry is designed to operated down to 1V input to output differential and the dropout voltage is fully specified as a function of load current.

The CM1117F offers current limiting and thermal protection. The on chip trimming adjusts the reference voltage accuracy to2%.

FEATURES

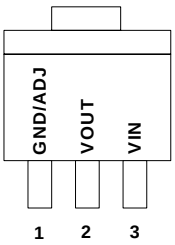
- Three Terminal Adjustable or Fixed Voltages: 1.8V, 2.5V, 3.3V, and adjustable.
- Output Current of 1A
- Low dropout voltage: 1.1V Typ.
- Line Regulation: 0.3% Max
- Load Regulation: 2% Max
- Fast Transient Response
- Thermal Protection
- SOT-223 package available
- Rugged 3KV ESD withstand capability.

APPLICATIONS

- High Efficiency Linear Regulators
- Post Regulators for Switching Supplies
- Battery Chargers
- Active SCSI Terminators
- Post Regulator for Switching DC/DC Converter
- Battery Powered Instrumentation

PIN CONFIGURATION

SOT-223
Top View



ORDERING INFORMATION

Package Type		Operating Temperature Range (T_A)	Output Voltage
SOT-223			
CM1117FDCM223		0℃ ~ +70℃	1.8V
CM1117FKCM223		0℃ ~ +70℃	2.5V
CM1117FSCM223		0℃ ~ +70℃	3.3V
CM1117FCM223		0℃ ~ +70℃	ADJ.
CM1117FGDCM223*		0℃ ~ +70℃	1.8V
CM1117FGKCM223*		0℃ ~ +70℃	2.5V
CM1117FGSCM223*		0℃ ~ +70℃	3.3V
CM1117FGCM223*		0℃ ~ +70℃	ADJ.

***Note:** G : Suffix for Pb Free Product

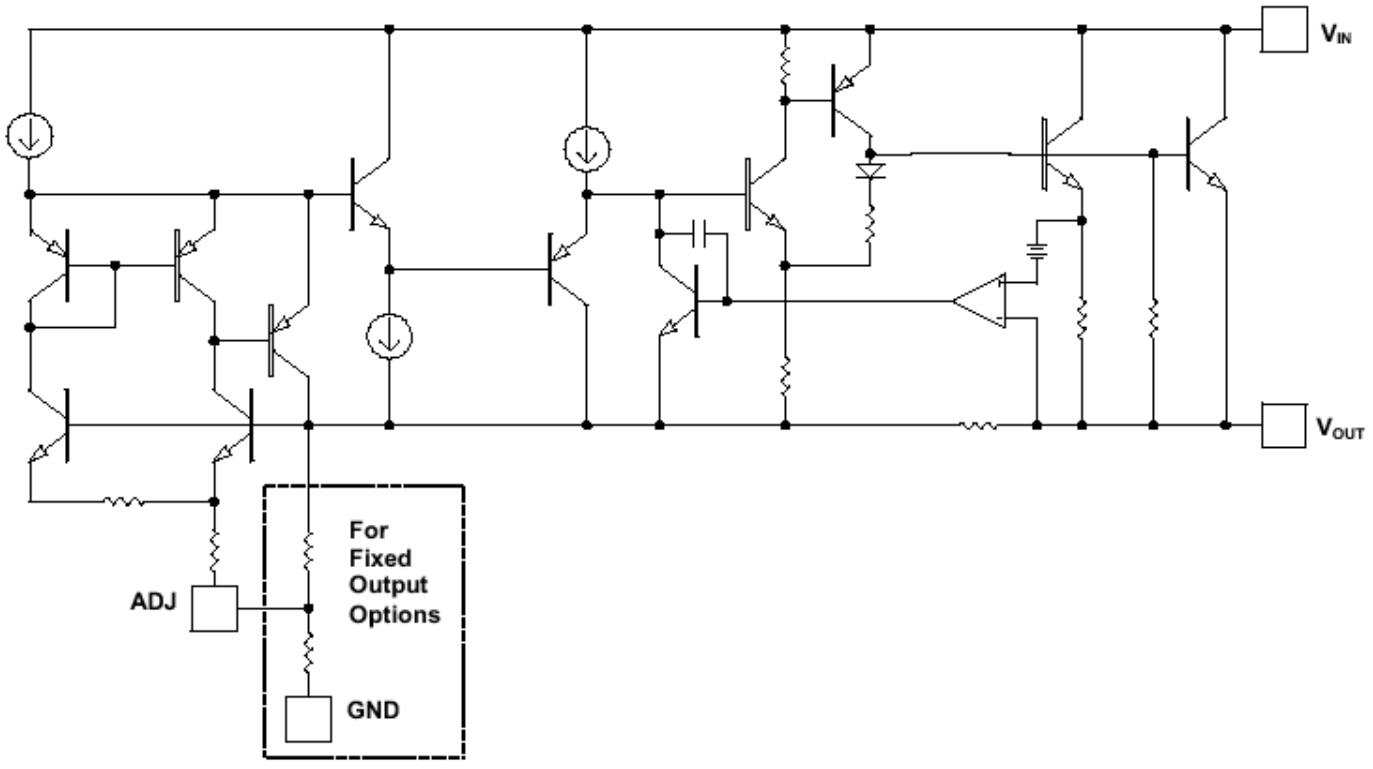
ABSOLUTE MAXIMUM RATINGS

Input Voltage ..... +12V
Operating Junction Temperature Range, T_J ..... 0℃ to +125℃
Storage Temperature ..... -40℃ to +150℃
Lead Temperature (10 sec.) ..... 260℃

THERMAL INFORMATION

Parameter		Maximum	Unit
Thermal Resistance (Θ_{ja})	SOT-223	60	℃/W
Internal Power Dissipation (P_D) (ΔT = 100℃, No Heatsink)	SOT-223	900	mW
Maximum Junction Temperature		125	℃
Maximum Lead Temperature (10 Sec)		260	℃

BLOCK DIAGRAM



ELECTRICAL CHARACTERISTICS

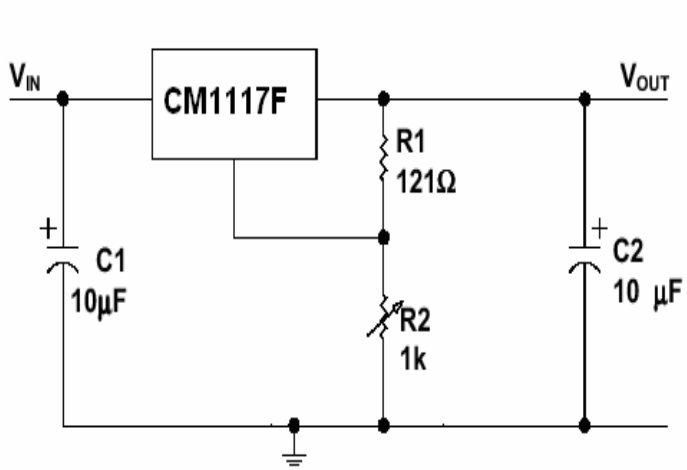
Electrical Characteristics at I_{OUT} = 10mA, and T_J = +25℃; unless otherwise noted

Parameter	Device	Test Conditions	CM1117F			Unit
			Min.	Typ.	Max.	
Reference Voltage	CM1117F	I_{OUT} = 10mA, V_{IN}= 5V	1.225	1.250	1.275	V
Output Voltage	CM1117FD	10mA <= I_{OUT} <= 1A, 3.2V<= V_{IN} <= 7V	1.764	1.800	1.836	V
	CM1117FK	10mA <= I_{OUT} <= 1A, 4.25V<= V_{IN} <= 7V	2.450	2.500	2.550	V
	CM1117FS	10mA <= I_{OUT} <= 1A, 4.75V<= V_{IN} <= 7V	3.234	3.300	3.366	V
Line Regulator	Adj. Output	I_{OUT} = 10mA, 1.5V<= V_{IN} <= 7V		0.05	0.30	%
	Fixed Output	I_{OUT} = 10mA, V_{OUT} + 1.5V<= V_{IN} <= 7V		1	6.0	mV
Load Regulation	Adj. Output	10mA <= I_{OUT} <= 1A, V_{IN} – V_{OUT} = 3V		0.4	2	%
	Fixed Output	10mA <= I_{OUT} <= 1A, V_{IN} = V_{OUT} + 1.5V		8	36	mV
Dropout Voltage (V_{IN} – V_{OUT})	Fixed Output	I_{OUT} = 10mA I_{OUT} = 1A		1 1.1	 1.25	V
Current Limit	Fixed Output	(V_{IN} – V_{OUT}) = 3V		1.8		A
Minimum Load Current (Note 1)		V_{IN} <= 7V	4			mA
Quiescent Current	Fixed Output	V_{IN} <= 7V		10	13	mA
Ripple Rejection (Note 2)		f_O = 120Hz, 1V_{RMS}, , I_{OUT} = 1A, (V_{IN} – V_{OUT})= 3V		35		dB
Thermal Regulation (Note 2)		T_A = 25℃, 30ms pulse		0.01	0.1	%W
Adjust Pin Current		I_{OUT} = 10mA, V_{IN} – V_{OUT} = 2V		48	120	μA

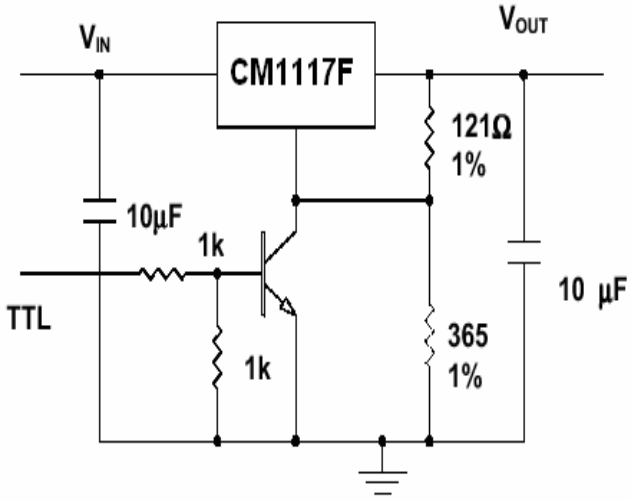
Note 1: For the adjustable device, the minimum load current is the minimum current required to maintain regulation. Normally the current in the resistor divider used to set the output voltage is selected to meet the minimum load current requirement.

Note 2: These parameters, although guaranteed, are not tested in production.

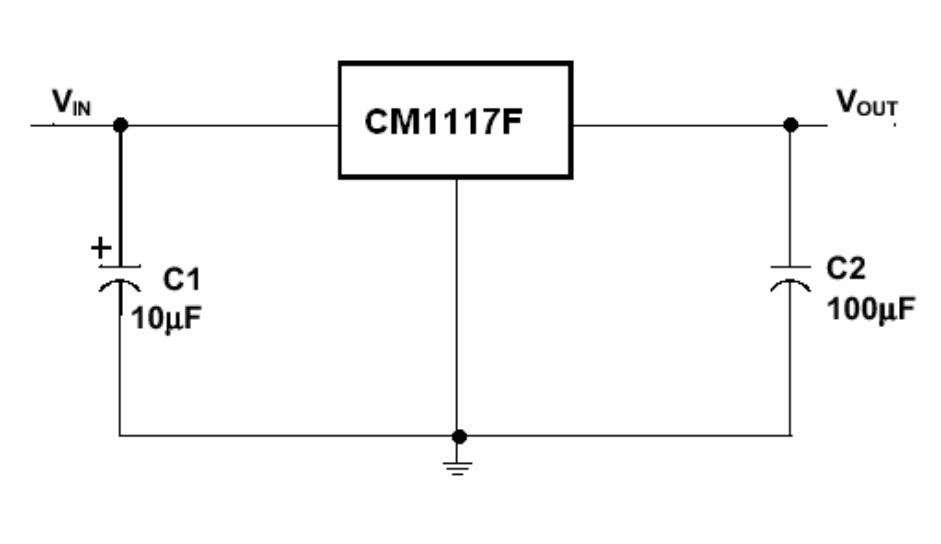
APPLICATION CIRCUIT



Adjustable Regulator
VOUT = 1.25V (1+R2/R1)



5V Regulator with Shutdown



Fixed Voltage Regulator

Application Hints

Like any linear voltage regulator, CM1117F requires external capacitors to ensure stability. The external capacitors must be carefully selected to ensure performance.

Input Capacitor

An input capacitor of at least 10μF is required. Ceramic or Tantalum can be used. The value can be increase without upper limit.

Output Capacitor

An output capacitor is required for stability. It must be placed no more than 1 cm away from the V_{OUT} pin, and connected directly between V_{OUT} and GND pins. The minimum value is 22μF but may be increase without limit.

Thermal Considerations

It is important that the thermal limit of the package is not exceeded. The CM1117F has built-in thermal protection. When the thermal limit is exceeded, the IC will enter protection, and V_{OUT} will be pulled to ground. The power dissipation for a given application can be calculated as following:

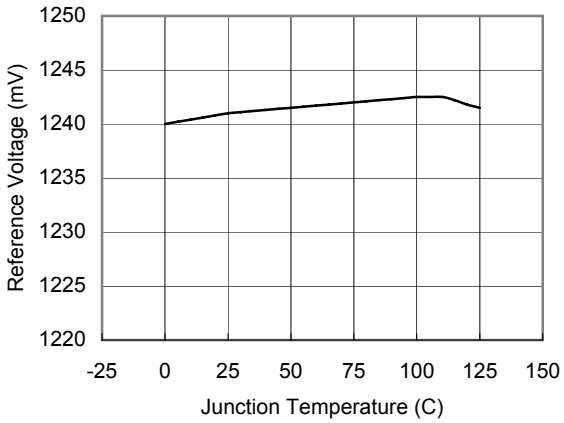
The power dissipation (P_D) is

$P_D = I_{OUT} * [V_{IN} – V_{OUT}]$

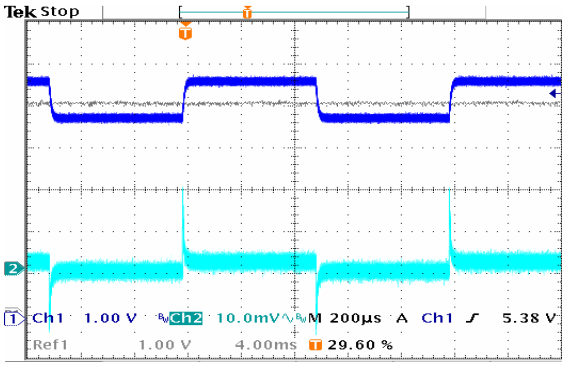
The thermal limit of the package is then limited to $P_{D(MAX)} = [T_J – T_A]/\Theta_{JA}$ where T_J is the junction temperature, TA is the ambient temperature, and Θ_{JA} is around 60°C/W for CM1117F. CM1117F is designed to enter thermal protection at 175°C. For example, if T_A is 25°C then the maximum P_D is limited to about 2.5W. In other words, if $I_{OUT(MAX)}$ = 1A, then [V_{IN} – V_{OUT}] cannot exceed 2.5V.

Typical Performance Characteristics

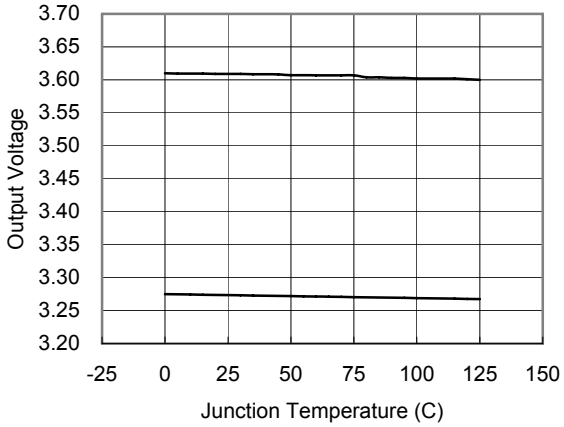
Reference Voltage vs Junction Temperature



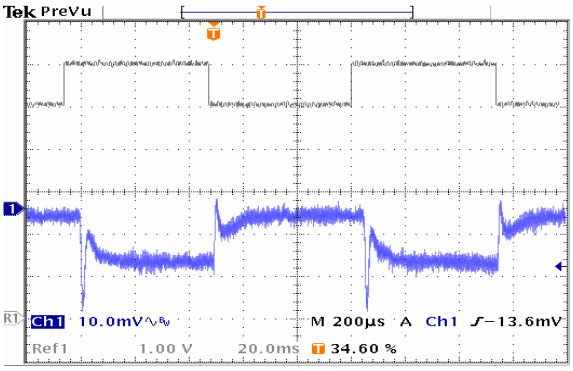
Line Transients



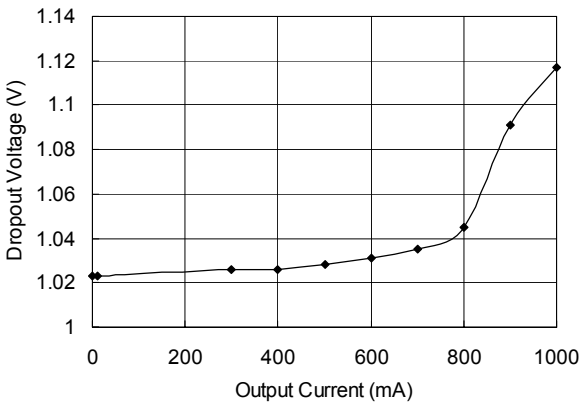
Output Voltage vs Junction Temperature



Load Transients

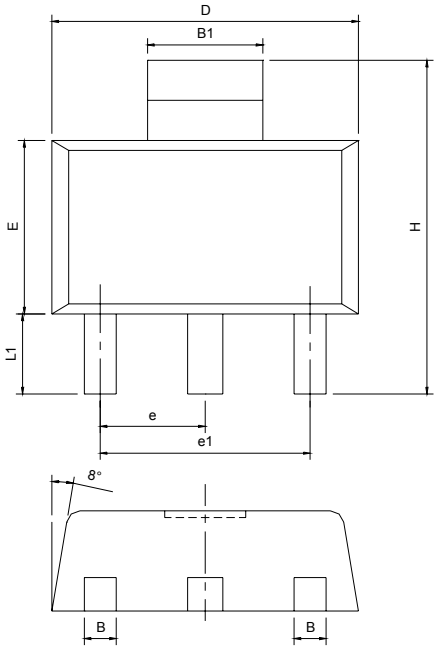


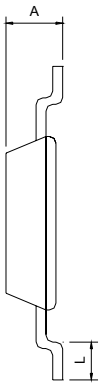
Dropout Voltage vs Output Current



PACKAGE DIMENSION

SOT-223 (M223)





SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.50	---	1.80	0.0591	---	0.0709
B	0.60	---	0.838	0.0236	---	0.033
B1	2.995	---	3.15	0.114	---	0.124
D	6.299	---	6.706	0.248	---	0.264
E	3.30	---	3.708	0.1299	---	0.146
e	---	2.30BSC	---	---	0.090BSC	---
e1	---	4.60BSC	---	---	0.181BSC	---
H	6.70	---	7.30	0.2638	---	0.2874
L	---	0.91MIN	---	---	0.036MIN	---
L1	---	2.00MAX	---	---	0.079MAX	---
θ	---	---	13°	---	---	13°

IMPORTANT NOTICE

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HsinChu Headquarter

5F, No. 11, Park Avenue II,
Science-Based Industrial Park,
HsinChu City, Taiwan
TEL: +886-3-567 9979
FAX: +886-3-567 9909

Sales & Marketing

11F, No. 306-3, SEC. 1, Ta Tung Road,
Hsichih, Taipei Hsien 221, Taiwan

TEL: +886-2-8692 1591
FAX: +886-2-8692 1596