

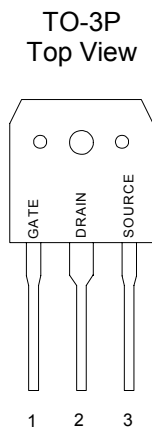
## GENERAL DESCRIPTION

This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

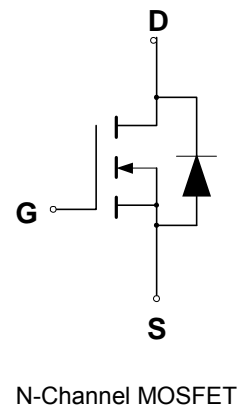
## FEATURES

- ◆ Robust High Voltage Termination
- ◆ Avalanche Energy Specified
- ◆ Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- ◆ Diode is Characterized for Use in Bridge Circuits
- ◆  $I_{DSS}$  and  $V_{DS(on)}$  Specified at Elevated Temperature
- ◆ Isolated Mounting Hole Reduces Mounting Hardware

## PIN CONFIGURATION



## SYMBOL



## ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain to Current – Continuous	$I_D$	20	A
– Pulsed	$I_{DM}$	60	
Gate-to-Source Voltage – Continue	$V_{GS}$	$\pm 20$	V
– Non-repetitive	$V_{GSM}$	$\pm 40$	V
Total Power Dissipation	$P_D$	250	W
Derate above 25°C		2.00	W/°C
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy – $T_J = 25^\circ\text{C}$ ( $V_{DD} = 100\text{V}, V_{GS} = 10\text{V}, I_L = 20\text{A}, L = 1.38\text{mH}, R_G = 25\Omega$ )	$E_{AS}$	276	mJ
Thermal Resistance – Junction to Case	$\theta_{JC}$	0.50	°C/W
– Junction to Ambient	$\theta_{JA}$	40	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	$T_L$	260	°C

(1) Pulse Width and frequency is limited by  $T_J(\text{max})$  and thermal response

### ORDERING INFORMATION

Part Number	Package
CMT20N50N3P	TO-3P

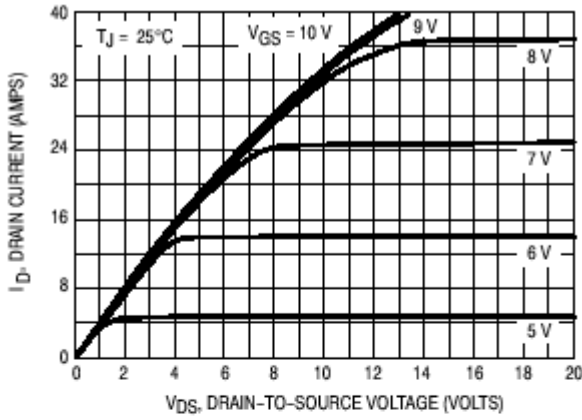
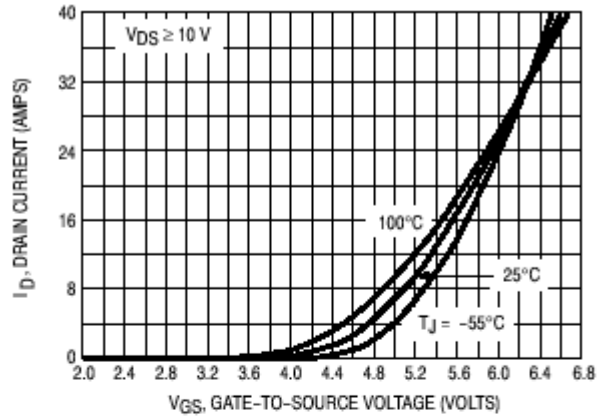
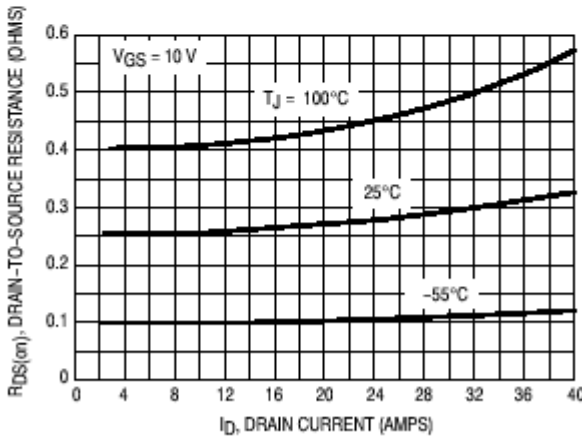
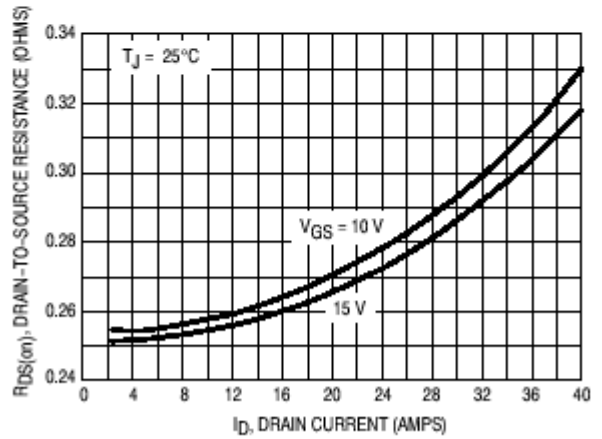
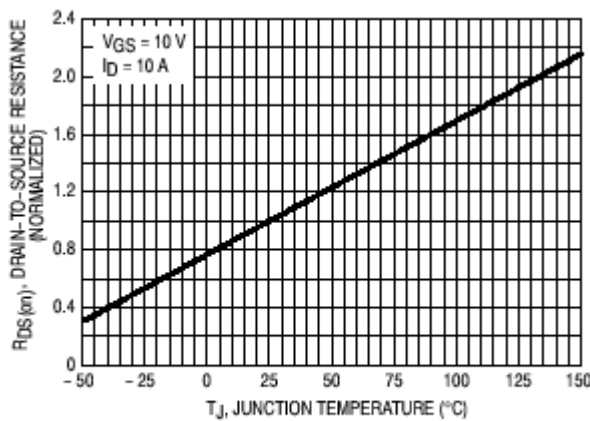
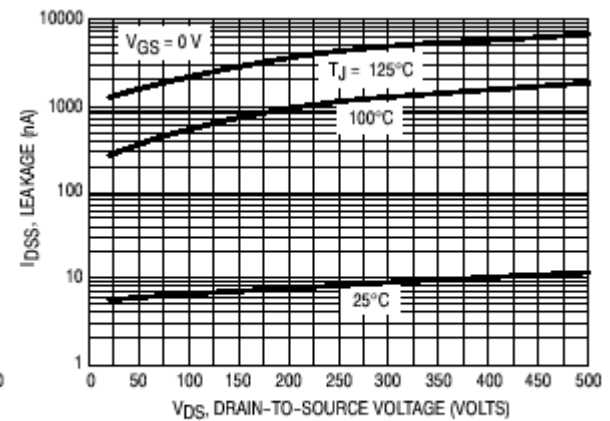
### ELECTRICAL CHARACTERISTICS

Unless otherwise specified,  $T_J = 25^\circ\text{C}$ .

Characteristic		Symbol	CMT20N50			Units
			Min	Typ	Max	
Drain-Source Breakdown Voltage ( $V_{GS} = 0\text{ V}$ , $I_D = 250\ \mu\text{A}$ )		$V_{(BR)DSS}$	500			V
Drain-Source Leakage Current ( $V_{DS} = 500\text{ V}$ , $V_{GS} = 0\text{ V}$ ) ( $V_{DS} = 500\text{ V}$ , $V_{GS} = 0\text{ V}$ , $T_J = 125^\circ\text{C}$ )		$I_{DSS}$			0.05 0.1	mA
Gate-Source Leakage Current-Forward ( $V_{gsf} = 20\text{ V}$ , $V_{DS} = 0\text{ V}$ )		$I_{GSSF}$			100	nA
Gate-Source Leakage Current-Reverse ( $V_{gsr} = 20\text{ V}$ , $V_{DS} = 0\text{ V}$ )		$I_{GSSR}$			100	nA
Gate Threshold Voltage ( $V_{DS} = V_{GS}$ , $I_D = 250\ \mu\text{A}$ )		$V_{GS(th)}$	2.0		4.0	V
Static Drain-Source On-Resistance ( $V_{GS} = 10\text{ V}$ , $I_D = 10\text{A}$ ) *		$R_{DS(on)}$			0.25	$\Omega$
Drain-Source On-Voltage ( $V_{GS} = 10\text{ V}$ ) ( $I_D = 20\text{ A}$ )		$V_{DS(on)}$		5.75	6.0	V
Forward Transconductance ( $V_{DS} = 50\text{ V}$ , $I_D = 10\text{A}$ ) *		$g_{FS}$	11			mhos
Input Capacitance	$(V_{DS} = 25\text{ V}$ , $V_{GS} = 0\text{ V}$ , $f = 1.0\text{ MHz}$ )	$C_{iss}$		3880	6950	pF
Output Capacitance		$C_{oss}$		452	920	pF
Reverse Transfer Capacitance		$C_{rss}$		96	140	pF
Turn-On Delay Time	$(V_{DD} = 250\text{ V}$ , $I_D = 20\text{ A}$ , $V_{GS} = 10\text{ V}$ , $R_G = 9.1\Omega$ ) *	$t_{d(on)}$		29	55	ns
Rise Time		$t_r$		90	165	ns
Turn-Off Delay Time		$t_{d(off)}$		97	190	ns
Fall Time		$t_f$		84	170	ns
Total Gate Charge	$(V_{DS} = 400\text{ V}$ , $I_D = 20\text{ A}$ , $V_{GS} = 10\text{ V}$ )*	$Q_g$		100	132	nC
Gate-Source Charge		$Q_{gs}$		20		nC
Gate-Drain Charge		$Q_{gd}$		44		nC
Internal Drain Inductance (Measured from the drain lead 0.25" from package to center of die)		$L_D$		5.0		nH
Internal Drain Inductance (Measured from the source lead 0.25" from package to source bond pad)		$L_S$		13		nH
<b>SOURCE-DRAIN DIODE CHARACTERISTICS</b>						
Forward On-Voltage(1)	$(I_S = 20\text{ A}$ , $d_i/d_t = 100\text{A}/\mu\text{s}$ )	$V_{SD}$			1.5	V
Forward Turn-On Time		$t_{on}$		**		ns
Reverse Recovery Time		$t_{rr}$		431		ns

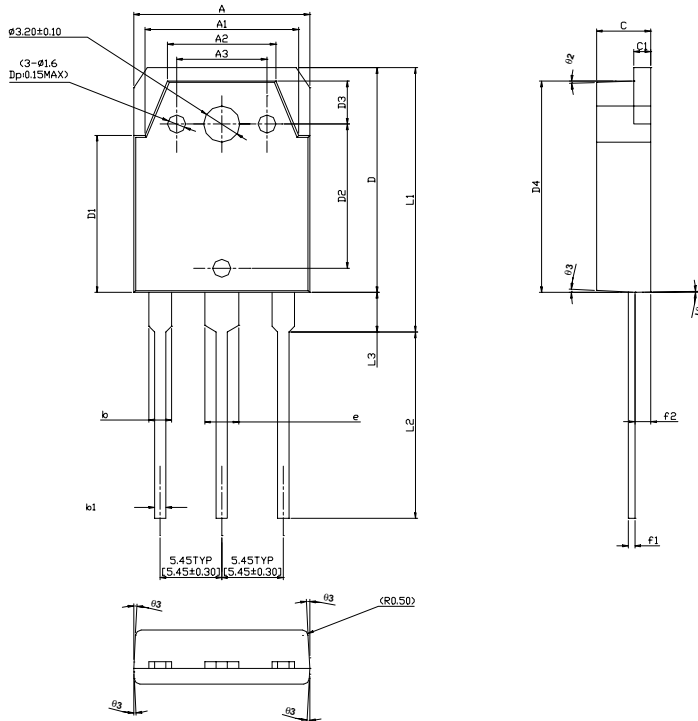
\* Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$

\*\* Negligible, Dominated by circuit inductance

**TYPICAL ELECTRICAL CHARACTERISTICS**

**Figure 1. On-Region Characteristics**

**Figure 2. Transfer Characteristics**

**Figure 3. On-Resistance versus Drain Current and Temperature**

**Figure 4. On-Resistance versus Drain Current and Gate Voltage**

**Figure 5. On-Resistance Variation with Temperature**

**Figure 6. Drain-to-Source Leakage Current versus Voltage**

### PACKAGE DIMENSION

TO-3P



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHS		
	MIN	NOM	MAX	MIN	NOM	MAX
A	15.40	---	15.80	0.606	---	0.622
A1	13.40	---	13.80	0.527	---	0.543
A2	9.40	---	9.80	0.370	---	0.386
A3	---	8.00	---	---	0.315	---
b	1.80	---	2.20	0.071	---	0.087
b1	0.80	---	1.20	0.031	---	0.047
C	4.60	---	5.00	0.181	---	0.197
C1	1.45	---	1.65	0.057	---	0.065
D	19.70	---	20.10	0.775	---	0.791
D1	13.70	---	14.10	0.539	---	0.555
D2	12.56	---	12.96	0.494	---	0.510
D3	3.60	---	4.00	0.142	---	0.157
D4	18.50	---	18.90	0.728	---	0.744
e	2.80	---	3.20	0.110	---	0.126
f1	0.55	---	0.75	0.021	---	0.029
f2	1.20	---	1.60	0.047	---	0.063
L1	23.20	---	23.60	0.913	---	0.929
L2	16.20	---	16.80	0.638	---	0.661
L3	3.30	---	3.70	0.130	---	0.146
θ1	---	1°	---	---	1°	---
θ2	---	2°	---	---	2°	---
θ3	---	3°	---	---	3°	---

## IMPORTANT NOTICE

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