October 2005

QFET ®

FQH90N10V2
100V N-Channel MOSFET

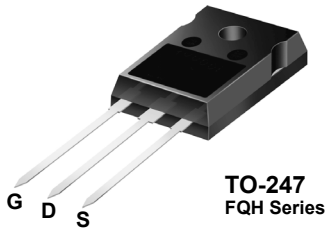
Features

- 105A, 100V, $R_{DS(on)}$ = 10mΩ @V_{GS} = 10 V
- Low gate charge (typical 147 nC)
- Low Crss (typical 300 pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- 175°C maximum junction temperature rating

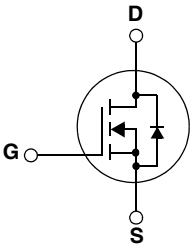
Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for DC to DC converters, sychronous rectification, and other applications lowest Rds(on) is required.



TO-247
FQH Series



Absolute Maximum Ratings

Symbol	Parameter		FQH90N10V2	Unit
V_{DSS}	Drain-Source Voltage		100	V
I_D	Drain Current	- Continuous (T_C = 25°C) - Continuous (T_C = 100°C)	105 78	A A
I_{DM}	Drain Current	- Pulsed (Note 1)	420	A
V_{GSS}	Gate-Source voltage		±30	V
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	2430	mJ
I_{AR}	Avalanche Current	(Note 1)	105	A
E_{AR}	Repetitive Avalanche Energy	(Note 1)	33	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5	V/ns
P_D	Power Dissipation	(T_C = 25°C) - Derate above 25°C	330 2.2	W W/°C
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +175	°C
T_L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds		300	°C

Thermal Characteristics

Symbol	Parameter	Min.	Max.	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	--	0.45	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink	0.24	--	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	--	40	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
HV290N10	FQH90N10V2	TO-247	-	-	30

Electrical Characteristics

T_C = 25°C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Typ.	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0V, I_D = 250μA	100	--	--	V
ΔBV_{DSS} / ΔT_J	Breakdown Voltage Temperature Coefficient	I_D = 250μA, Referenced to 25°C	--	0.1	--	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 100V, V_{GS} = 0V V_{DS} = 80V, T_C = 150°C	-- --	-- --	1 10	μA μA
I_{GSSF}	Gate-Body Leakage Current, Forward	V_{GS} = 30V, V_{DS} = 0V	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	V_{GS} = -30V, V_{DS} = 0V	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250μA	2.0	--	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	V_{GS} = 10V, I_D = 52.5A	--	8.5	10	mΩ
g_{FS}	Forward Transconductance	V_{DS} = 40V, I_D = 52.5A (Note 4)	--	72	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz	--	4730	6150	pF
C_{oss}	Output Capacitance		--	1180	1530	pF
C_{rss}	Reverse Transfer Capacitance		--	300	390	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 100V, I_D = 90A R_G = 25Ω	--	52	114	ns
t_r	Turn-On Rise Time		--	492	994	ns
$t_{d(off)}$	Turn-Off Delay Time		--	304	618	ns
t_f	Turn-Off Fall Time	(Note 4, 5)	--	355	720	ns
Q_g	Total Gate Charge	V_{DS} = 80V, I_D = 90A V_{GS} = 10V	--	147	191	nC
Q_{gs}	Gate-Source Charge		--	28	--	nC
Q_{gd}	Gate-Drain Charge	(Note 4, 5)	--	60	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	105	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	420	A
V_{SD}	Drain-Source Diode Forward Voltage	V_{GS} = 0V, I_S = 105A	--	--	1.4	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0V, I_S = 90A dI_F/dt =100A/μs (Note 4)	--	114	--	ns
Q_{rr}	Reverse Recovery Charge		--	0.54	--	μC

NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L = 0.22mH, I_{AS} = 105A, V_{DD} = 50V, R_G = 25Ω, Starting T_J = 25°C
3. I_{SD} ≤ 105A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
4. Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 2%
5. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

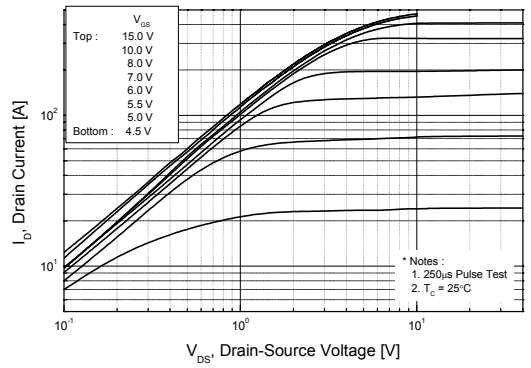


Figure 2. Transfer Characteristics

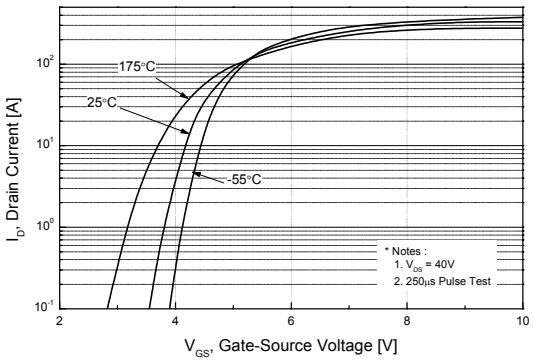


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

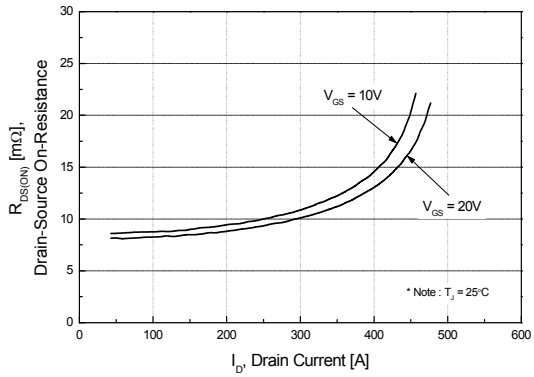


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue

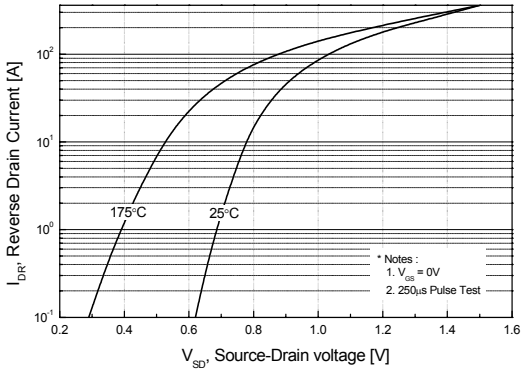


Figure 5. Capacitance Characteristics

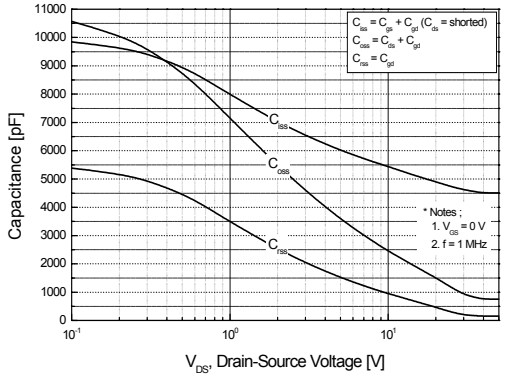
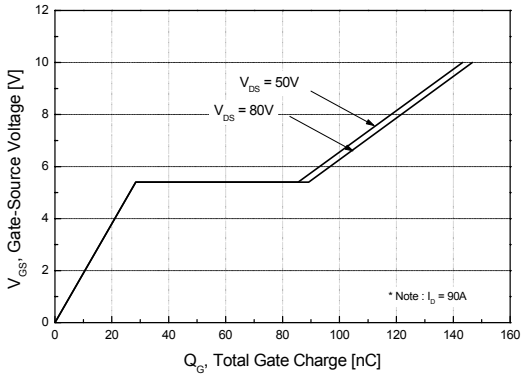


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

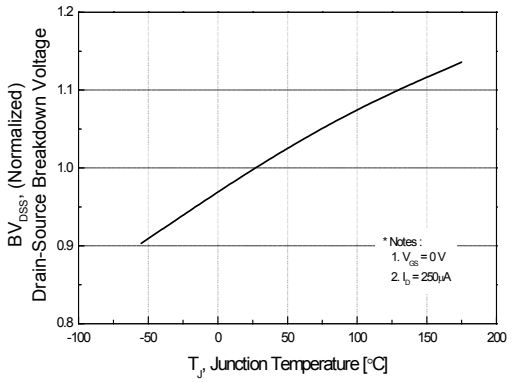


Figure 8. On-Resistance Variation vs. Temperature

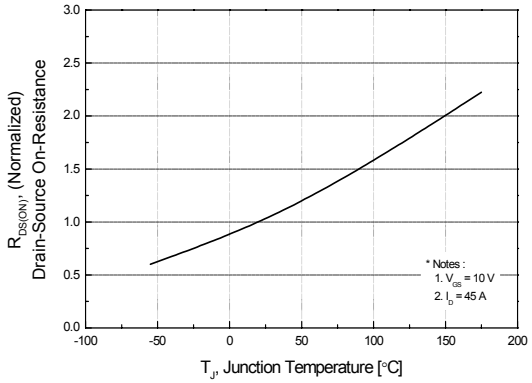


Figure 9. Maximum Safe Operating Area

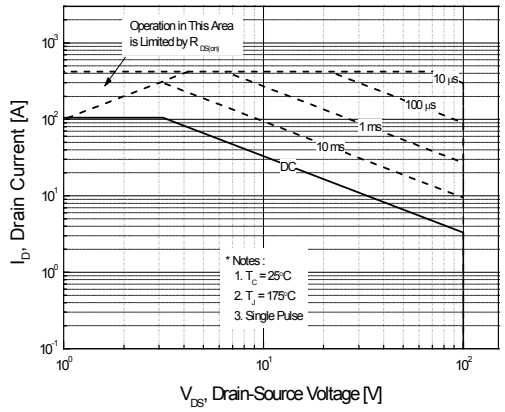


Figure 10. Maximum Drain Current vs. Case Temperature

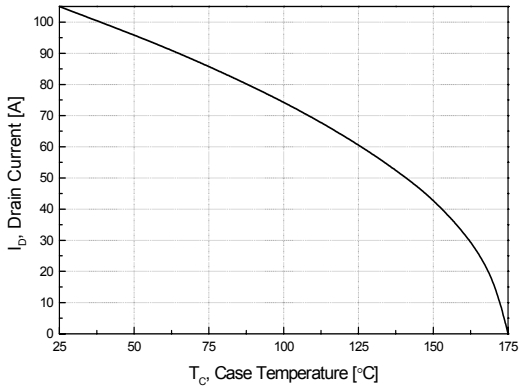
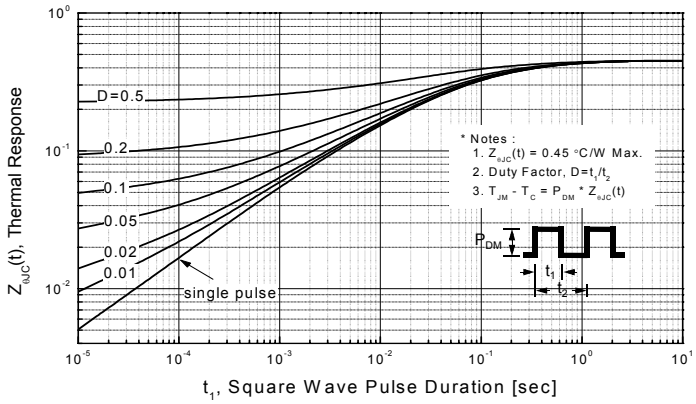
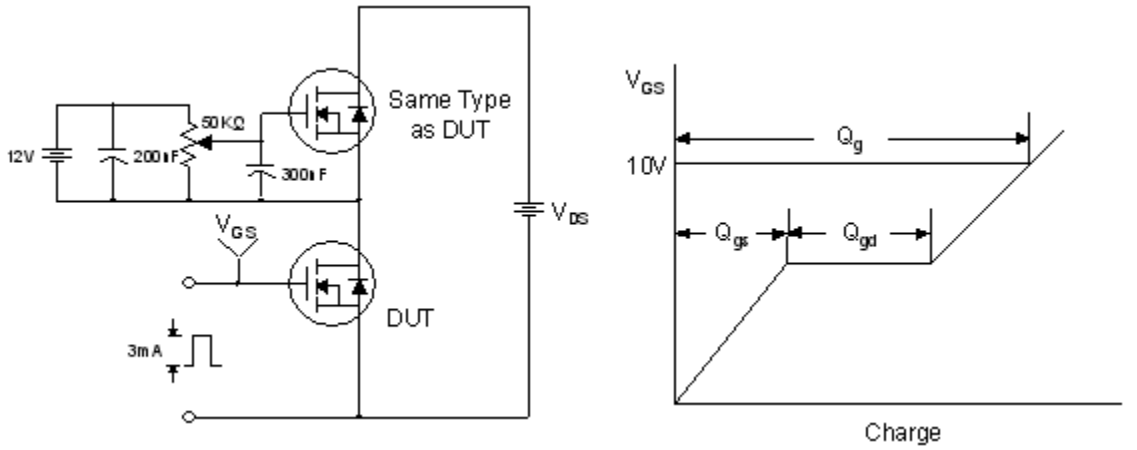


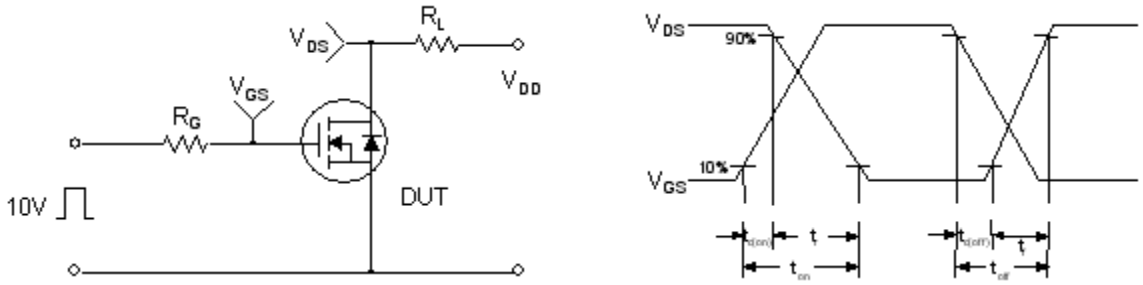
Figure 11. Transient Thermal Response Curve



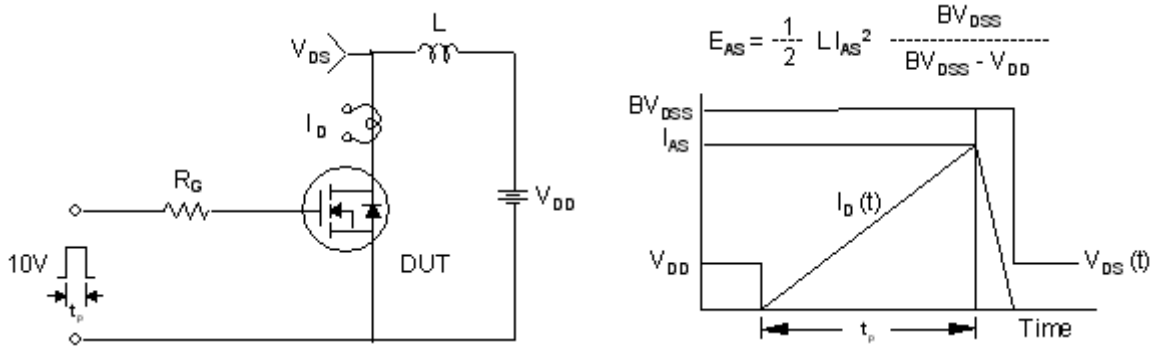
Gate Charge Test Circuit & Waveform



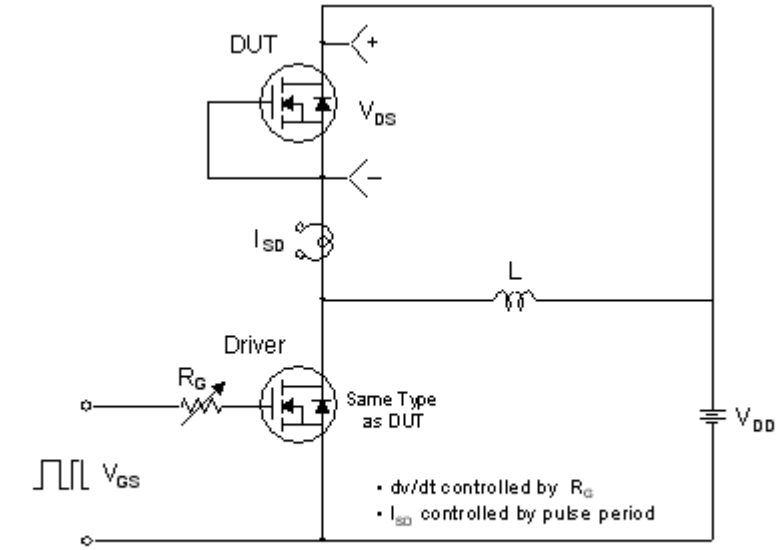
Resistive Switching Test Circuit & Waveforms

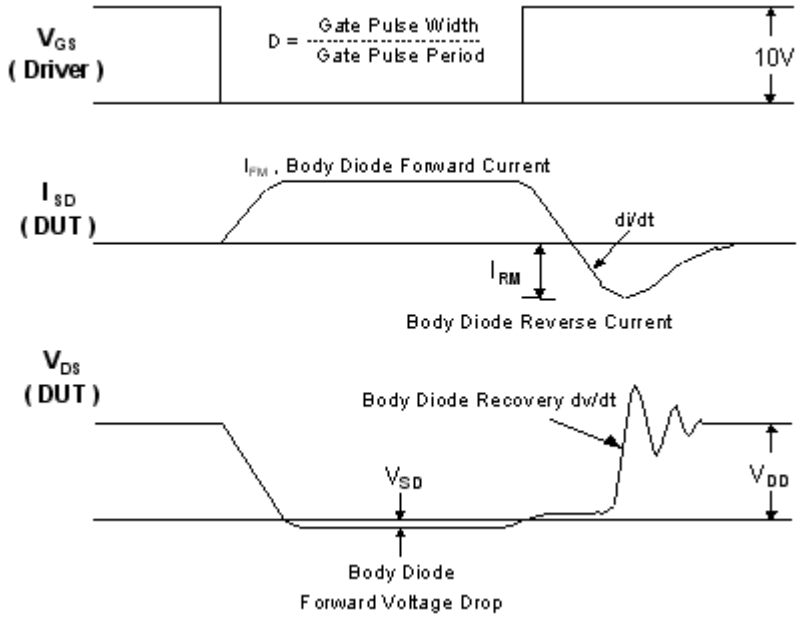


Unclamped Inductive Switching Test Circuit & Waveforms



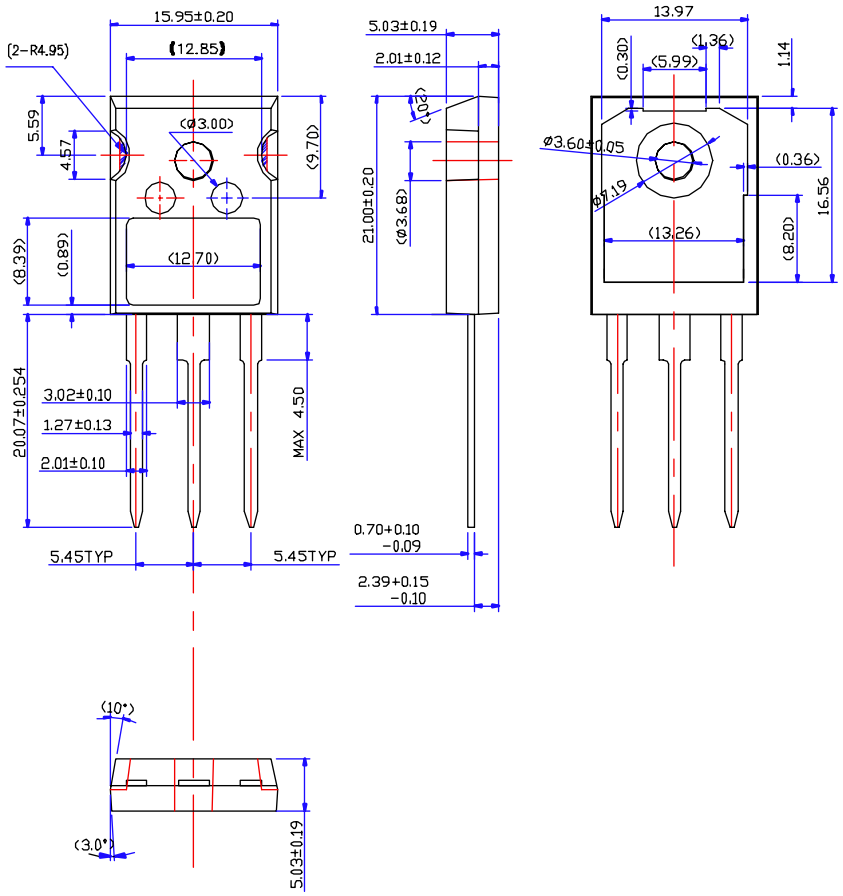
Peak Diode Recovery dv/dt Test Circuit & Waveforms





Mechanical Dimensions

TO-247AD (FKS PKG CODE 001)



Dimensions in Millimeters

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